

Test Report: cpt-short

1 Run Information

Test Name	cpt-short
Test Version	0.1
Test Time	Tuesday 15 th October, 2024 at 05:09
Operator	lusee-night @ lusee-01.inst.bnl.gov
Comments	None
Overall Result	PASSED

Options

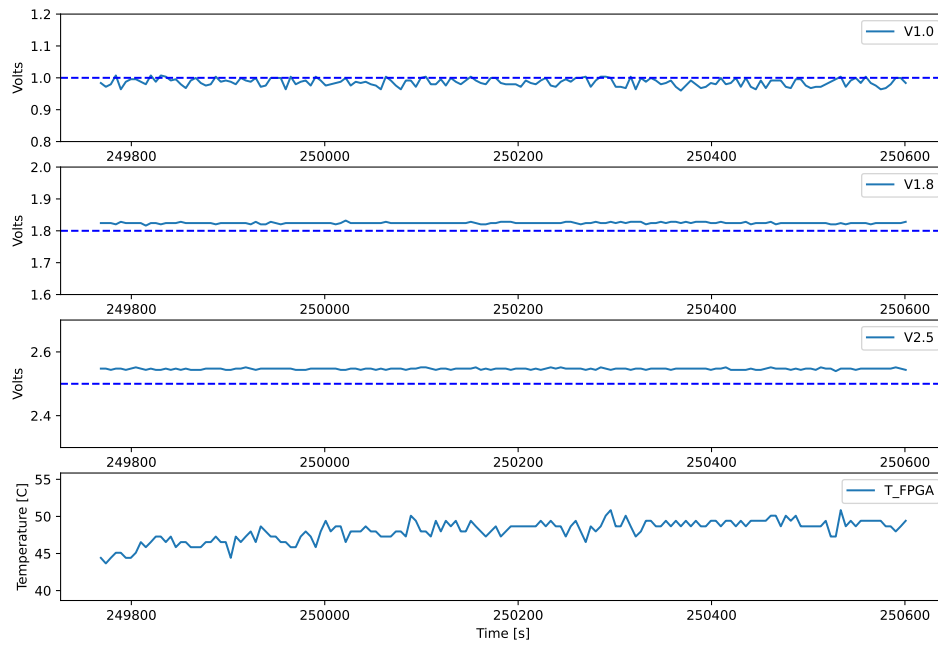
channels	3
gains	LMH
freqs	1 2 5 10 12 15 20 22 25 27 30 32 35 40 45 50 52 55
amplitudes	400 100 0
bitslices	23 20 16
amplitude_fact	5

2 Basic Info

Packets Received	1541
Startup packet received	✓
Software Version	00.01 r02
Firmware Version	04.06
Firmware ID	0x0534
Firmware Date	07/12/2024
Firmware Time	10:29.42
Waveforms present	648/648 ✓
Spectra present	162/162 ✓

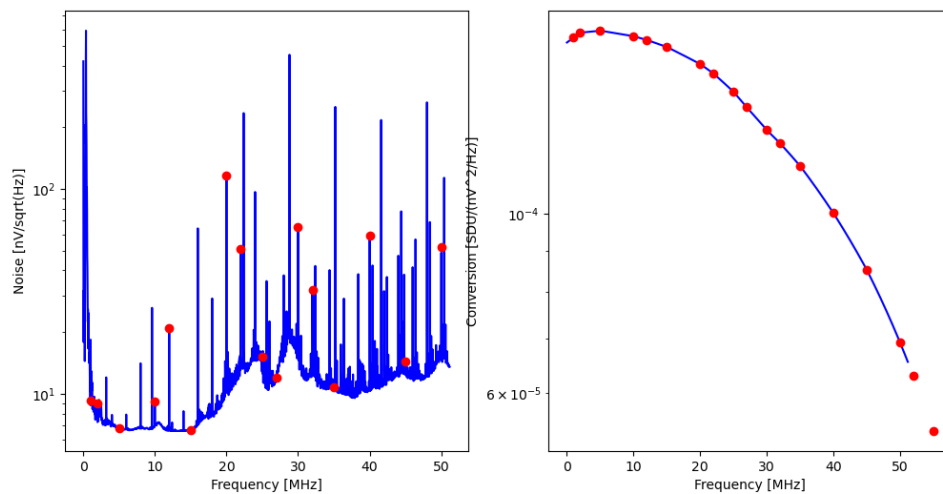
3 Results

Telemetry Plots

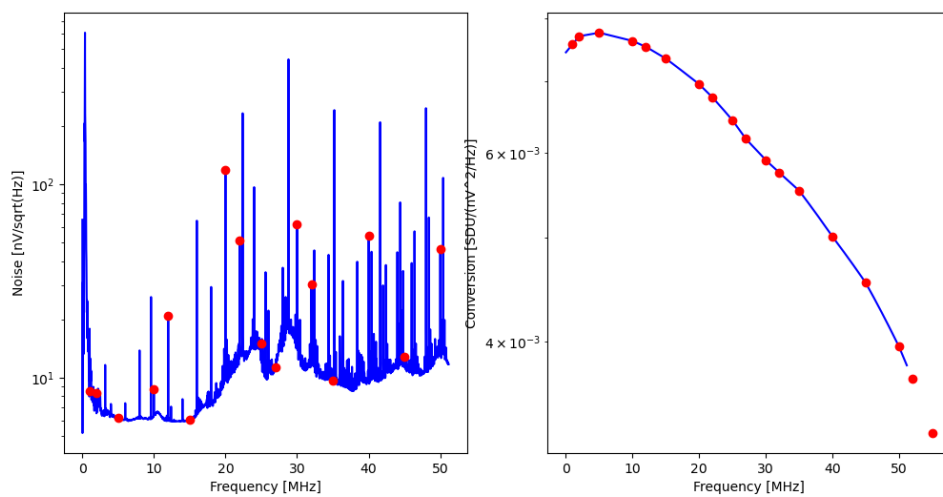


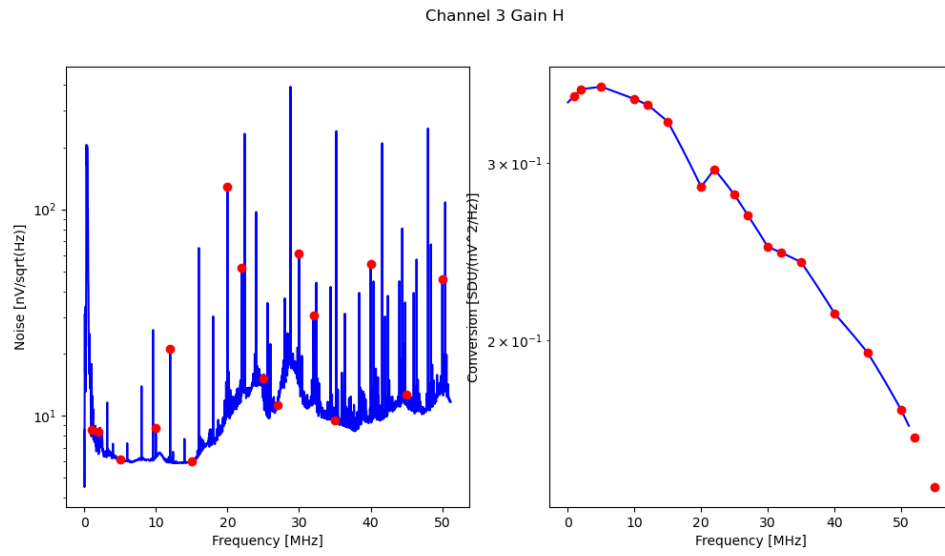
Power spectrum results

Channel 3 Gain L

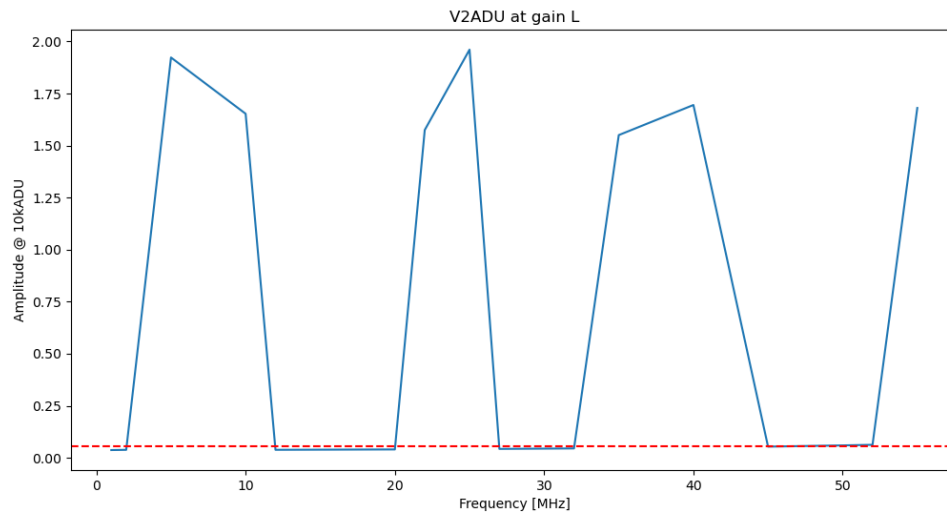


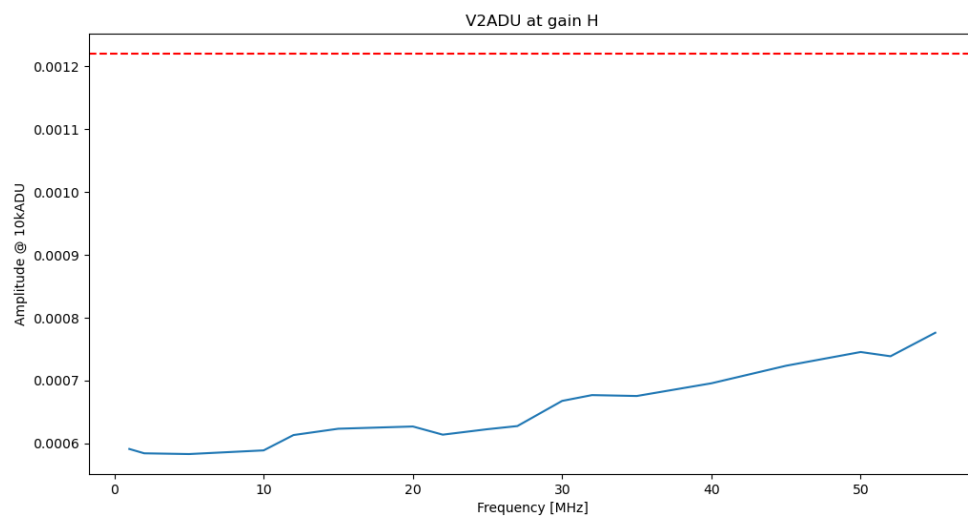
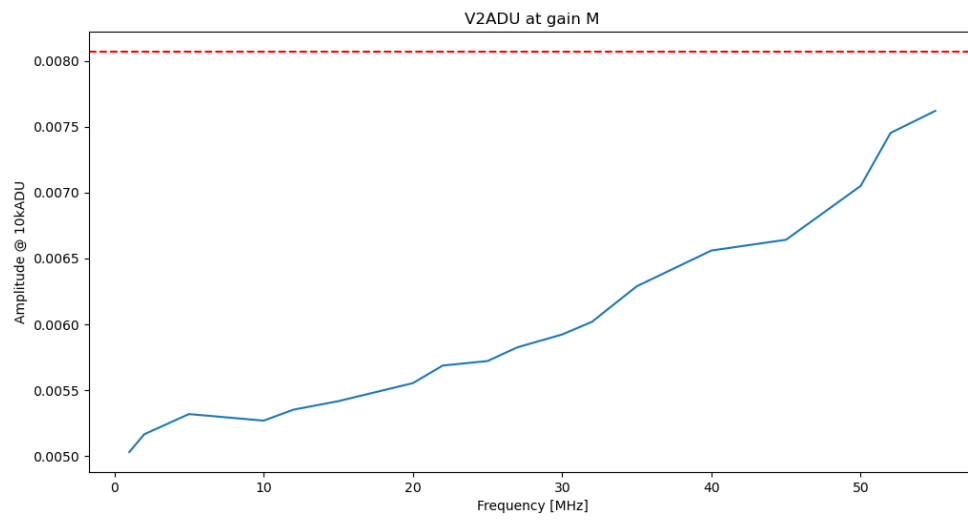
Channel 3 Gain M





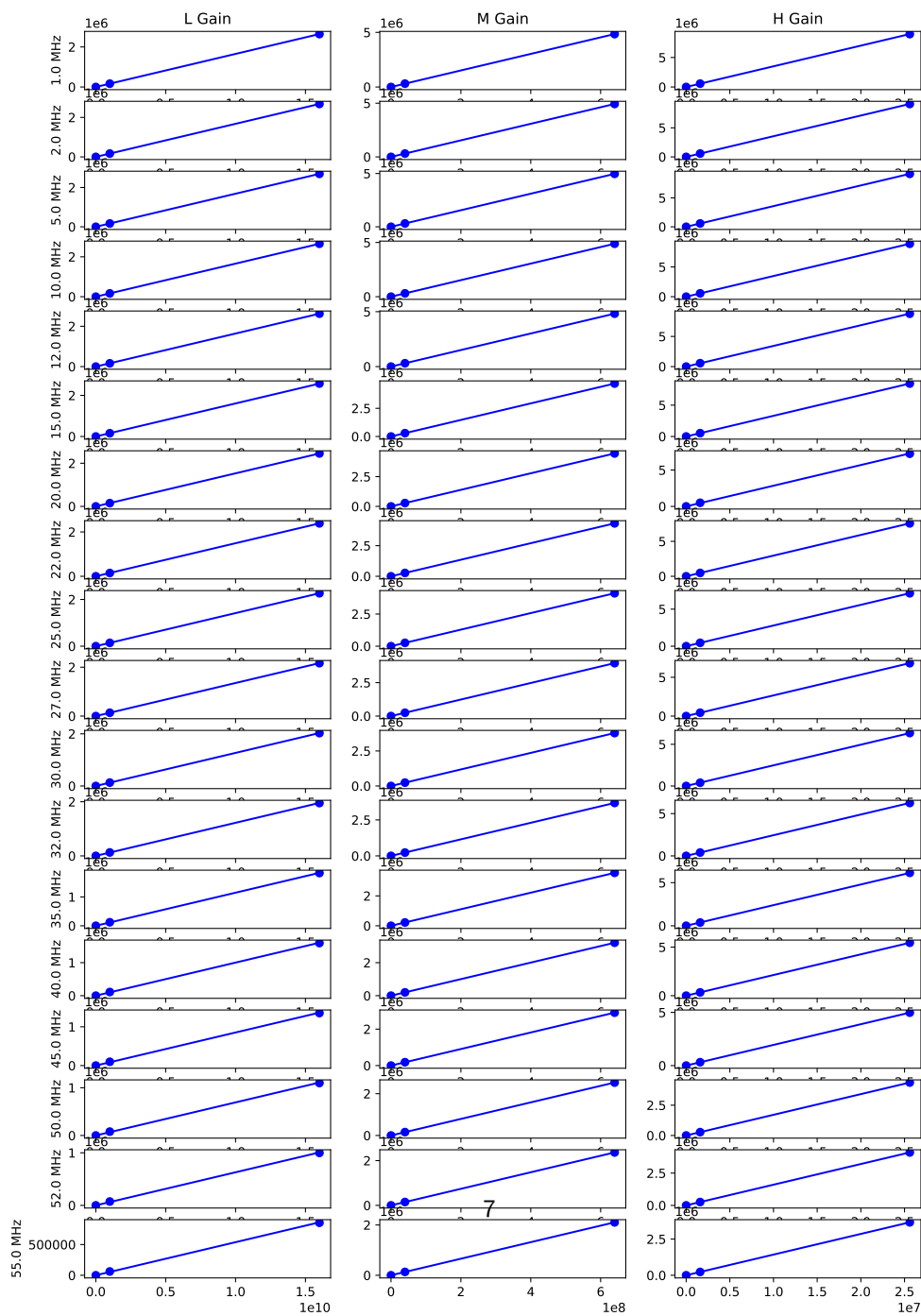
3.0.1 Waveform results





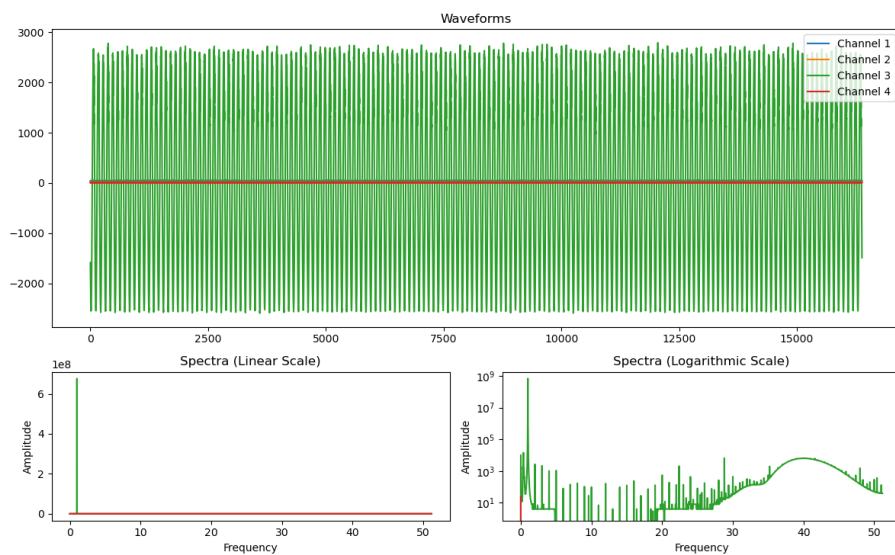
4 Plots

Power fits

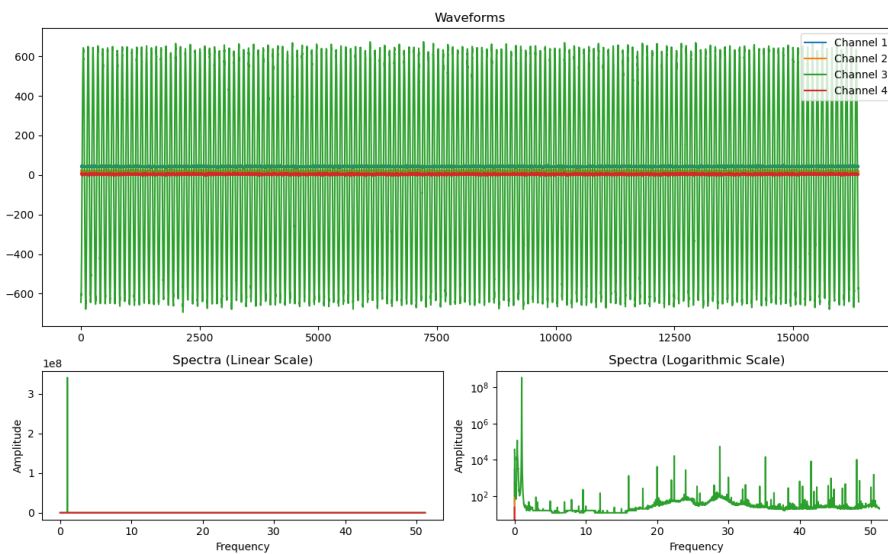


Raw Data

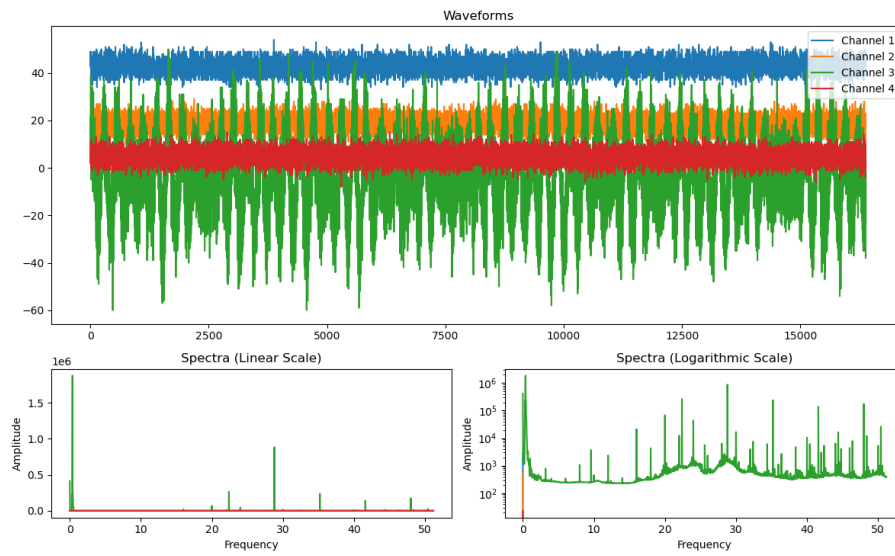
Gain L (1.0 1.0 1.0 1.0)MHz (0.0 0.0 2000.0 0.0)mVpp



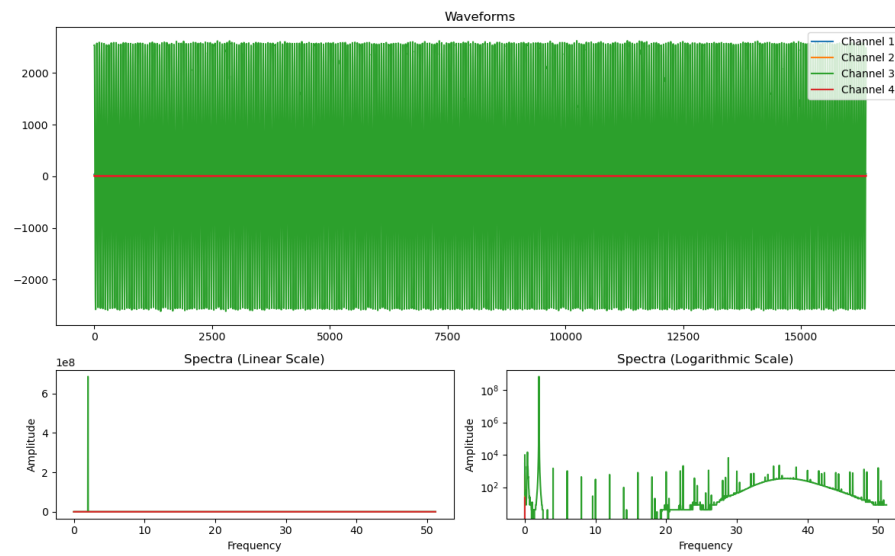
Gain L (1.0 1.0 1.0 1.0)MHz (0.0 0.0 500.0 0.0)mVpp



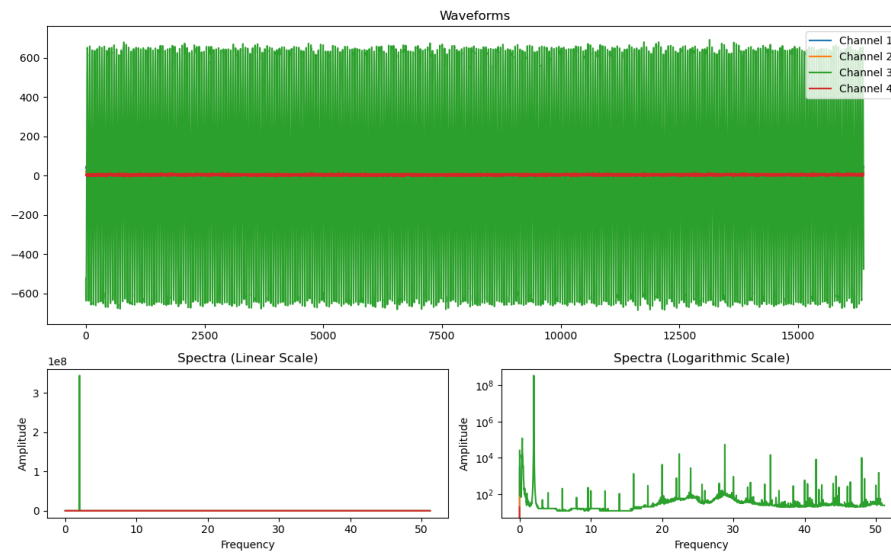
Gain L (1.0 1.0 1.0 1.0)MHz (0.0 0.0 0.0 0.0)mVpp



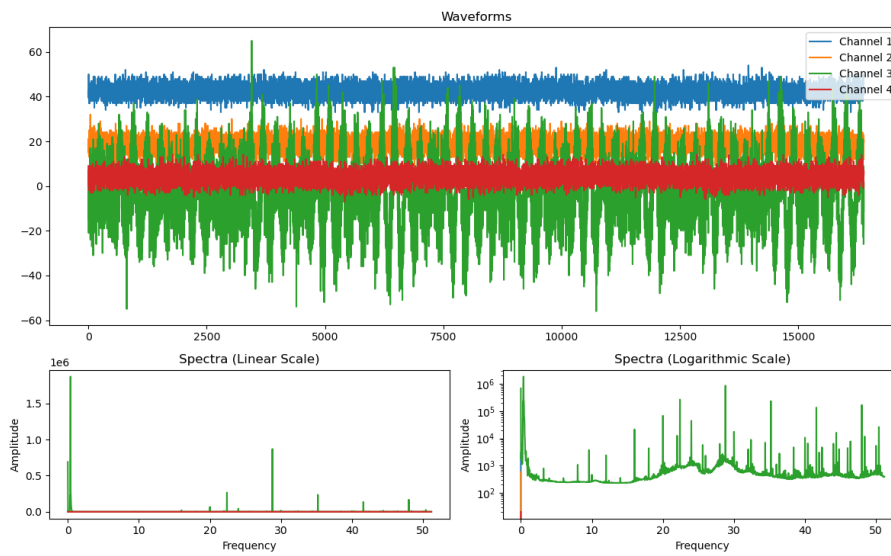
Gain L (2.0 2.0 2.0 2.0)MHz (0.0 0.0 2000.0 0.0)mVpp



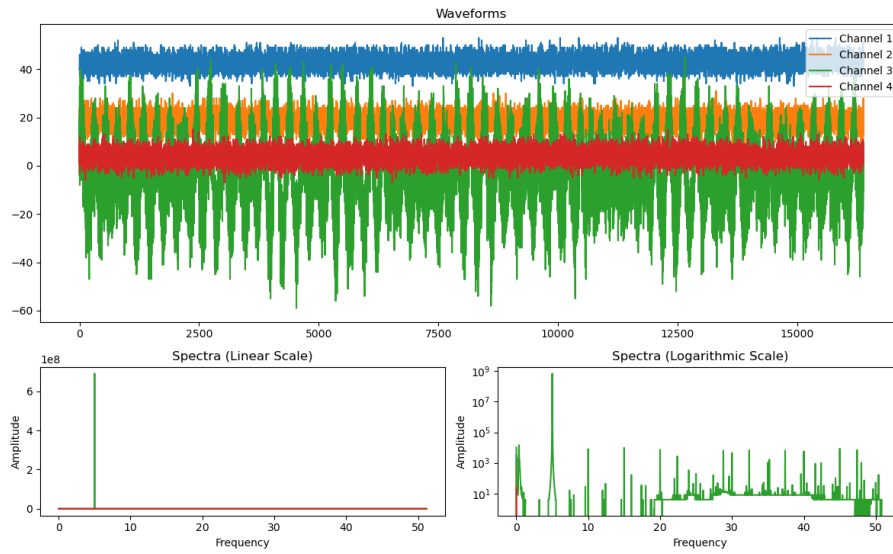
Gain L (2.0 2.0 2.0 2.0)MHz (0.0 0.0 500.0 0.0)mVpp



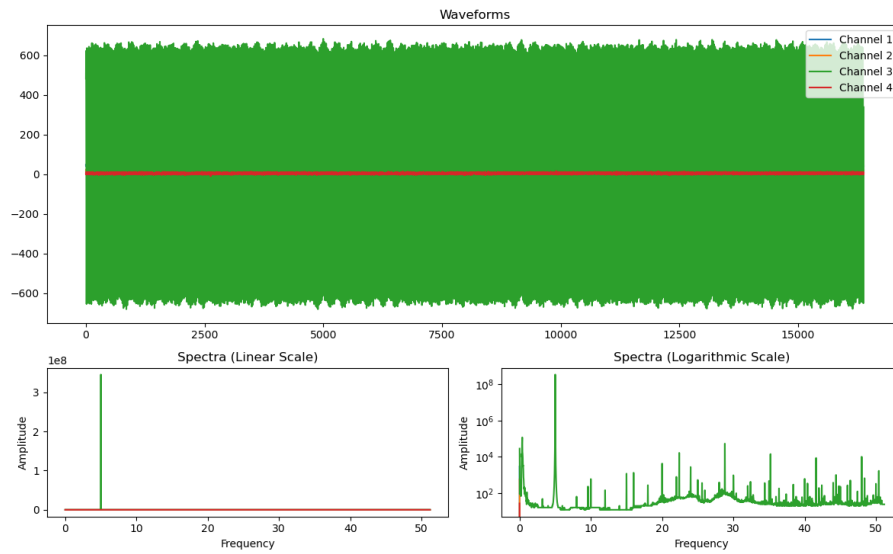
Gain L (2.0 2.0 2.0 2.0)MHz (0.0 0.0 0.0 0.0)mVpp



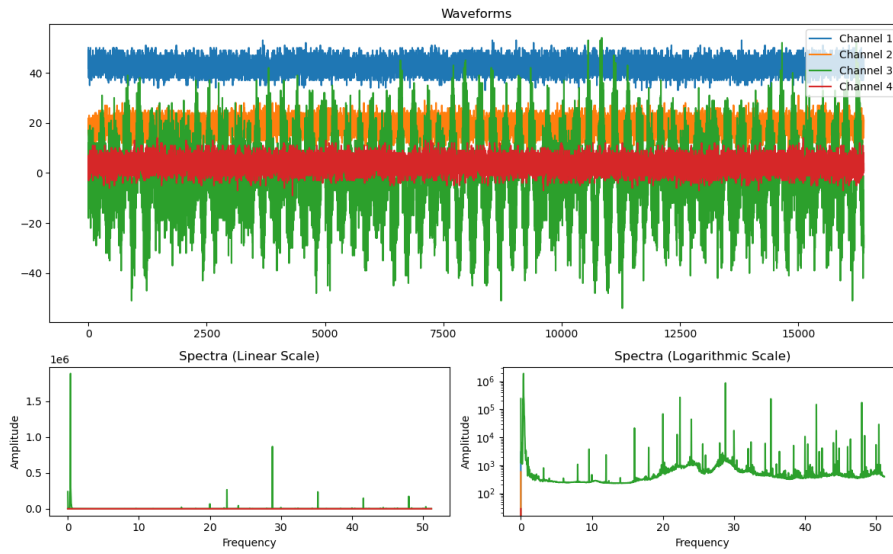
Gain L (5.0 5.0 5.0 5.0)MHz (0.0 0.0 2000.0 0.0)mVpp



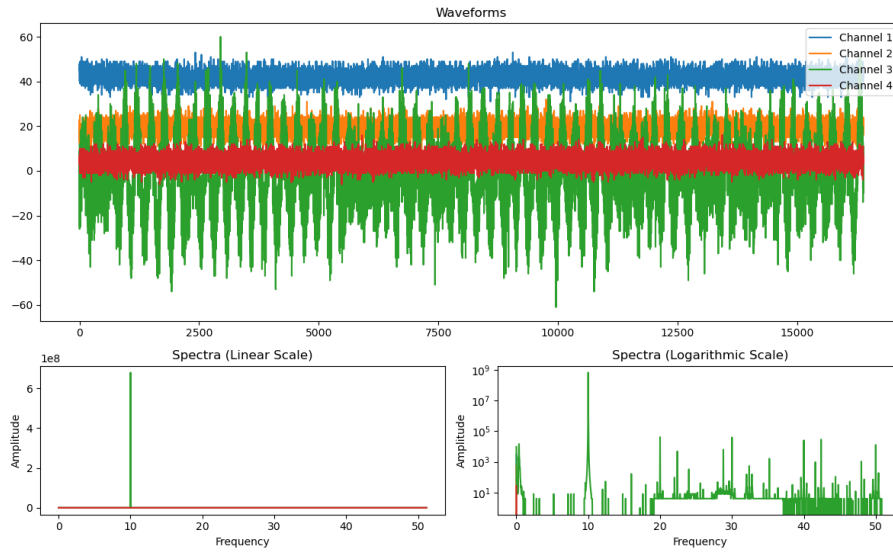
Gain L (5.0 5.0 5.0 5.0)MHz (0.0 0.0 500.0 0.0)mVpp



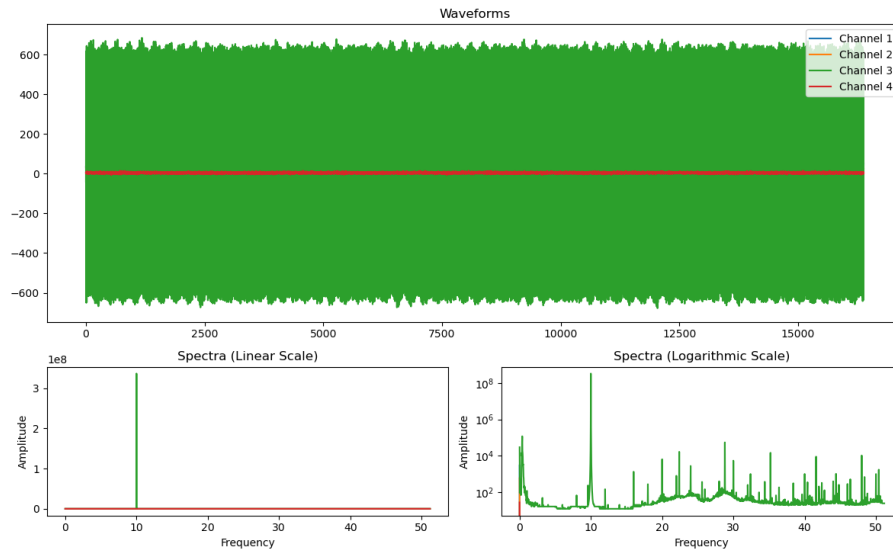
Gain L (5.0 5.0 5.0 5.0)MHz (0.0 0.0 0.0 0.0)mVpp



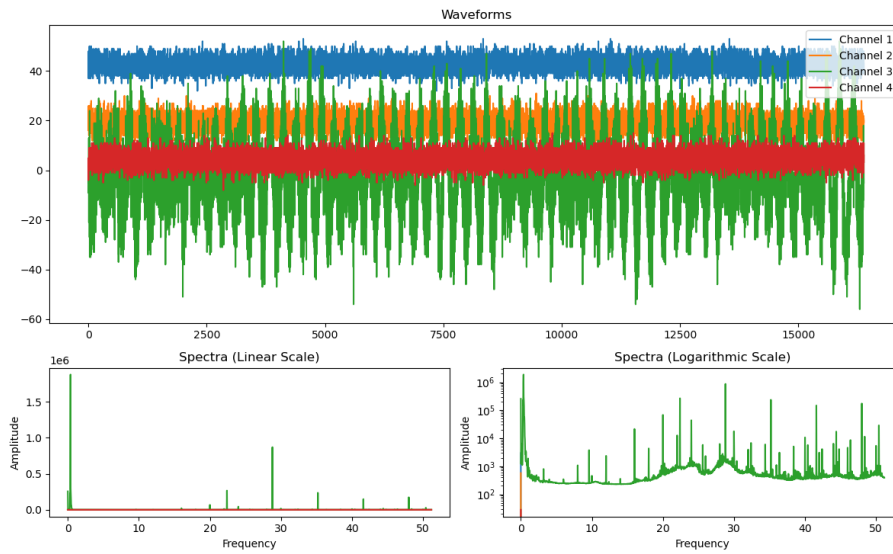
Gain L (10.0 10.0 10.0 10.0)MHz (0.0 0.0 2000.0 0.0)mVpp



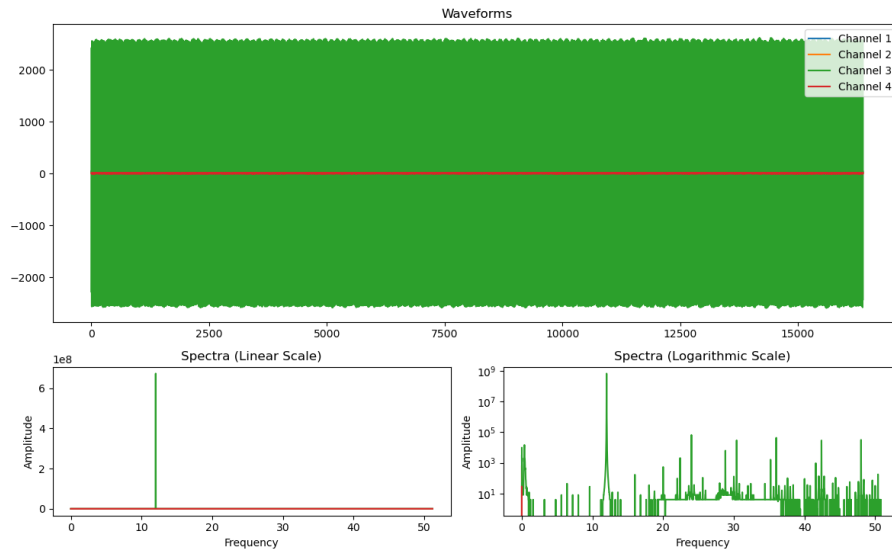
Gain L (10.0 10.0 10.0 10.0)MHz (0.0 0.0 500.0 0.0)mVpp



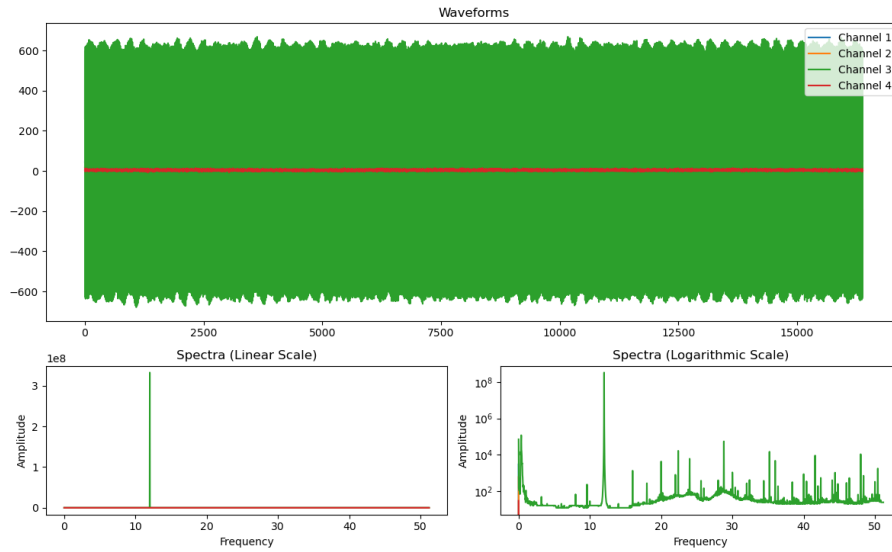
Gain L (10.0 10.0 10.0 10.0)MHz (0.0 0.0 0.0 0.0)mVpp



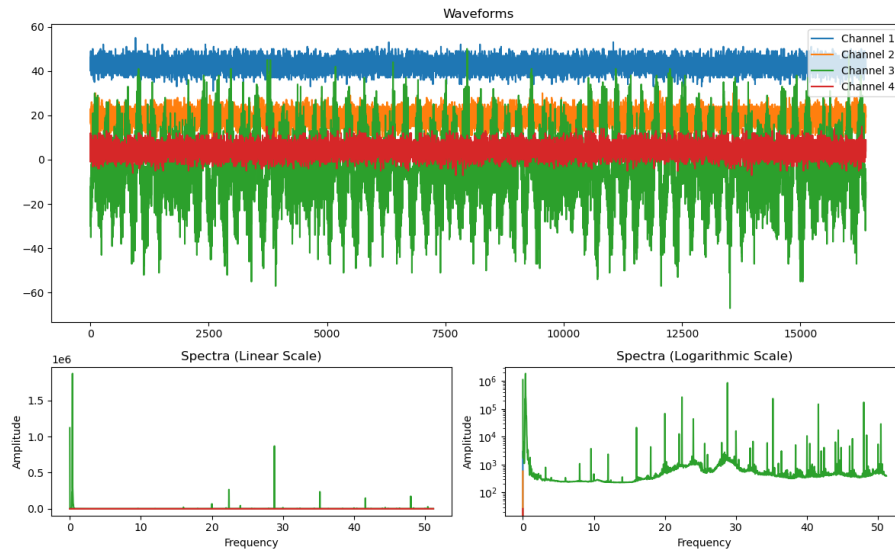
Gain L (12.0 12.0 12.0 12.0)MHz (0.0 0.0 2000.0 0.0)mVpp



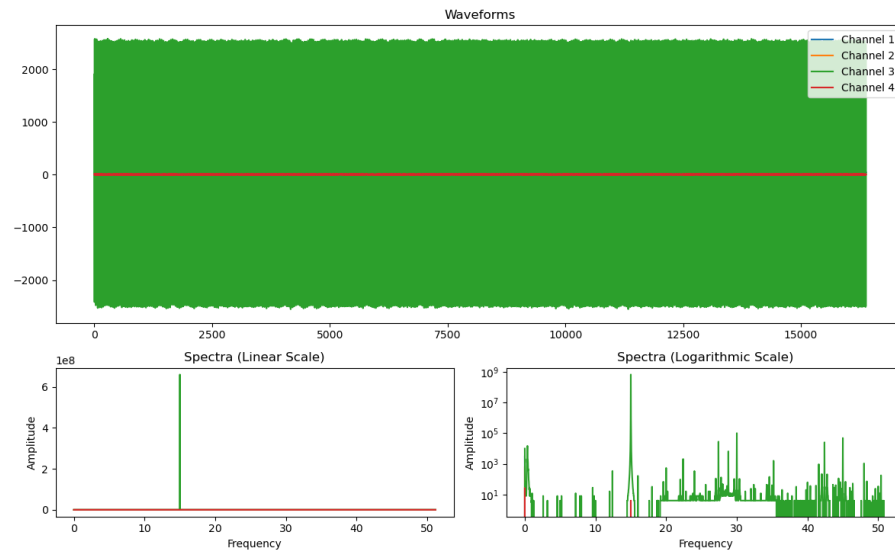
Gain L (12.0 12.0 12.0 12.0)MHz (0.0 0.0 500.0 0.0)mVpp



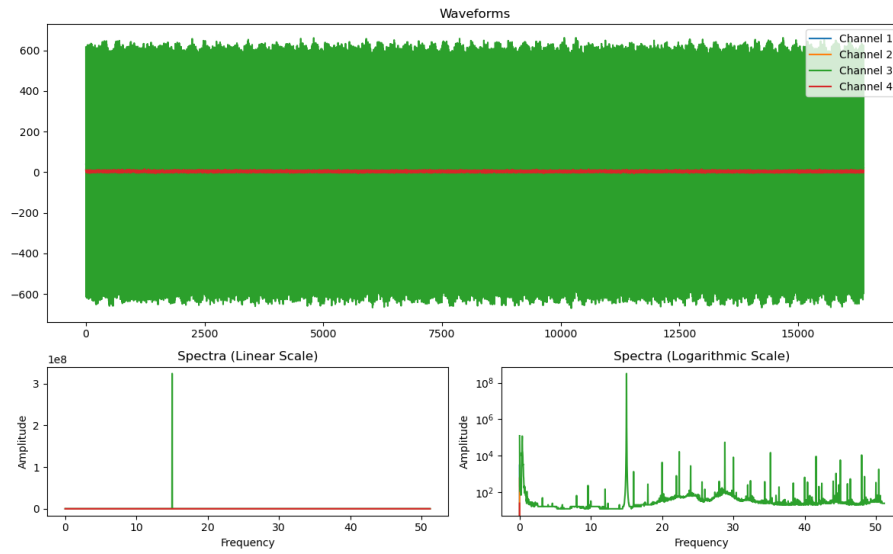
Gain L (12.0 12.0 12.0 12.0)MHz (0.0 0.0 0.0 0.0)mVpp



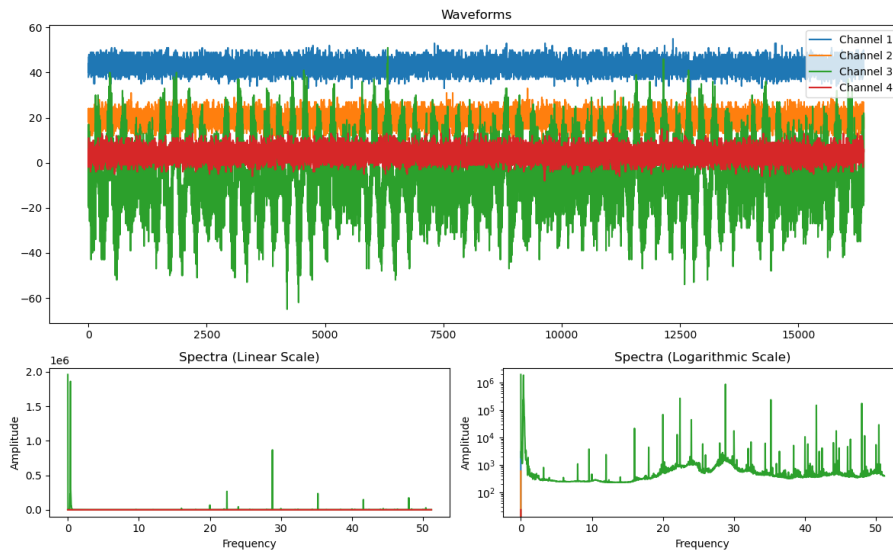
Gain L (15.0 15.0 15.0 15.0)MHz (0.0 0.0 2000.0 0.0)mVpp



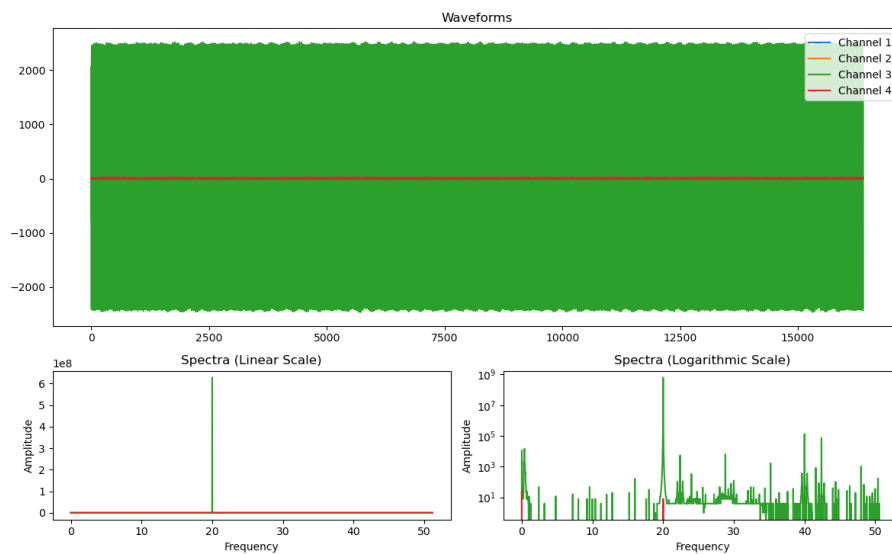
Gain L (15.0 15.0 15.0 15.0)MHz (0.0 0.0 500.0 0.0)mVpp



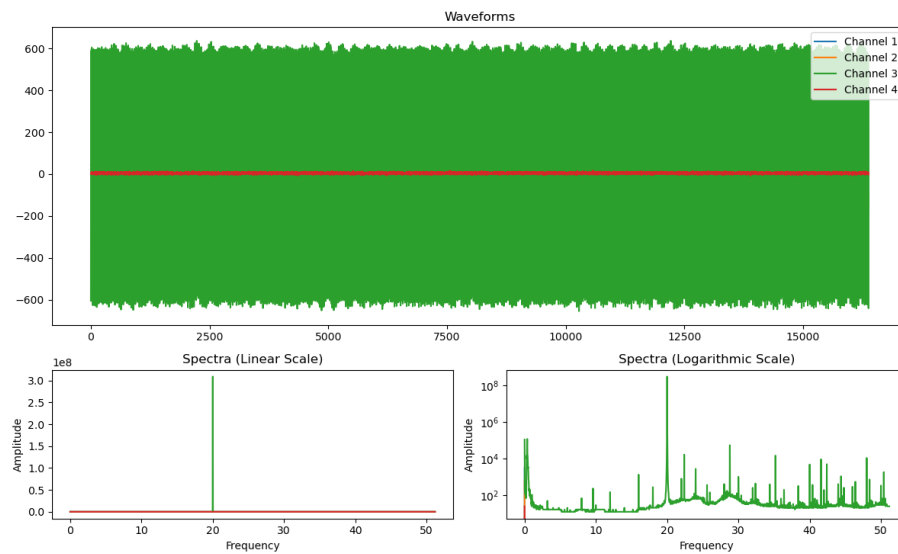
Gain L (15.0 15.0 15.0 15.0)MHz (0.0 0.0 0.0 0.0)mVpp



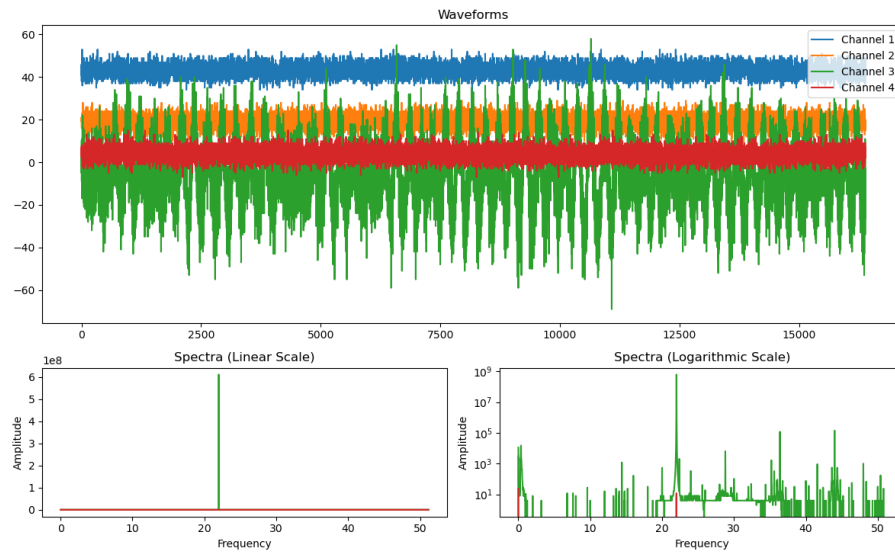
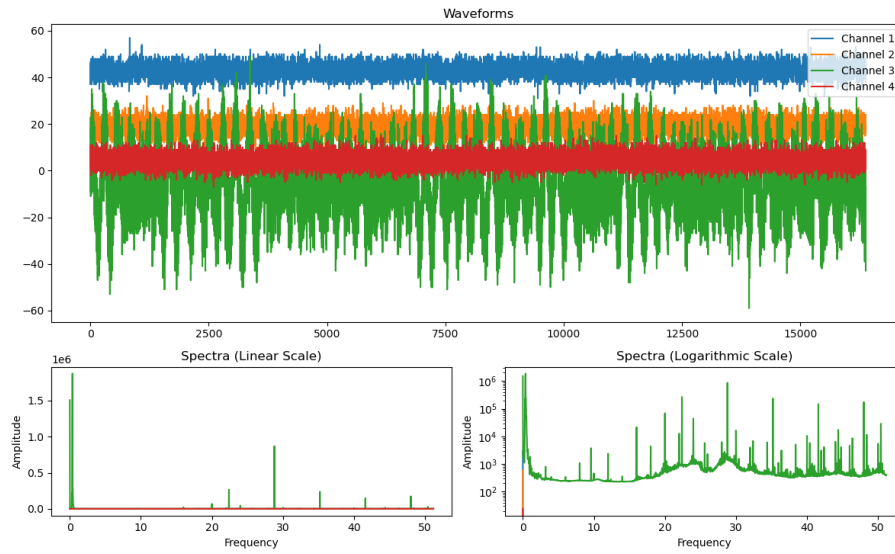
Gain L (20.0 20.0 20.0 20.0)MHz (0.0 0.0 2000.0 0.0)mVpp



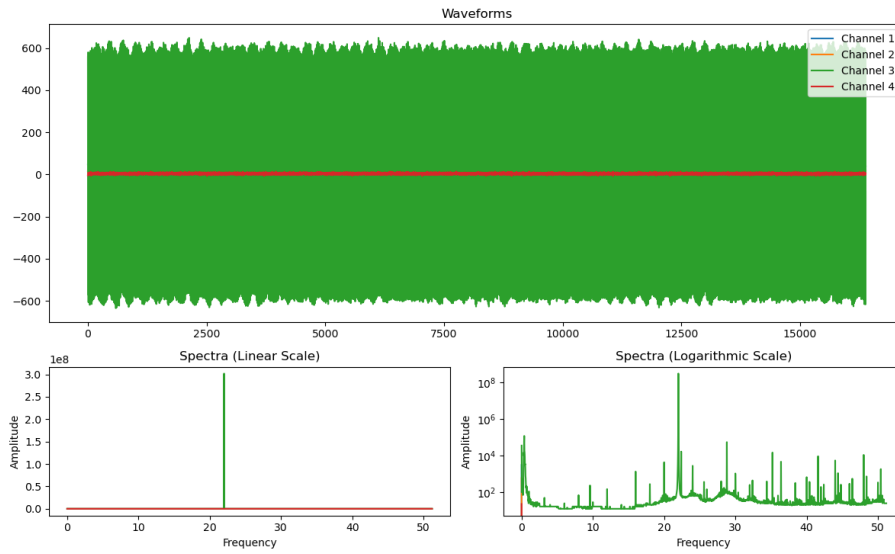
Gain L (20.0 20.0 20.0 20.0)MHz (0.0 0.0 500.0 0.0)mVpp



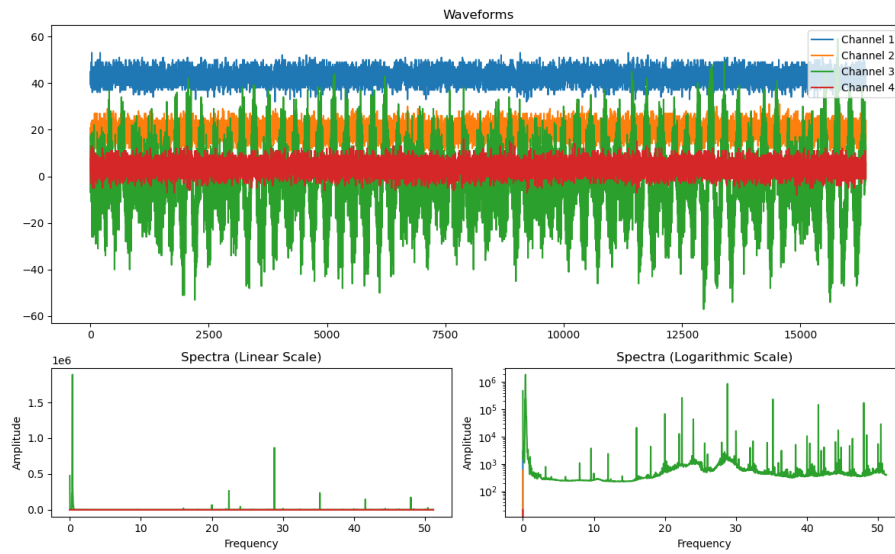
Gain L (20.0 20.0 20.0 20.0)MHz (0.0 0.0 0.0 0.0)mVpp



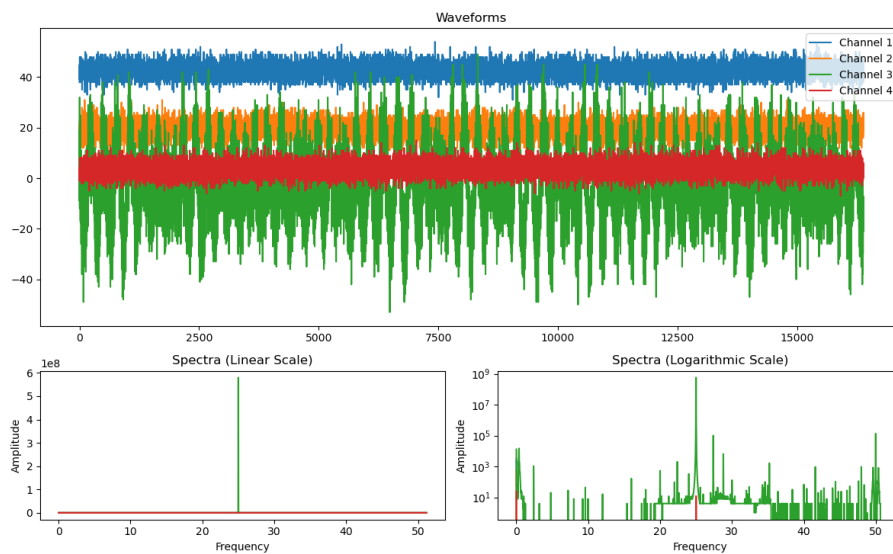
Gain L (22.0 22.0 22.0 22.0)MHz (0.0 0.0 500.0 0.0)mVpp



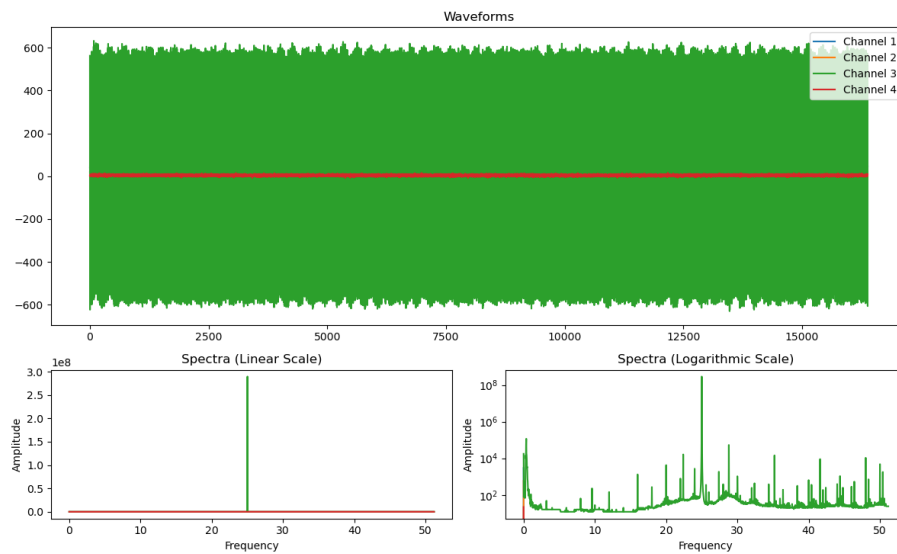
Gain L (22.0 22.0 22.0 22.0)MHz (0.0 0.0 0.0 0.0)mVpp



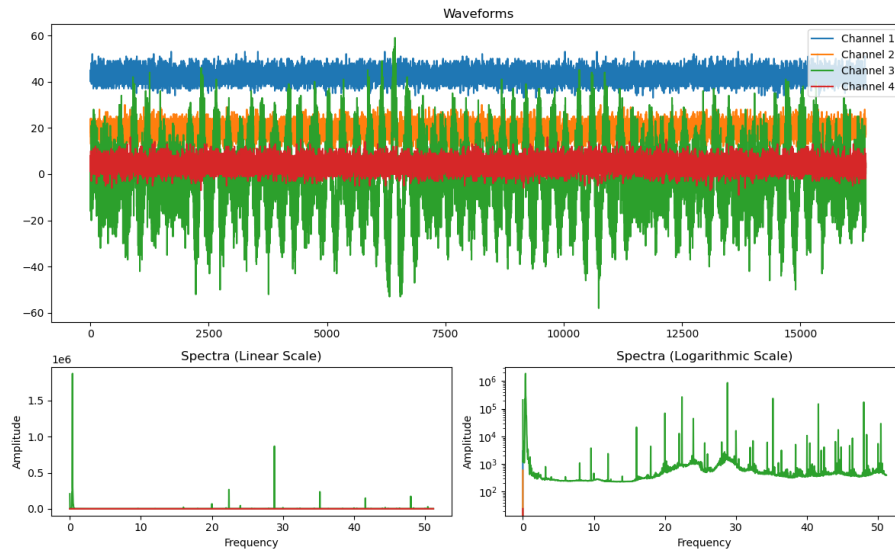
Gain L (25.0 25.0 25.0 25.0)MHz (0.0 0.0 2000.0 0.0)mVpp



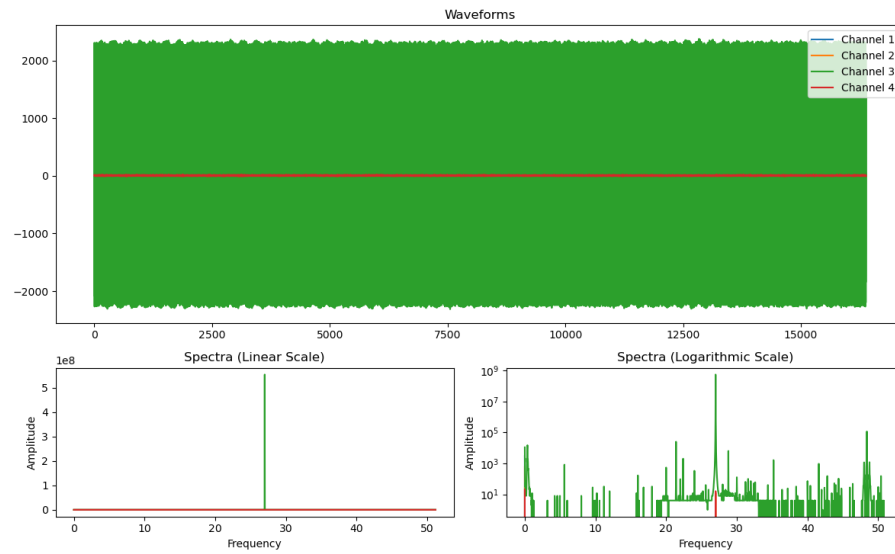
Gain L (25.0 25.0 25.0 25.0)MHz (0.0 0.0 500.0 0.0)mVpp



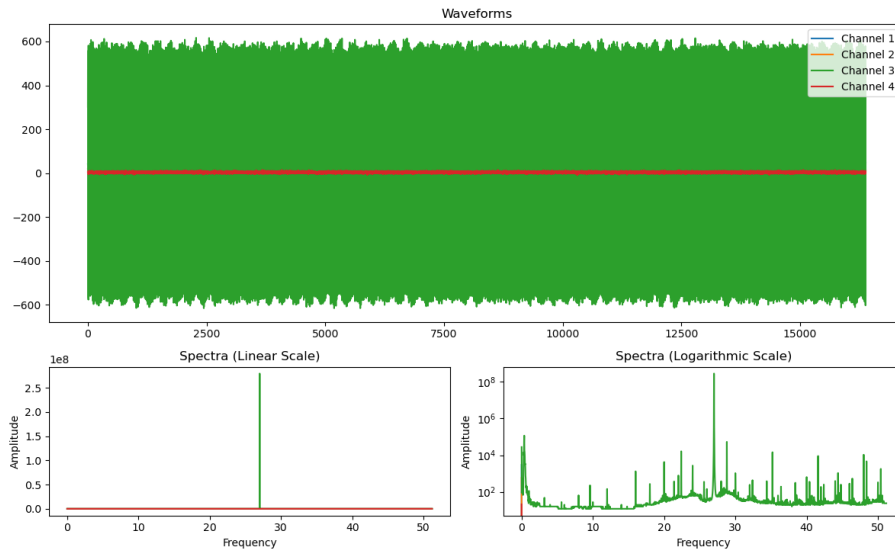
Gain L (25.0 25.0 25.0 25.0)MHz (0.0 0.0 0.0 0.0)mVpp



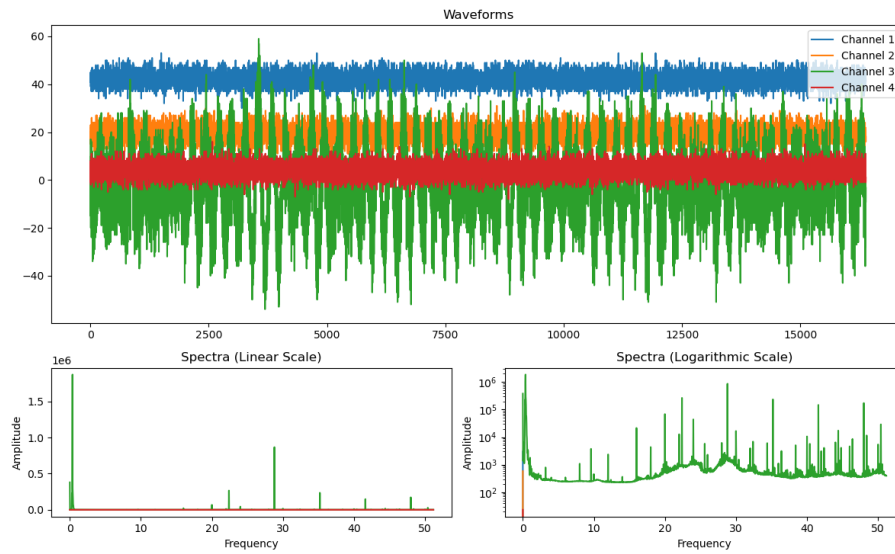
Gain L (27.0 27.0 27.0 27.0)MHz (0.0 0.0 2000.0 0.0)mVpp



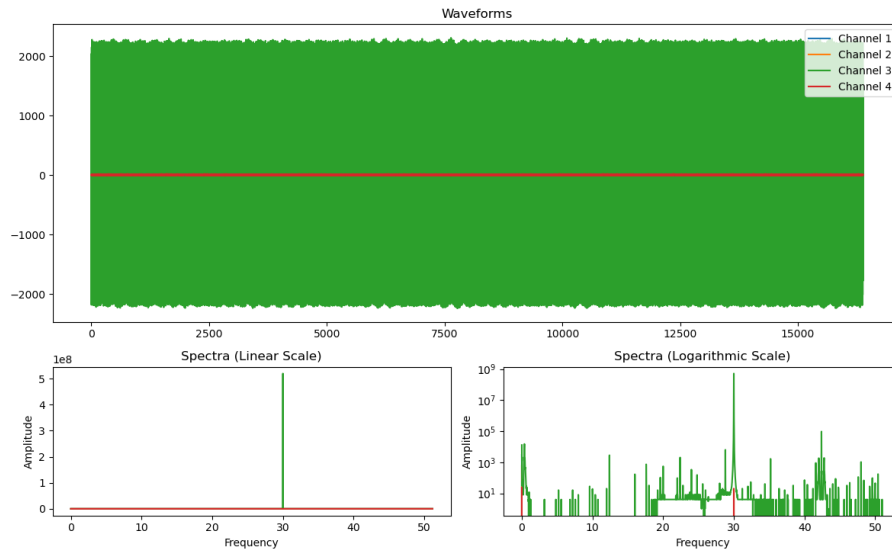
Gain L (27.0 27.0 27.0 27.0)MHz (0.0 0.0 500.0 0.0)mVpp



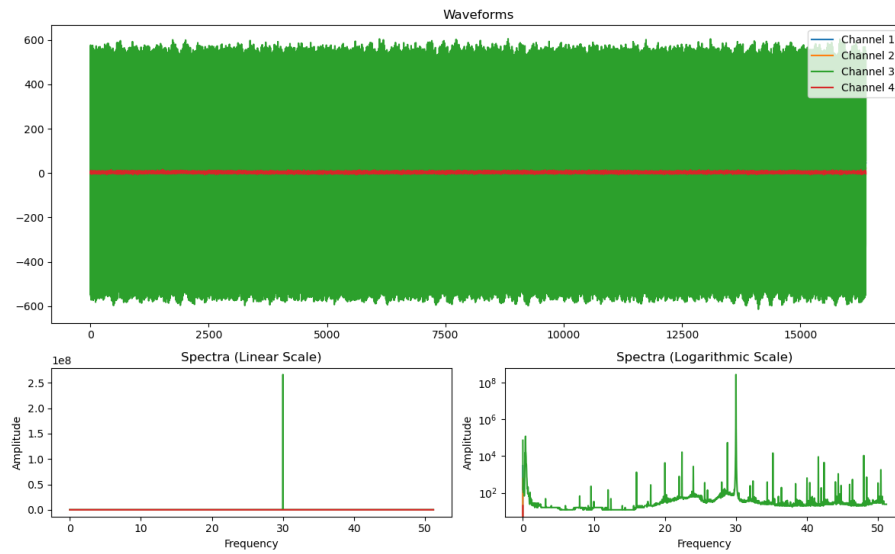
Gain L (27.0 27.0 27.0 27.0)MHz (0.0 0.0 0.0 0.0)mVpp



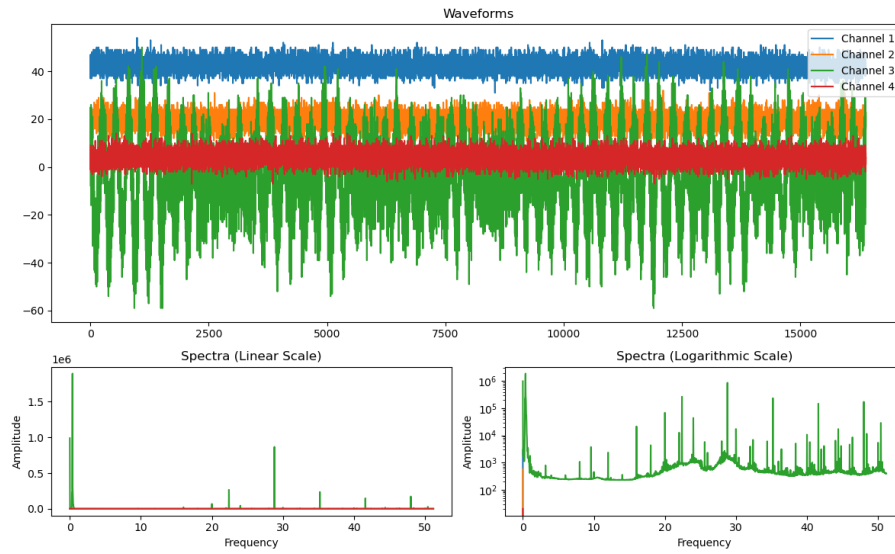
Gain L (30.0 30.0 30.0 30.0)MHz (0.0 0.0 2000.0 0.0)mVpp



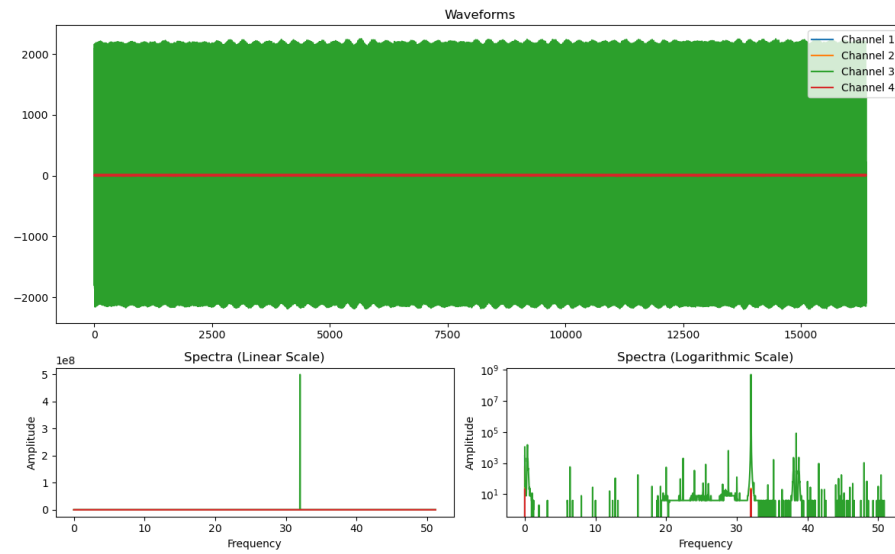
Gain L (30.0 30.0 30.0 30.0)MHz (0.0 0.0 500.0 0.0)mVpp



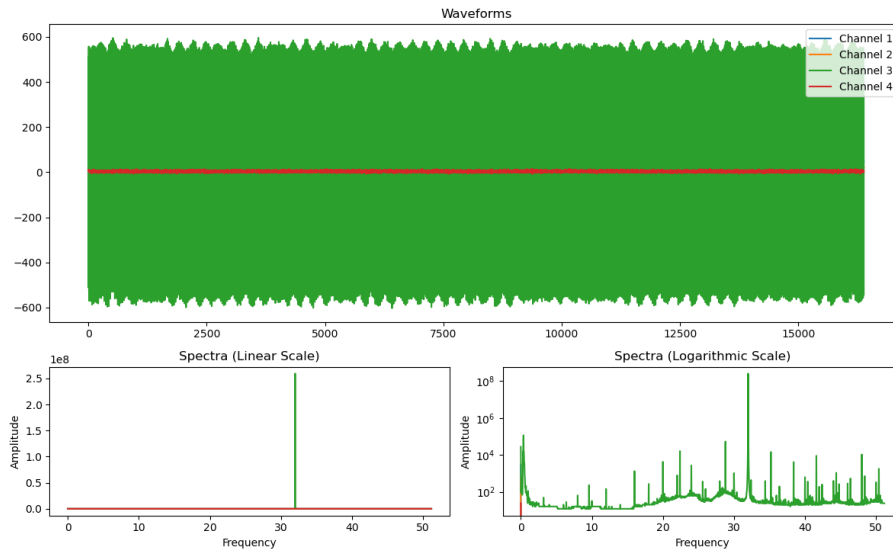
Gain L (30.0 30.0 30.0 30.0)MHz (0.0 0.0 0.0 0.0)mVpp



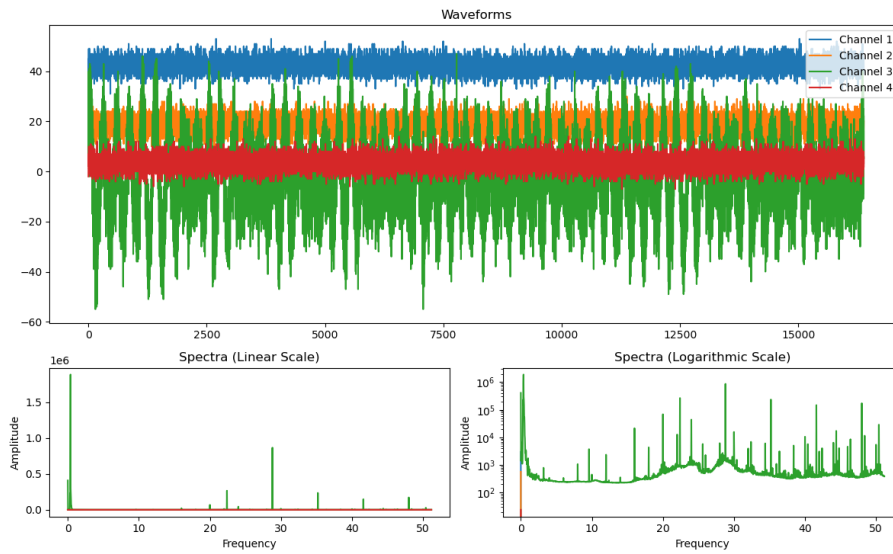
Gain L (32.0 32.0 32.0 32.0)MHz (0.0 0.0 2000.0 0.0)mVpp



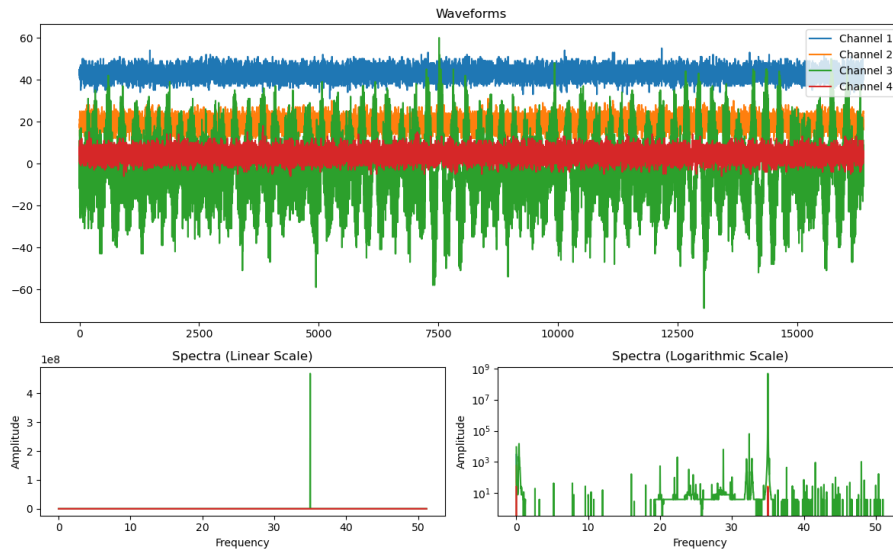
Gain L (32.0 32.0 32.0 32.0)MHz (0.0 0.0 500.0 0.0)mVpp



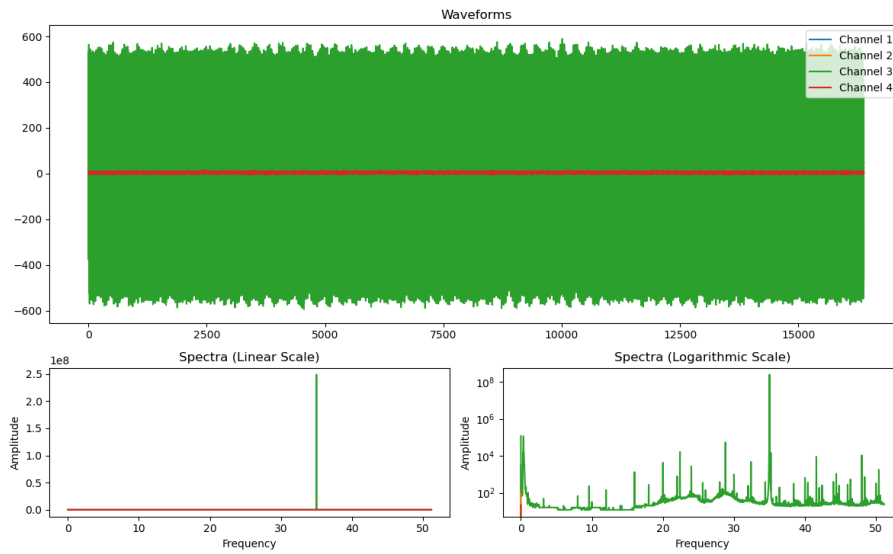
Gain L (32.0 32.0 32.0 32.0)MHz (0.0 0.0 0.0 0.0)mVpp



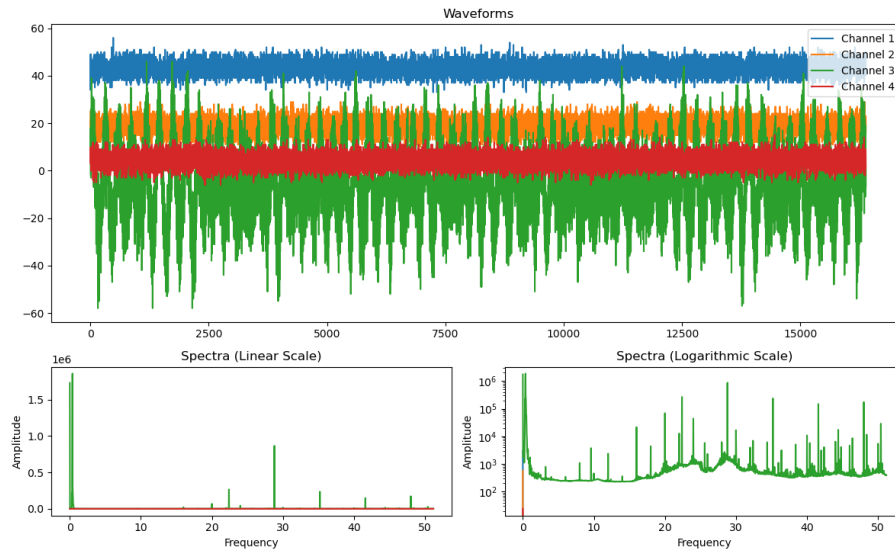
Gain L (35.0 35.0 35.0 35.0)MHz (0.0 0.0 2000.0 0.0)mVpp



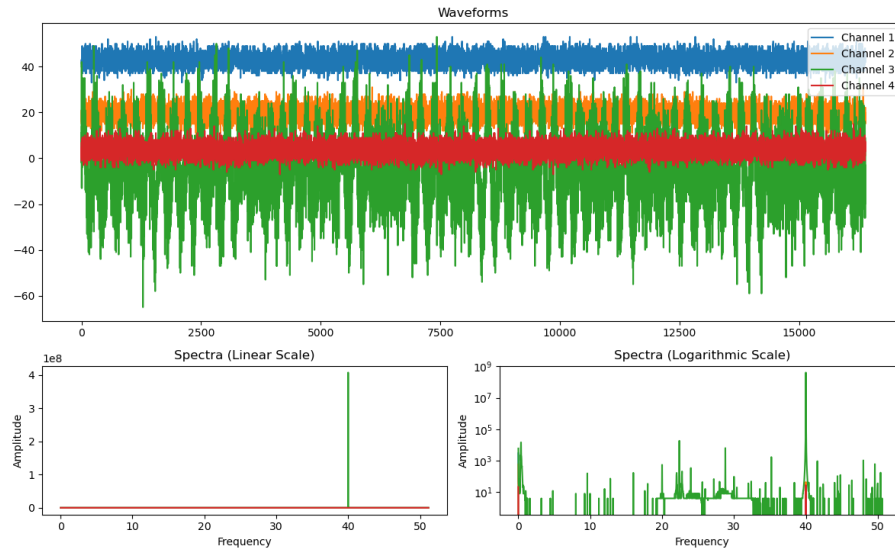
Gain L (35.0 35.0 35.0 35.0)MHz (0.0 0.0 500.0 0.0)mVpp



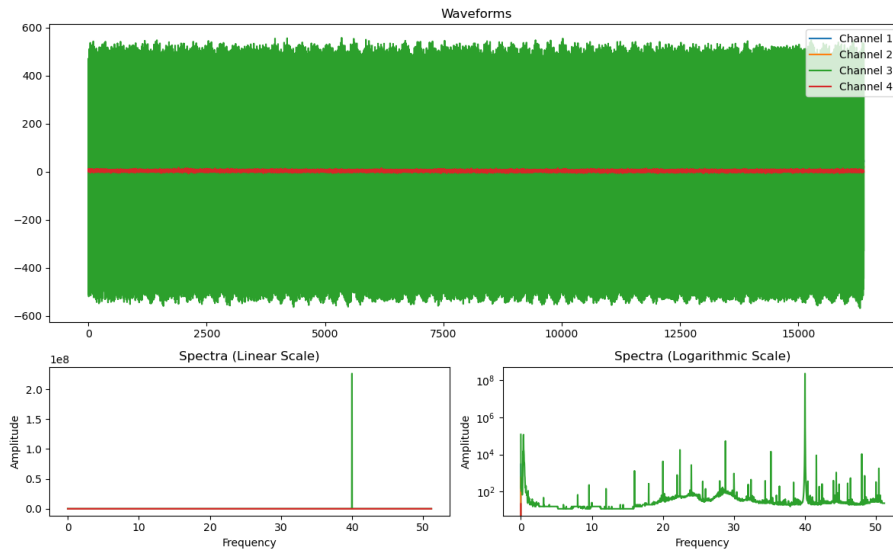
Gain L (35.0 35.0 35.0 35.0)MHz (0.0 0.0 0.0 0.0)mVpp



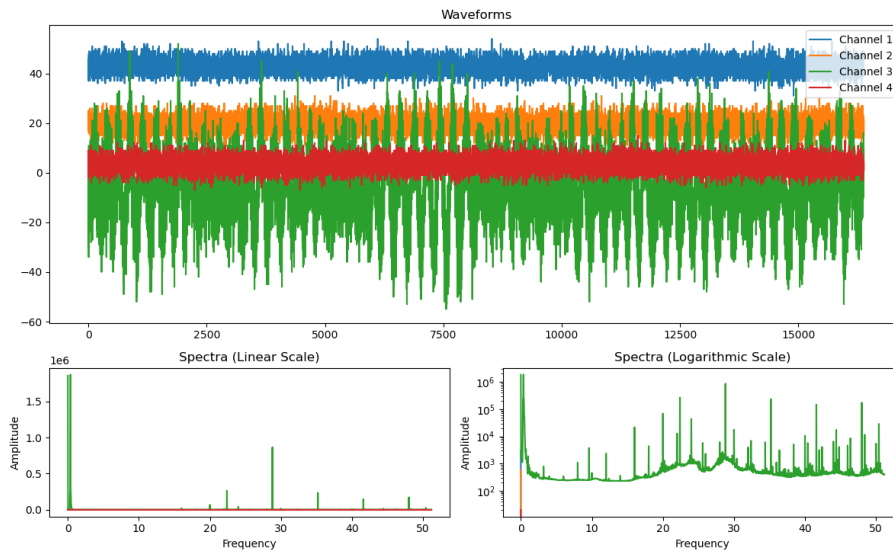
Gain L (40.0 40.0 40.0 40.0)MHz (0.0 0.0 2000.0 0.0)mVpp



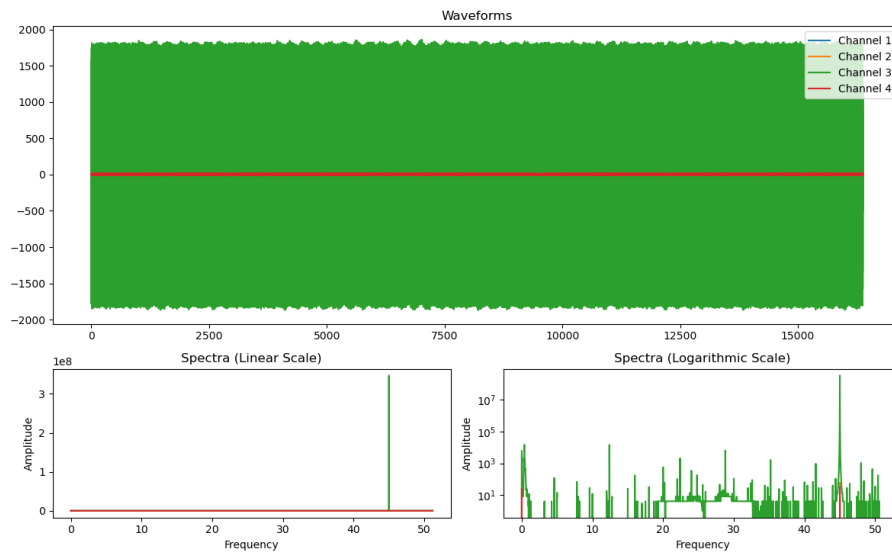
Gain L (40.0 40.0 40.0 40.0)MHz (0.0 0.0 500.0 0.0)mVpp



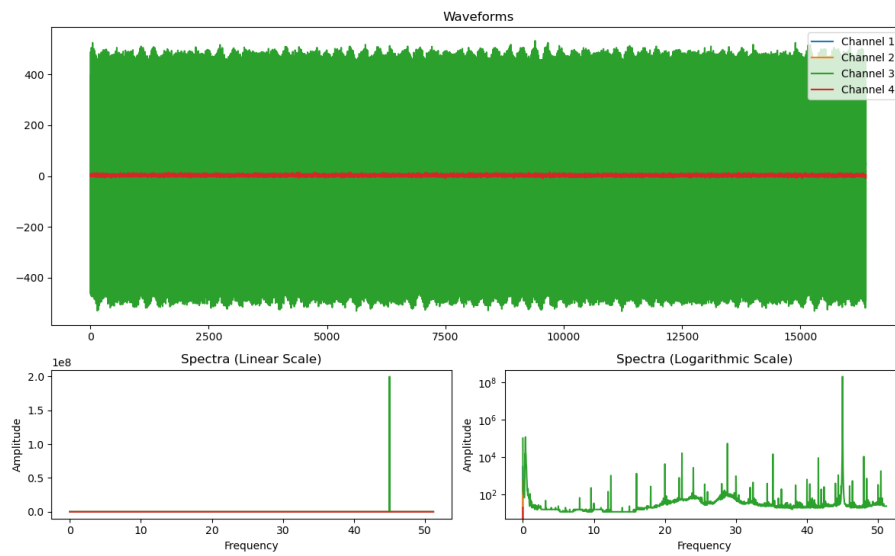
Gain L (40.0 40.0 40.0 40.0)MHz (0.0 0.0 0.0 0.0)mVpp



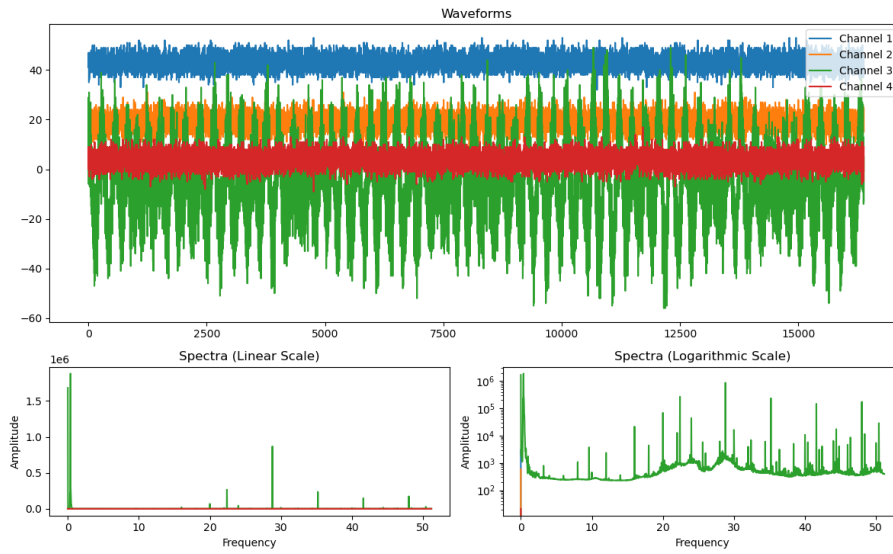
Gain L (45.0 45.0 45.0 45.0)MHz (0.0 0.0 2000.0 0.0)mVpp



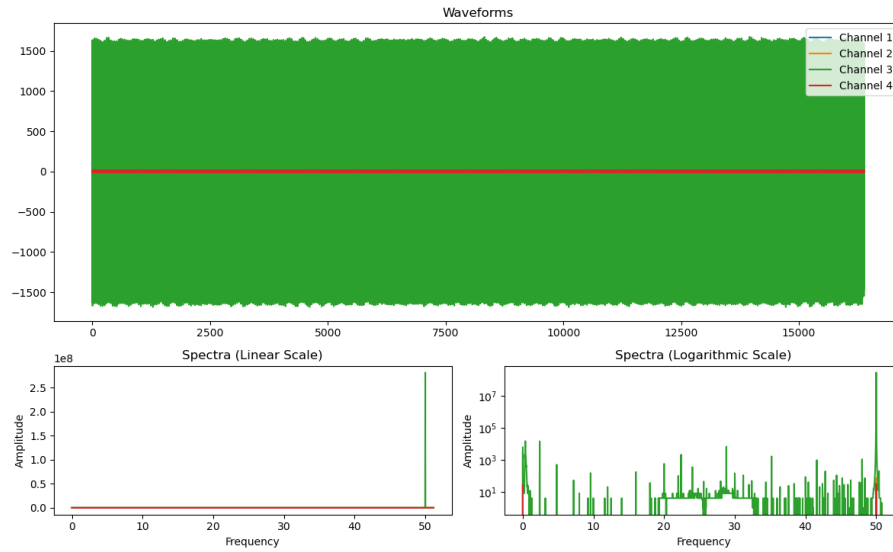
Gain L (45.0 45.0 45.0 45.0)MHz (0.0 0.0 500.0 0.0)mVpp



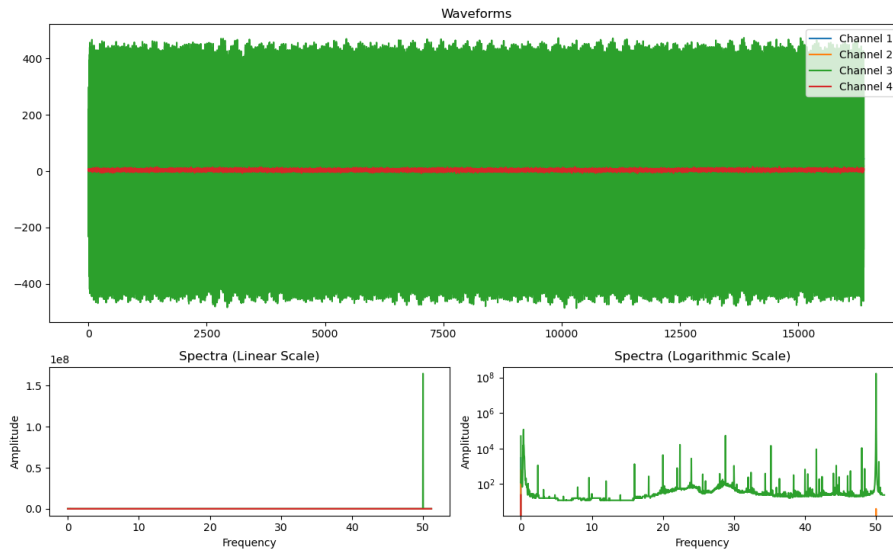
Gain L (45.0 45.0 45.0 45.0)MHz (0.0 0.0 0.0 0.0)mVpp



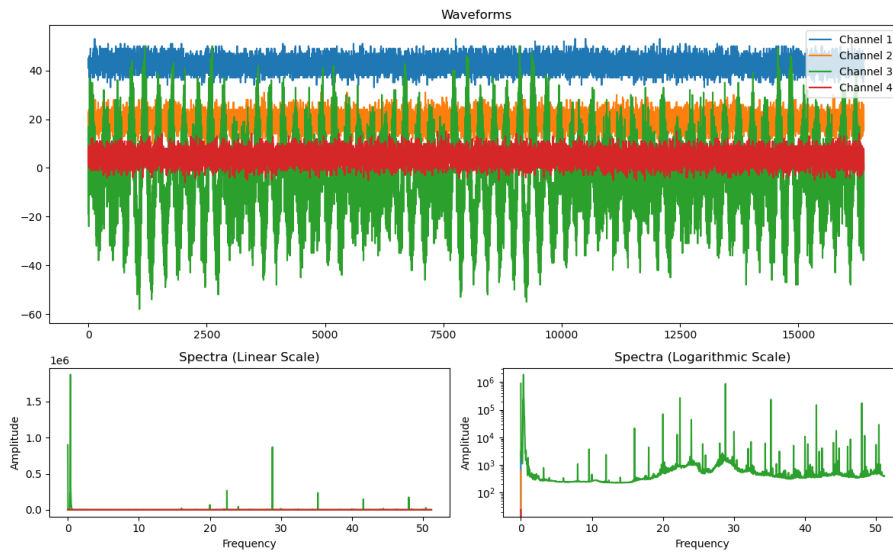
Gain L (50.0 50.0 50.0 50.0)MHz (0.0 0.0 2000.0 0.0)mVpp



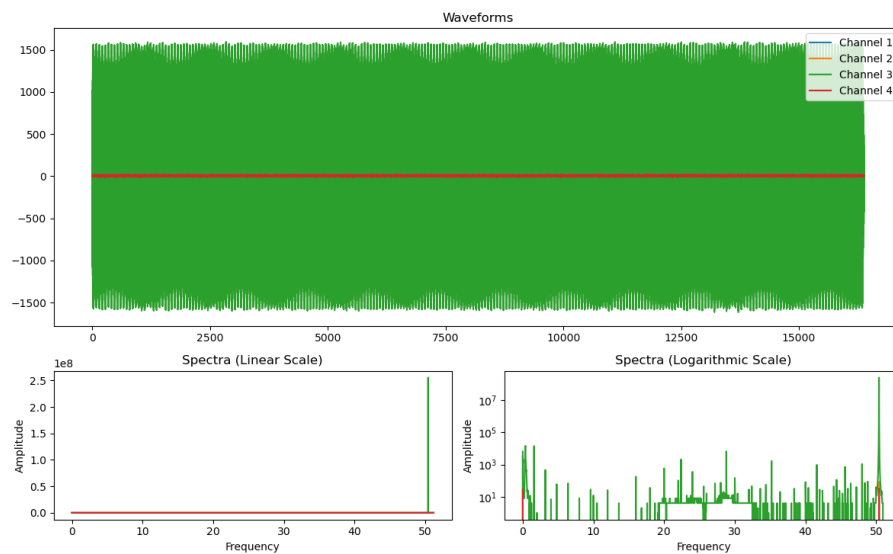
Gain L (50.0 50.0 50.0 50.0)MHz (0.0 0.0 500.0 0.0)mVpp



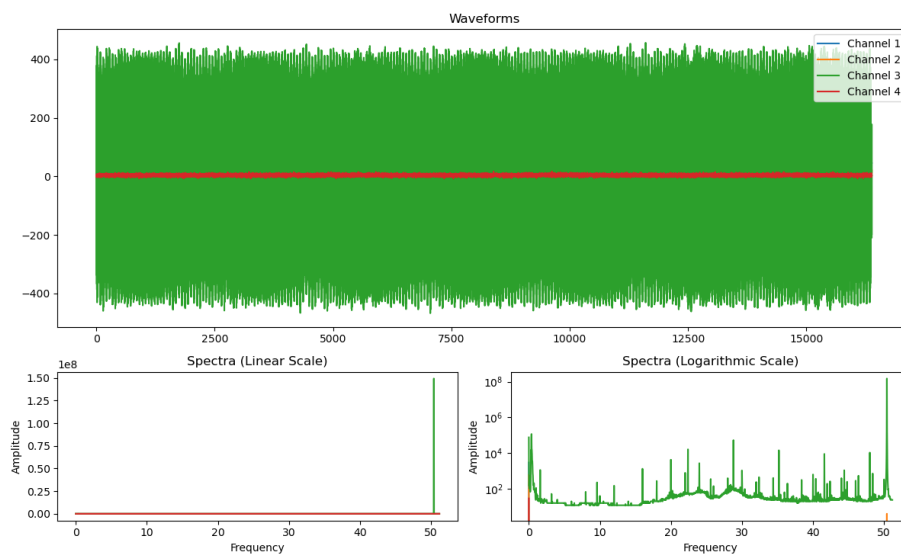
Gain L (50.0 50.0 50.0 50.0)MHz (0.0 0.0 0.0 0.0)mVpp



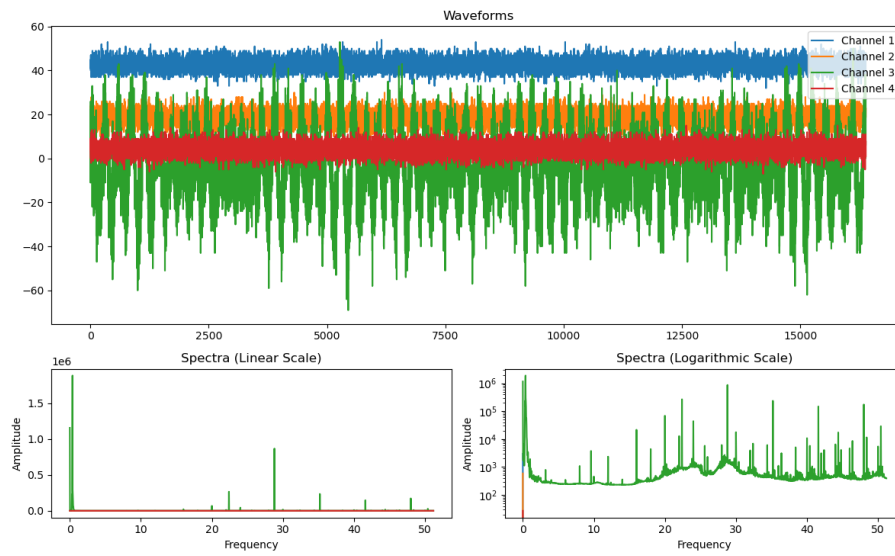
Gain L (52.0 52.0 52.0 52.0)MHz (0.0 0.0 2000.0 0.0)mVpp



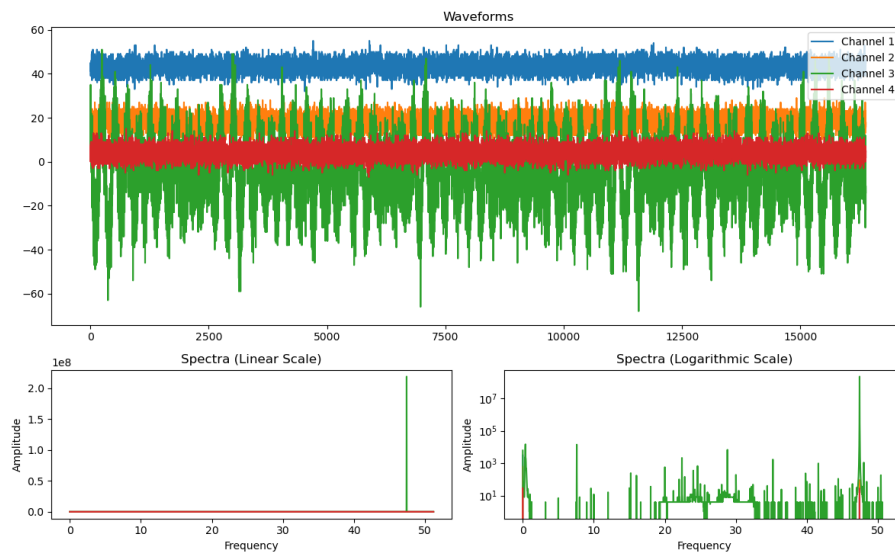
Gain L (52.0 52.0 52.0 52.0)MHz (0.0 0.0 500.0 0.0)mVpp



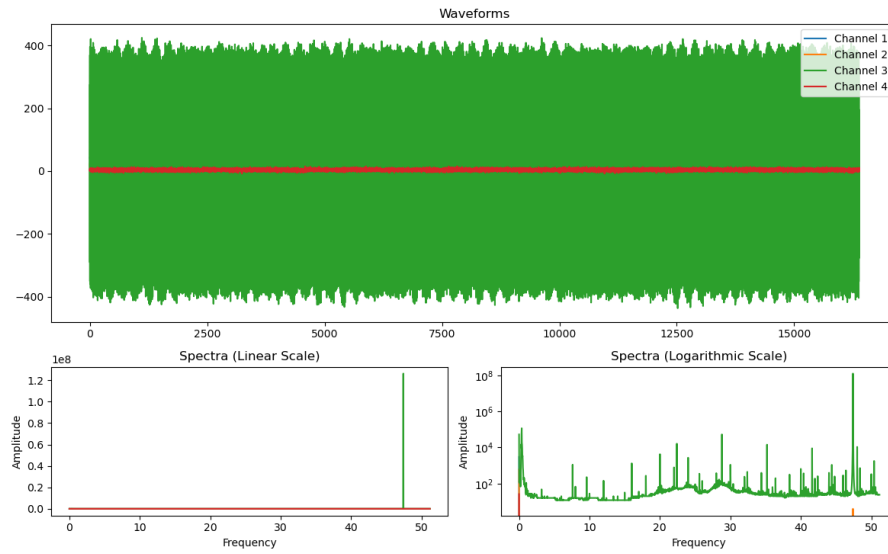
Gain L (52.0 52.0 52.0 52.0)MHz (0.0 0.0 0.0 0.0)mVpp



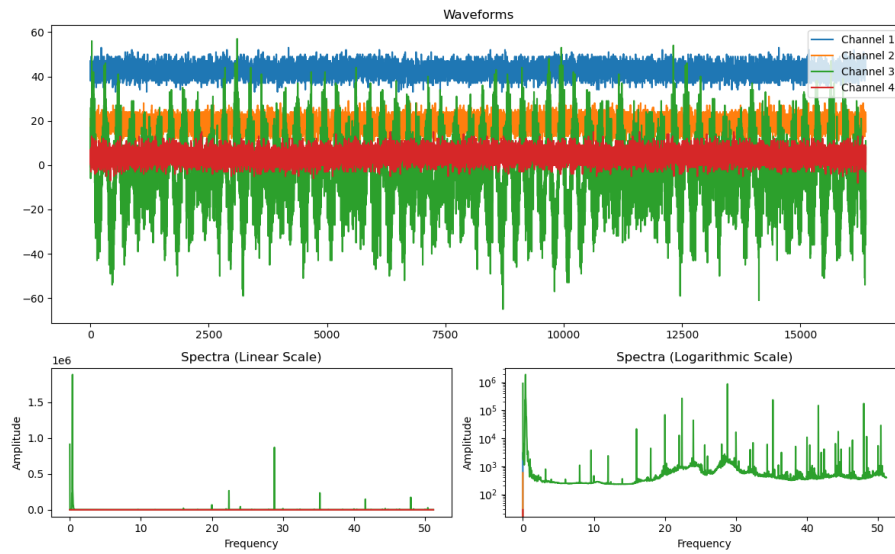
Gain L (55.0 55.0 55.0 55.0)MHz (0.0 0.0 2000.0 0.0)mVpp



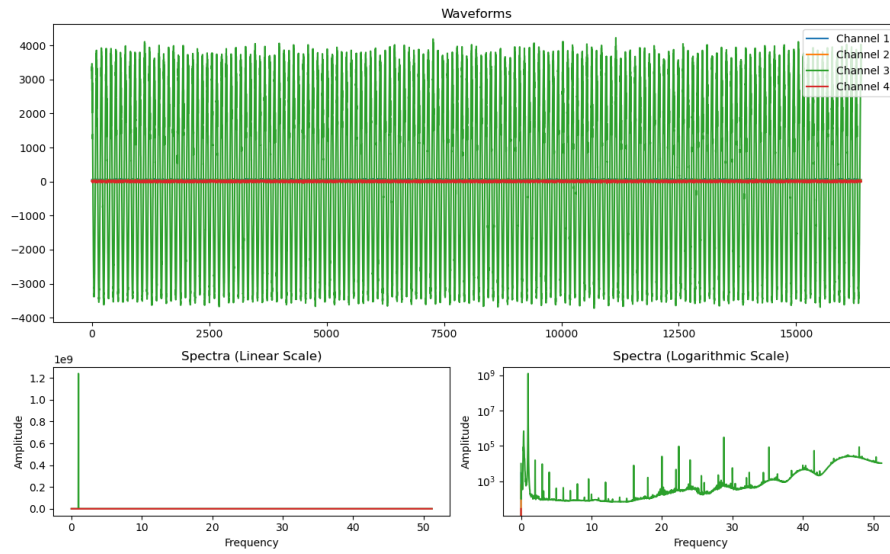
Gain L (55.0 55.0 55.0 55.0)MHz (0.0 0.0 500.0 0.0)mVpp



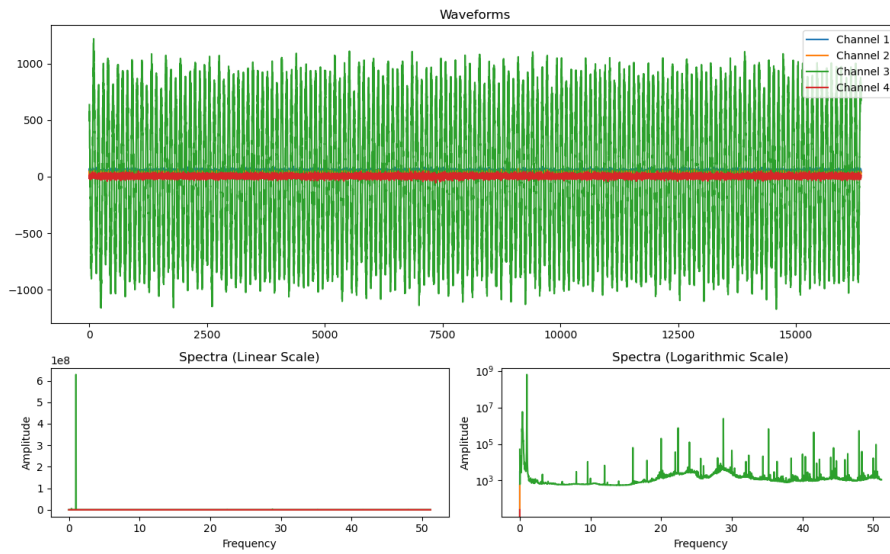
Gain L (55.0 55.0 55.0 55.0)MHz (0.0 0.0 0.0 0.0)mVpp



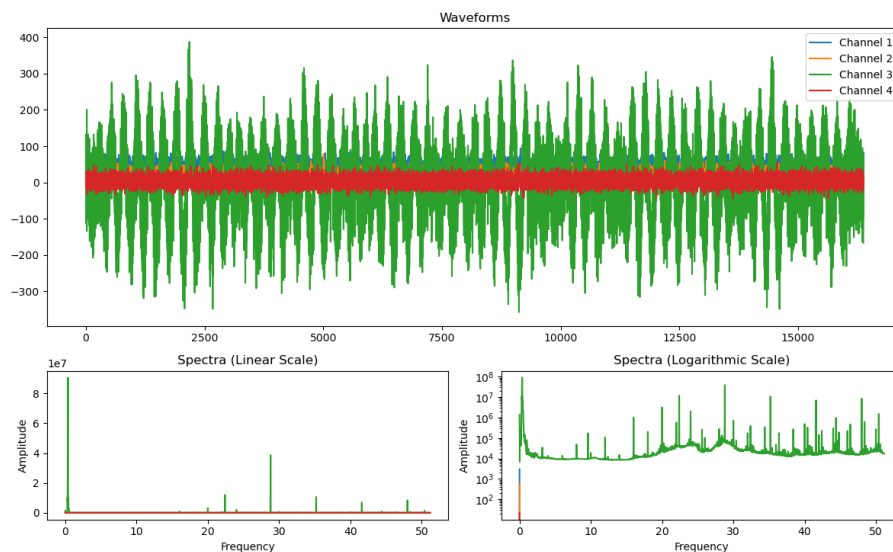
Gain M (1.0 1.0 1.0 1.0)MHz (0.0 0.0 400.0 0.0)mVpp



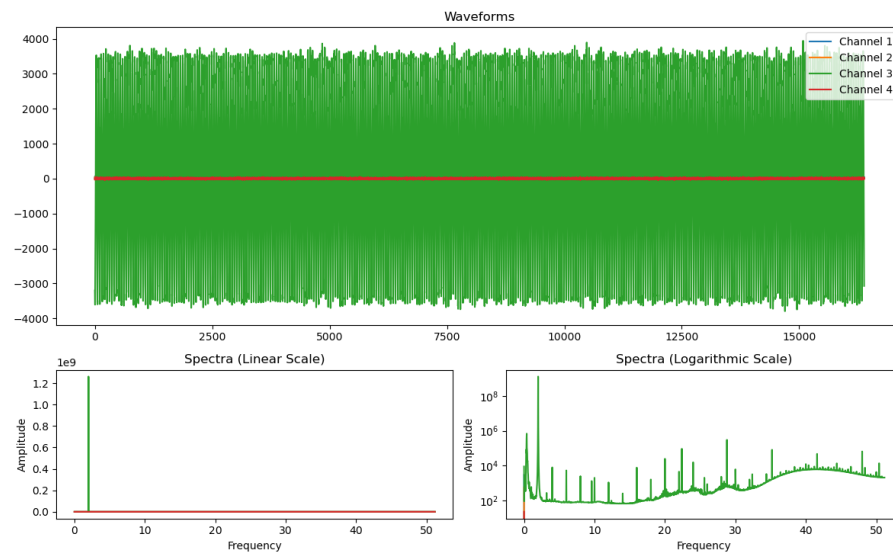
Gain M (1.0 1.0 1.0 1.0)MHz (0.0 0.0 100.0 0.0)mVpp



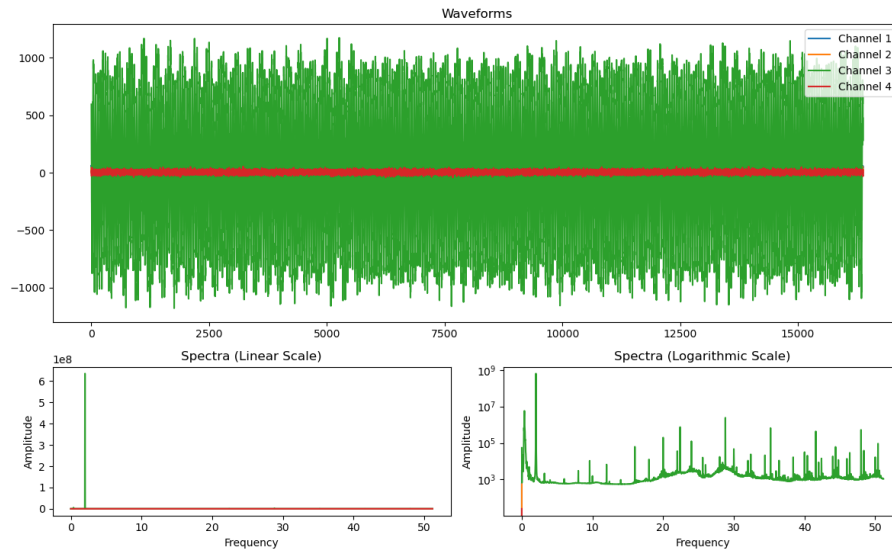
Gain M (1.0 1.0 1.0 1.0)MHz (0.0 0.0 0.0 0.0)mVpp



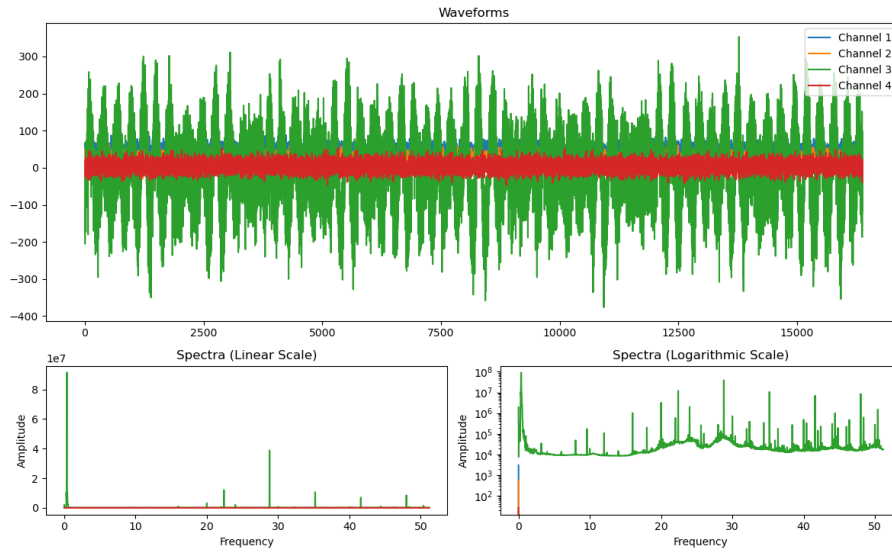
Gain M (2.0 2.0 2.0 2.0)MHz (0.0 0.0 400.0 0.0)mVpp



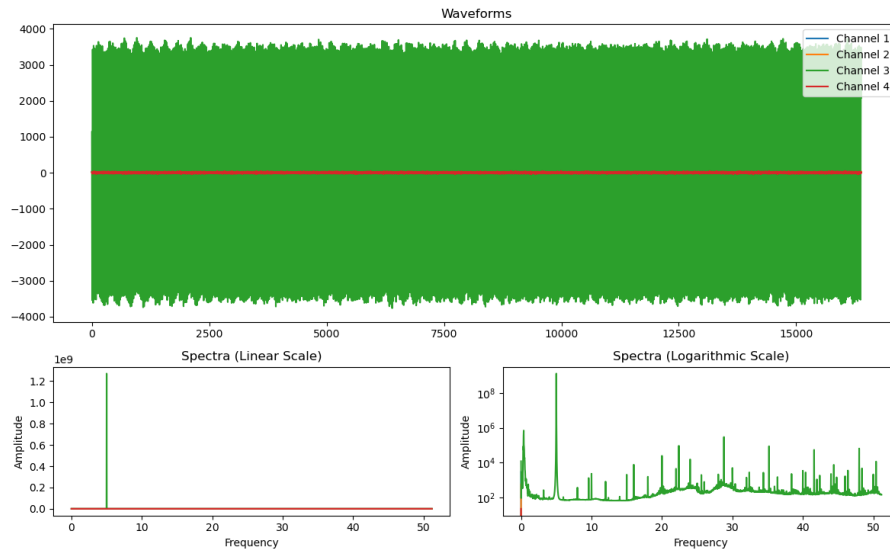
Gain M (2.0 2.0 2.0 2.0)MHz (0.0 0.0 100.0 0.0)mVpp



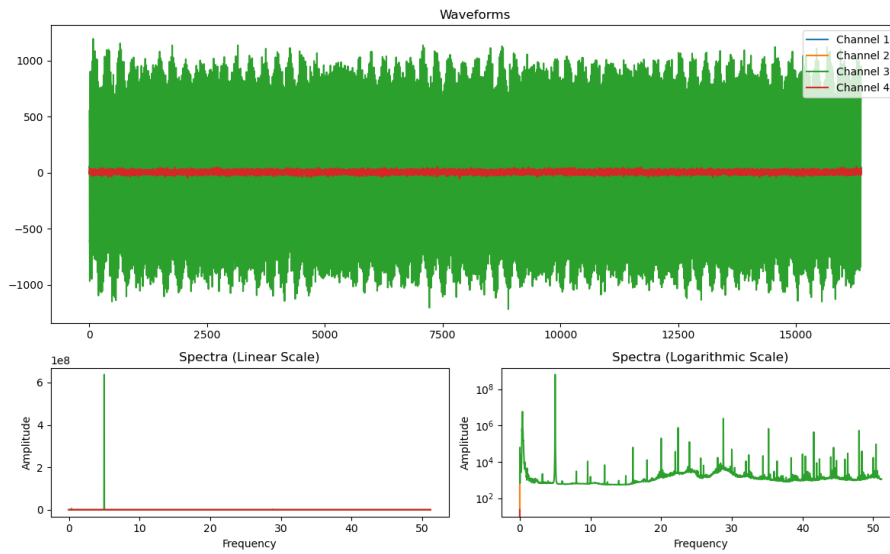
Gain M (2.0 2.0 2.0 2.0)MHz (0.0 0.0 0.0 0.0)mVpp



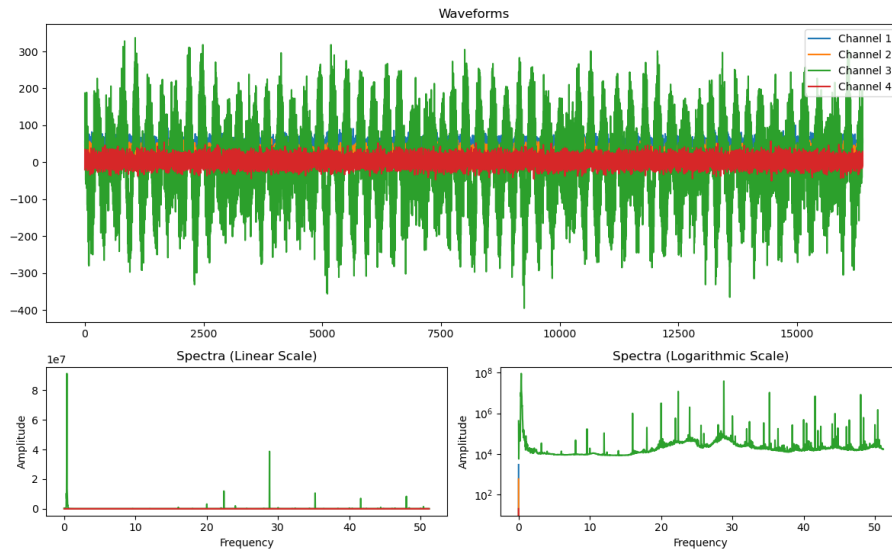
Gain M (5.0 5.0 5.0 5.0)MHz (0.0 0.0 400.0 0.0)mVpp



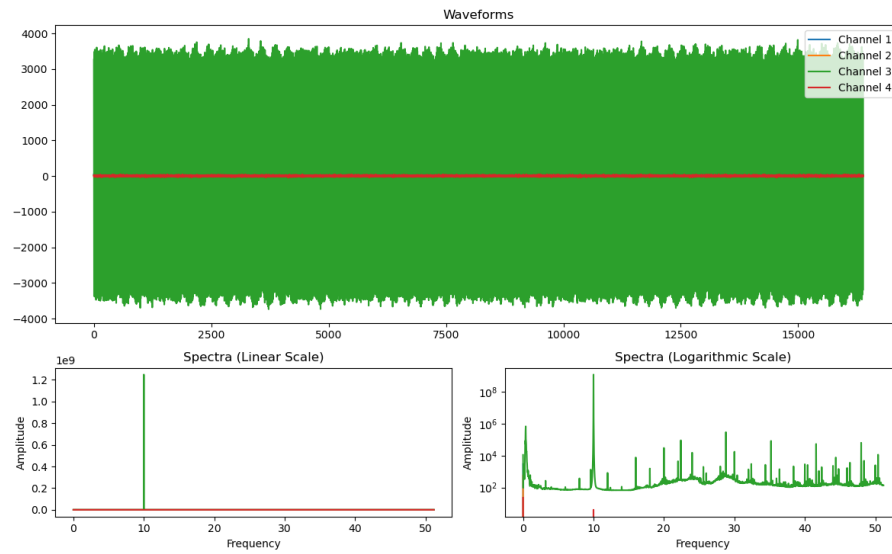
Gain M (5.0 5.0 5.0 5.0)MHz (0.0 0.0 100.0 0.0)mVpp



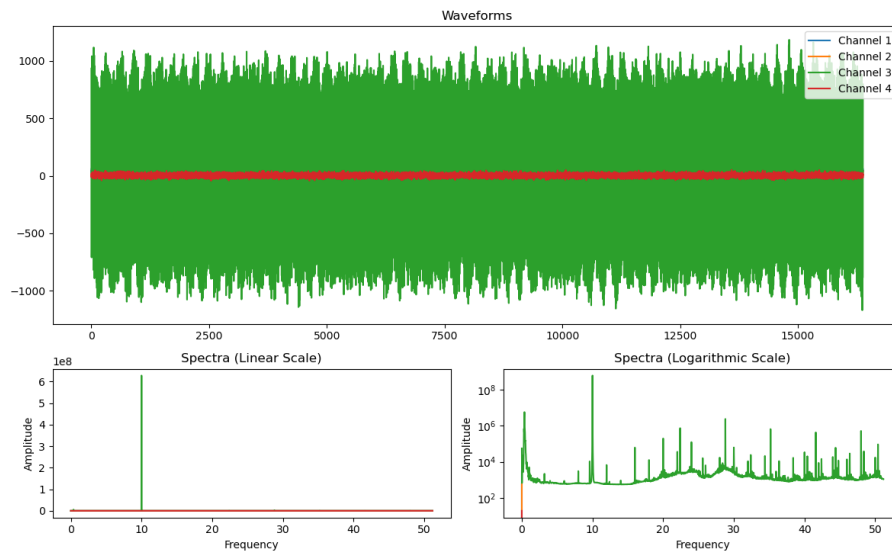
Gain M (5.0 5.0 5.0 5.0)MHz (0.0 0.0 0.0 0.0)mVpp



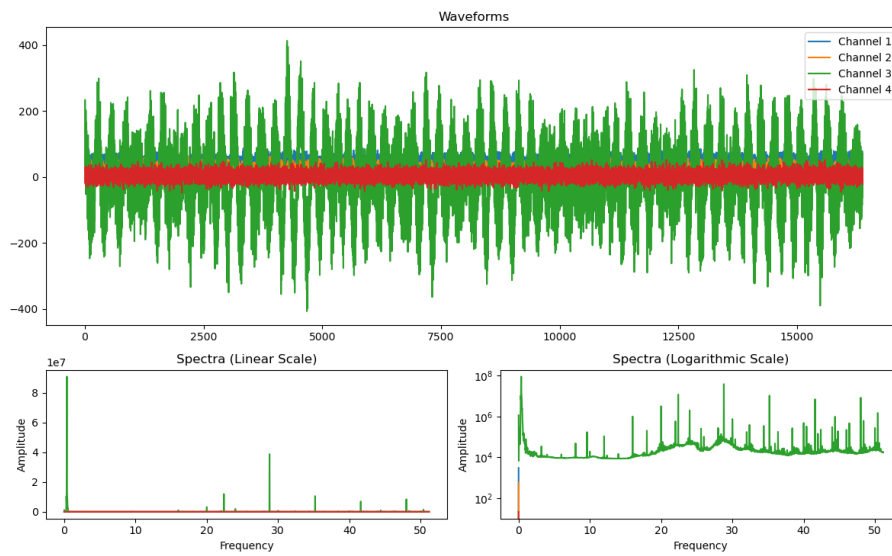
Gain M (10.0 10.0 10.0 10.0)MHz (0.0 0.0 400.0 0.0)mVpp



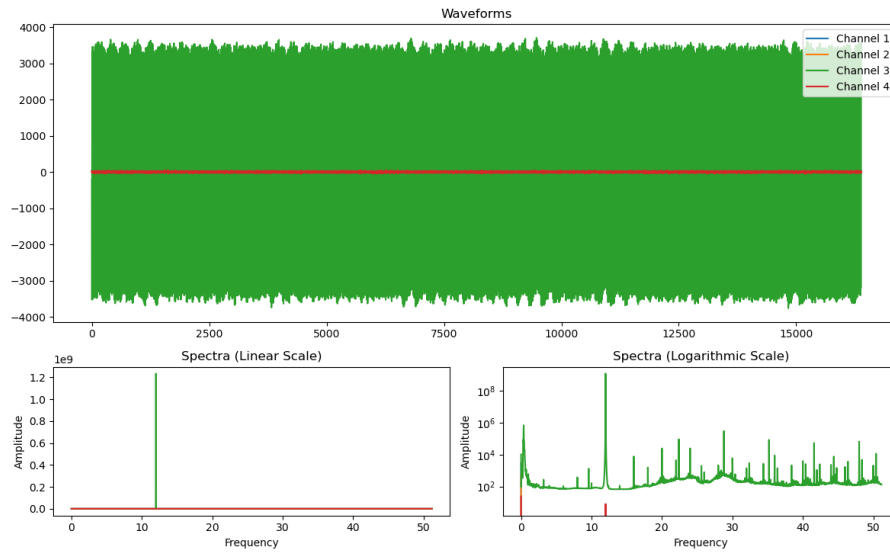
Gain M (10.0 10.0 10.0 10.0)MHz (0.0 0.0 100.0 0.0)mVpp



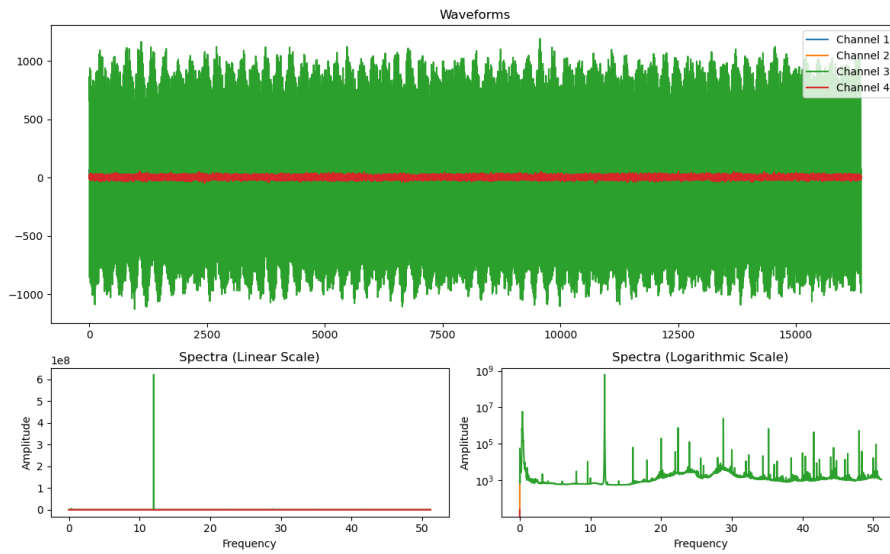
Gain M (10.0 10.0 10.0 10.0)MHz (0.0 0.0 0.0 0.0)mVpp



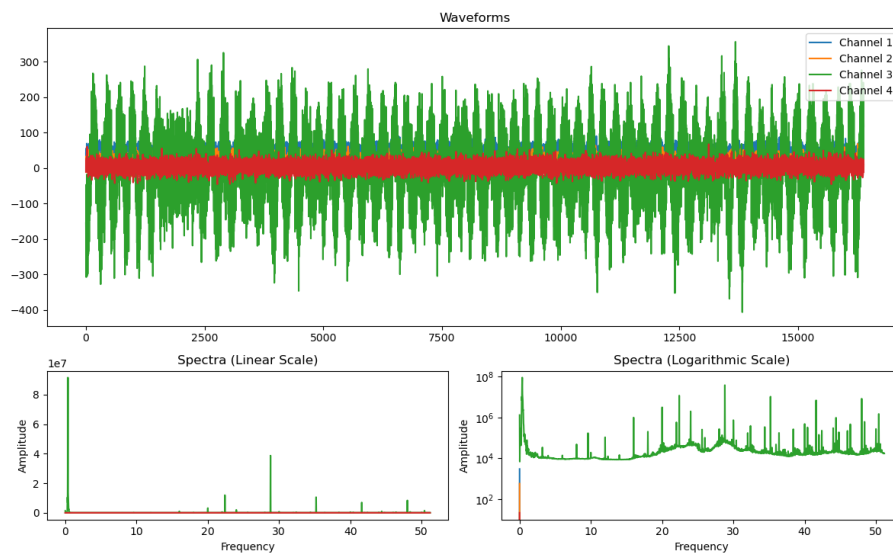
Gain M (12.0 12.0 12.0 12.0)MHz (0.0 0.0 400.0 0.0)mVpp



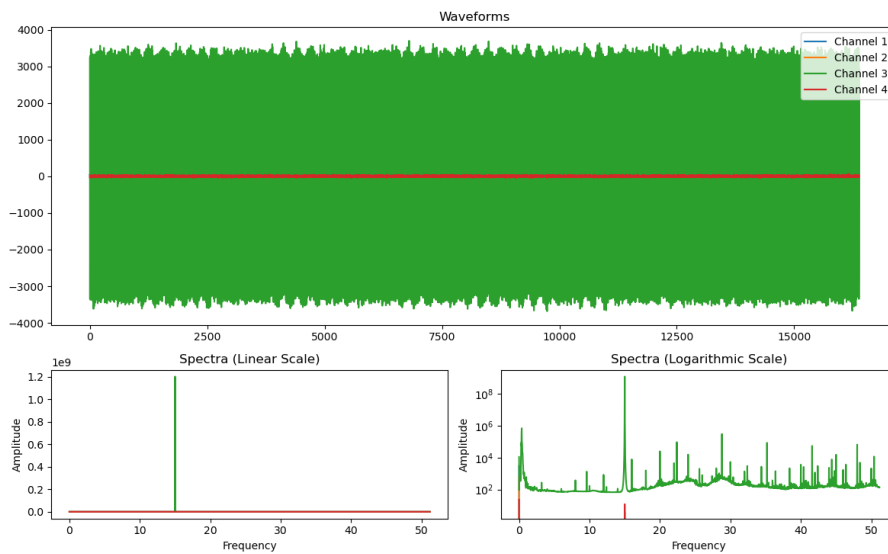
Gain M (12.0 12.0 12.0 12.0)MHz (0.0 0.0 100.0 0.0)mVpp



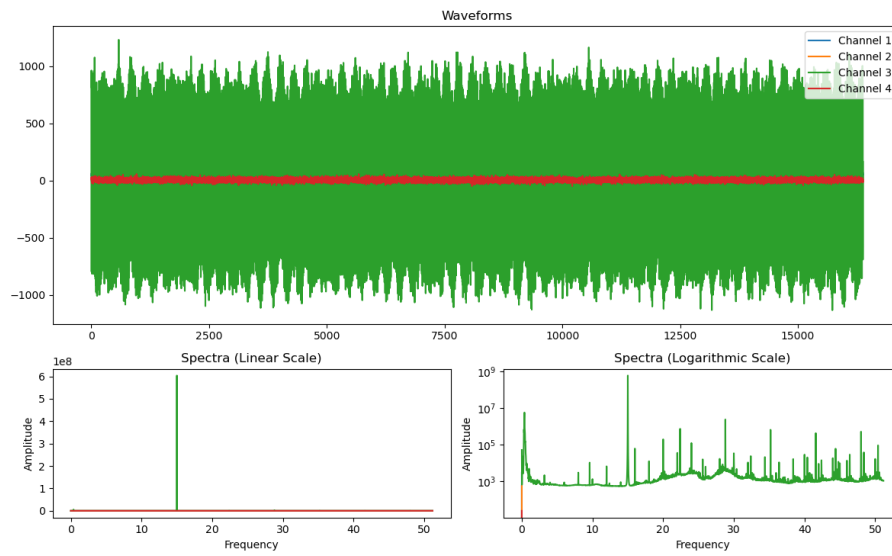
Gain M (12.0 12.0 12.0 12.0)MHz (0.0 0.0 0.0 0.0)mVpp



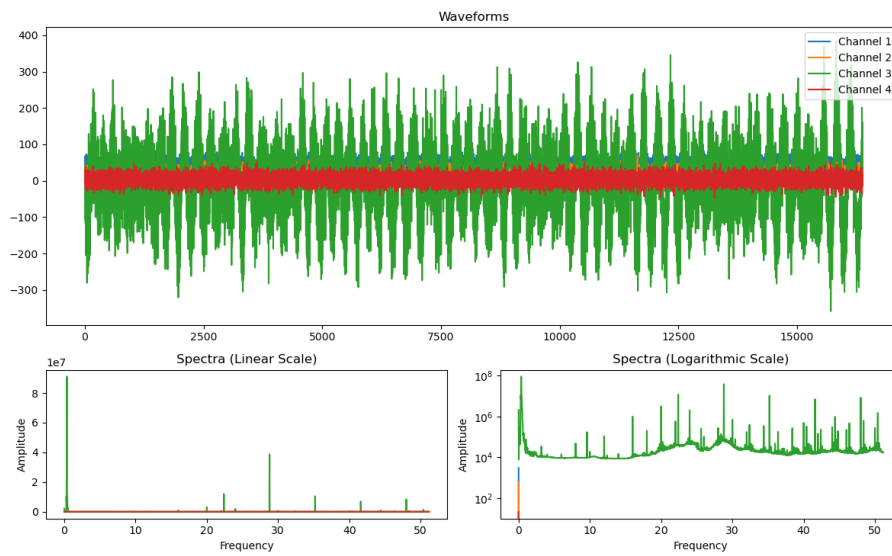
Gain M (15.0 15.0 15.0 15.0)MHz (0.0 0.0 400.0 0.0)mVpp



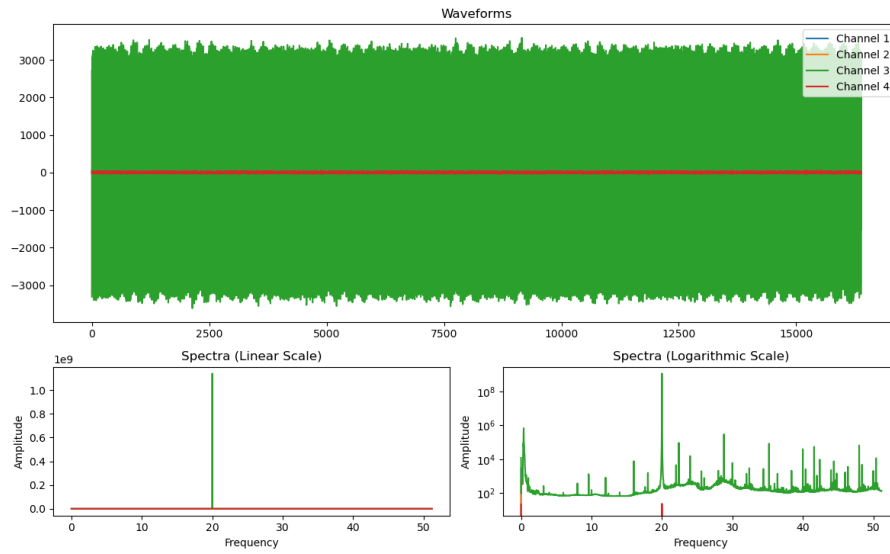
Gain M (15.0 15.0 15.0 15.0)MHz (0.0 0.0 100.0 0.0)mVpp



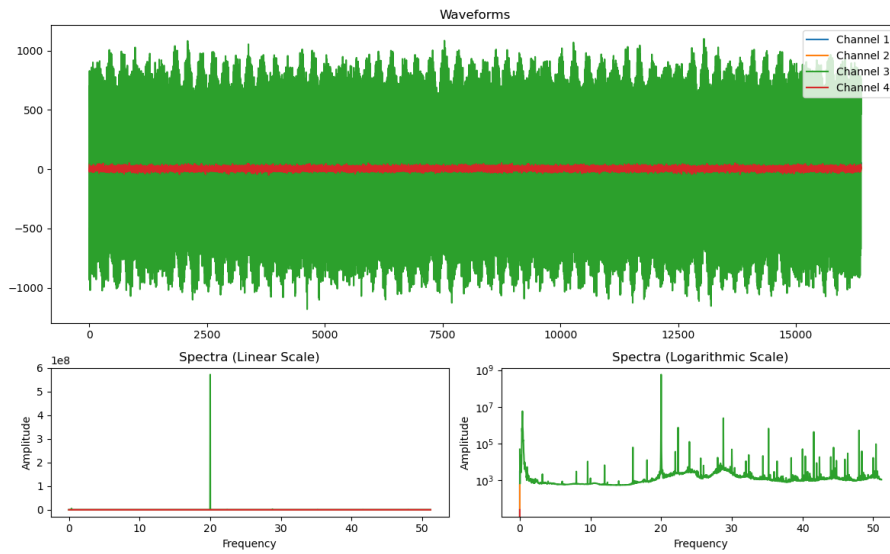
Gain M (15.0 15.0 15.0 15.0)MHz (0.0 0.0 0.0 0.0)mVpp



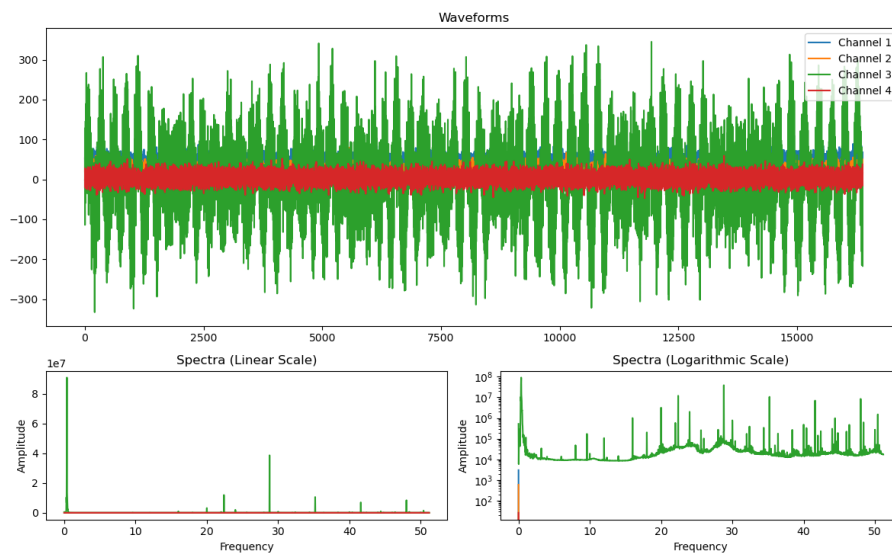
Gain M (20.0 20.0 20.0 20.0)MHz (0.0 0.0 400.0 0.0)mVpp



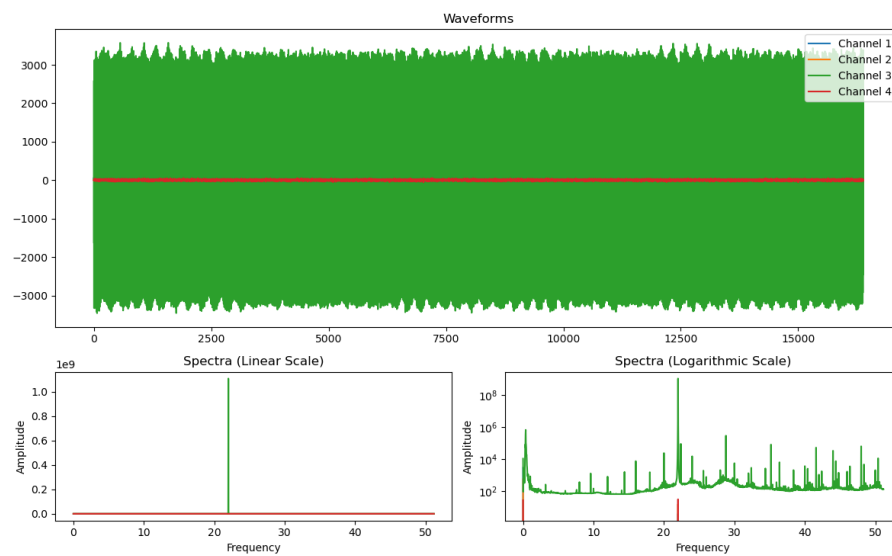
Gain M (20.0 20.0 20.0 20.0)MHz (0.0 0.0 100.0 0.0)mVpp



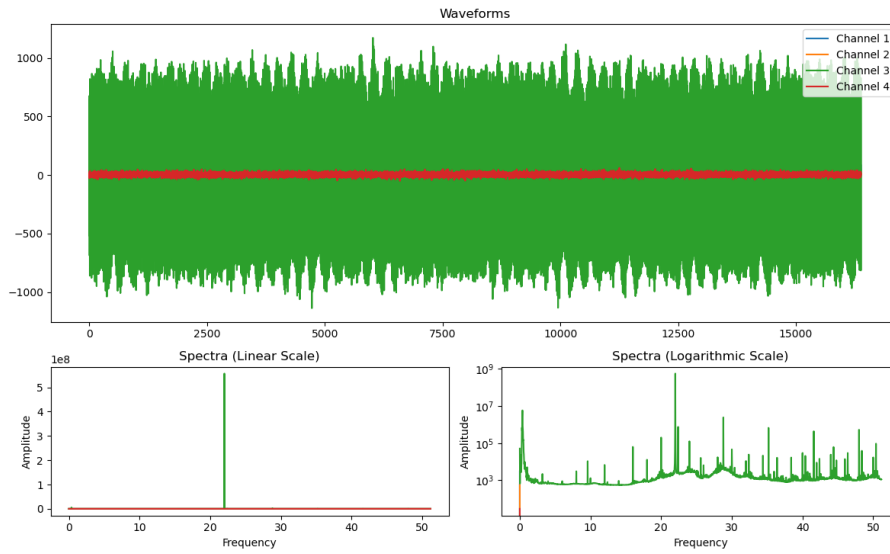
Gain M (20.0 20.0 20.0 20.0)MHz (0.0 0.0 0.0 0.0)mVpp



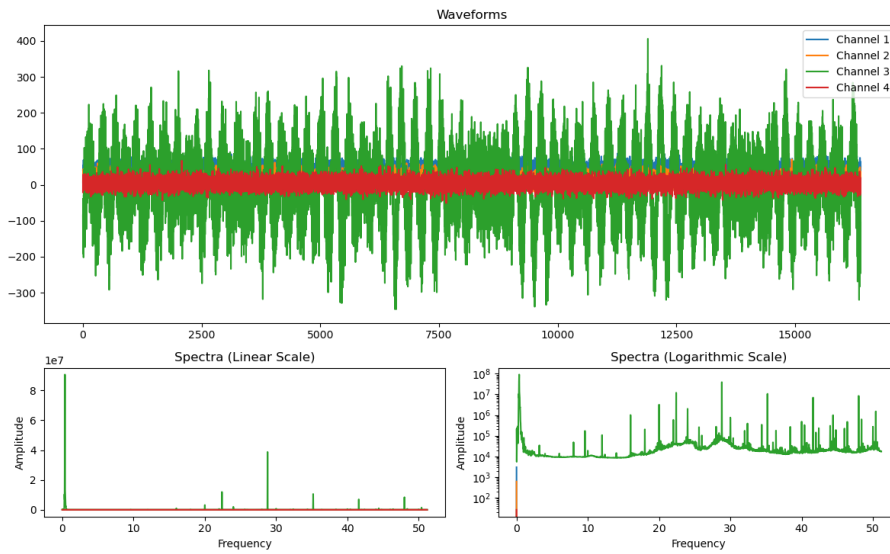
Gain M (22.0 22.0 22.0 22.0)MHz (0.0 0.0 400.0 0.0)mVpp



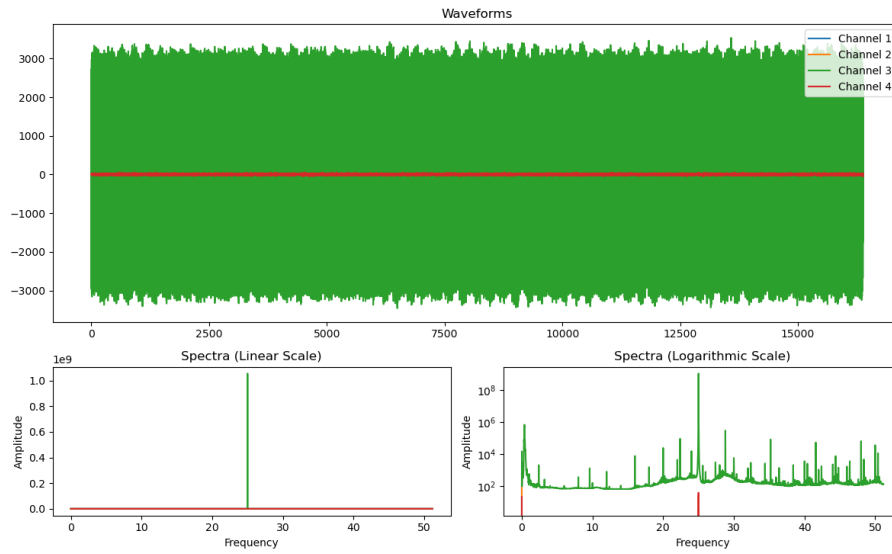
Gain M (22.0 22.0 22.0 22.0)MHz (0.0 0.0 100.0 0.0)mVpp



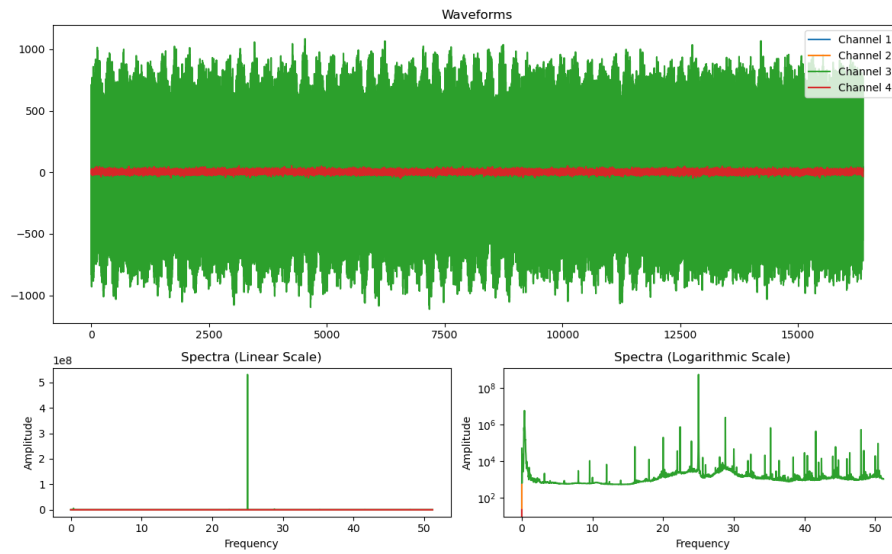
Gain M (22.0 22.0 22.0 22.0)MHz (0.0 0.0 0.0 0.0)mVpp



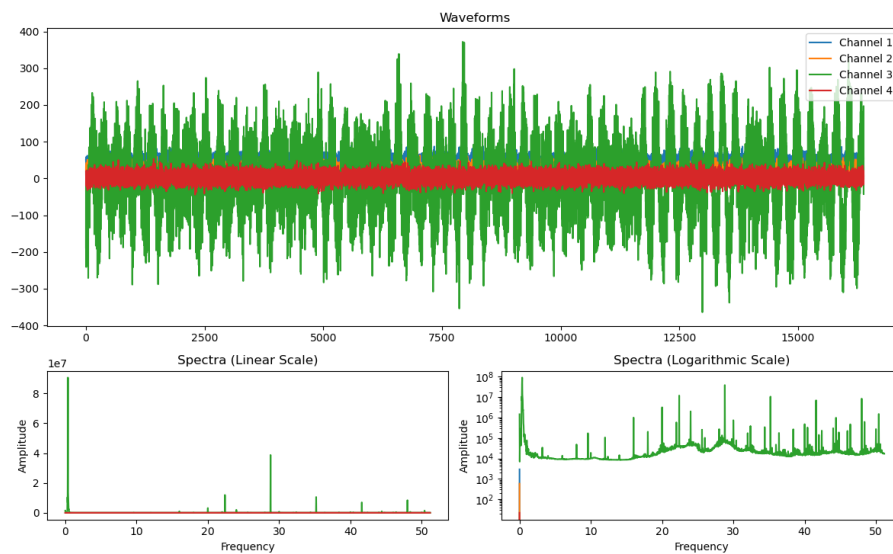
Gain M (25.0 25.0 25.0 25.0)MHz (0.0 0.0 400.0 0.0)mVpp



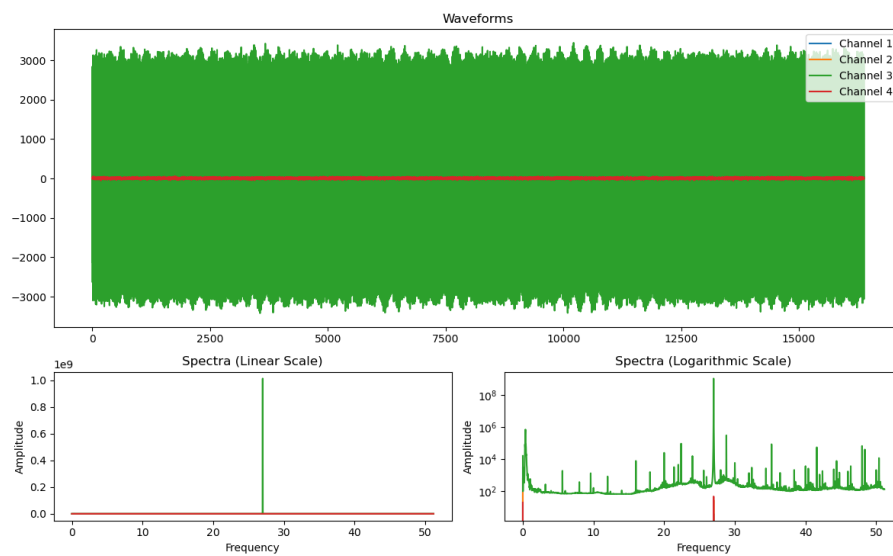
Gain M (25.0 25.0 25.0 25.0)MHz (0.0 0.0 100.0 0.0)mVpp



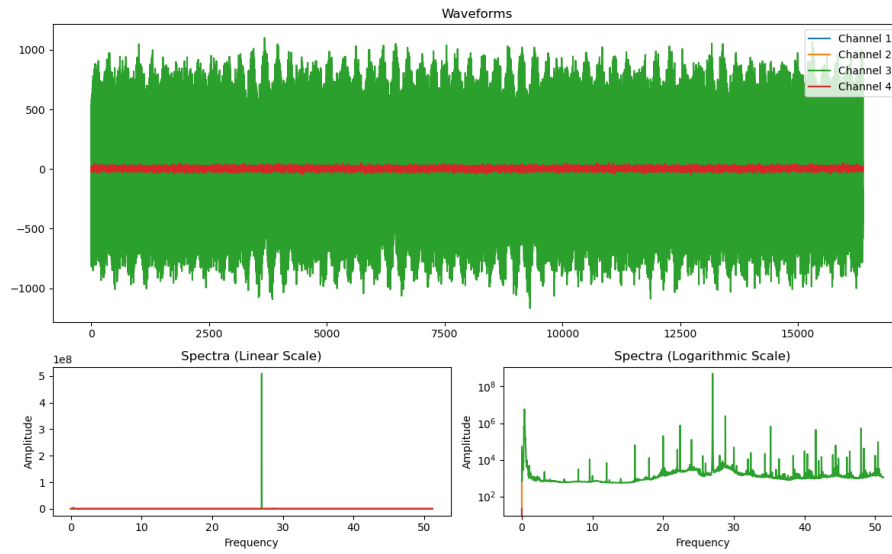
Gain M (25.0 25.0 25.0 25.0)MHz (0.0 0.0 0.0 0.0)mVpp



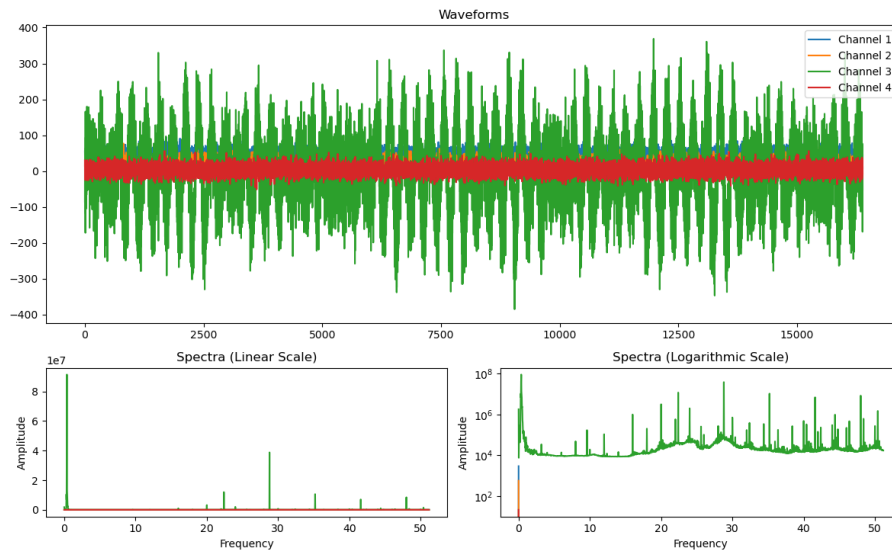
Gain M (27.0 27.0 27.0 27.0)MHz (0.0 0.0 400.0 0.0)mVpp



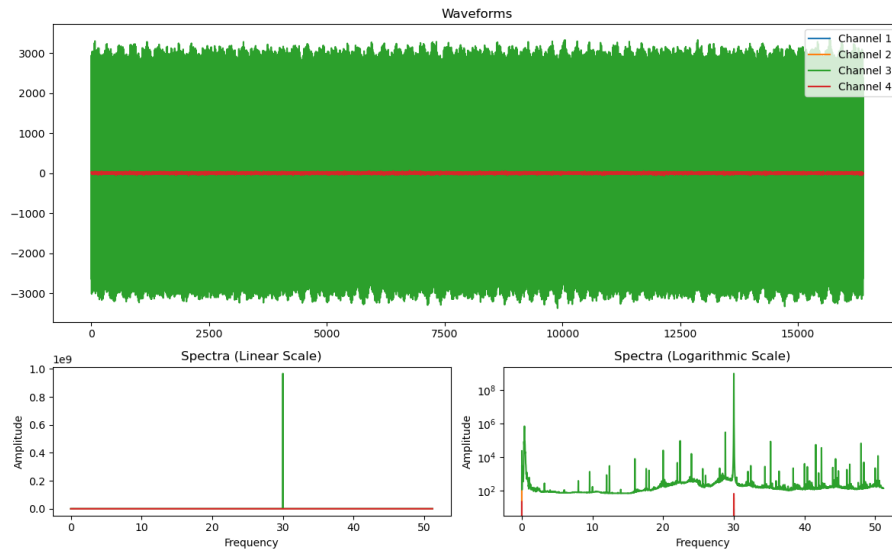
Gain M (27.0 27.0 27.0 27.0)MHz (0.0 0.0 100.0 0.0)mVpp



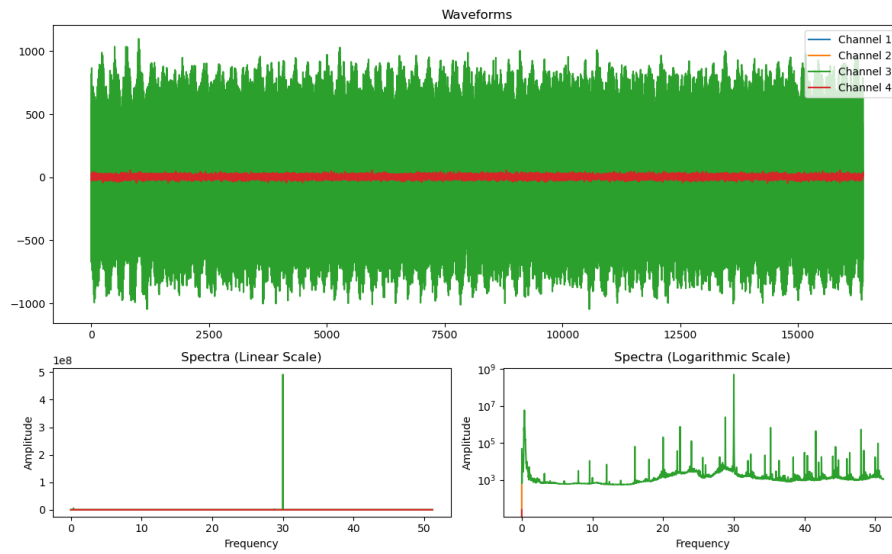
Gain M (27.0 27.0 27.0 27.0)MHz (0.0 0.0 0.0 0.0)mVpp



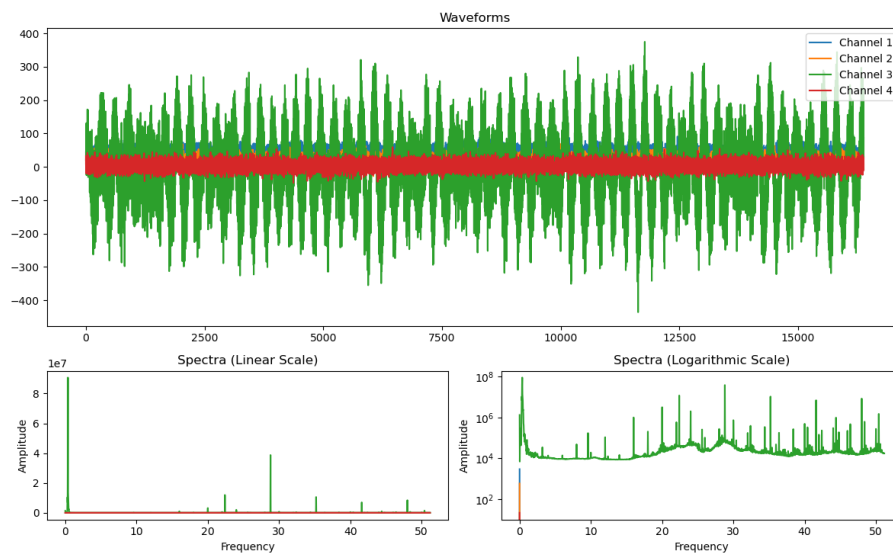
Gain M (30.0 30.0 30.0 30.0)MHz (0.0 0.0 400.0 0.0)mVpp



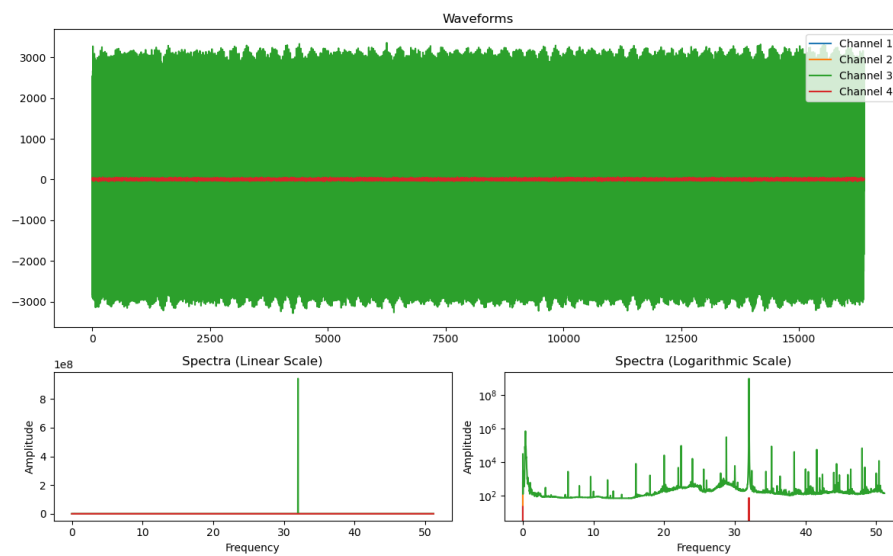
Gain M (30.0 30.0 30.0 30.0)MHz (0.0 0.0 100.0 0.0)mVpp



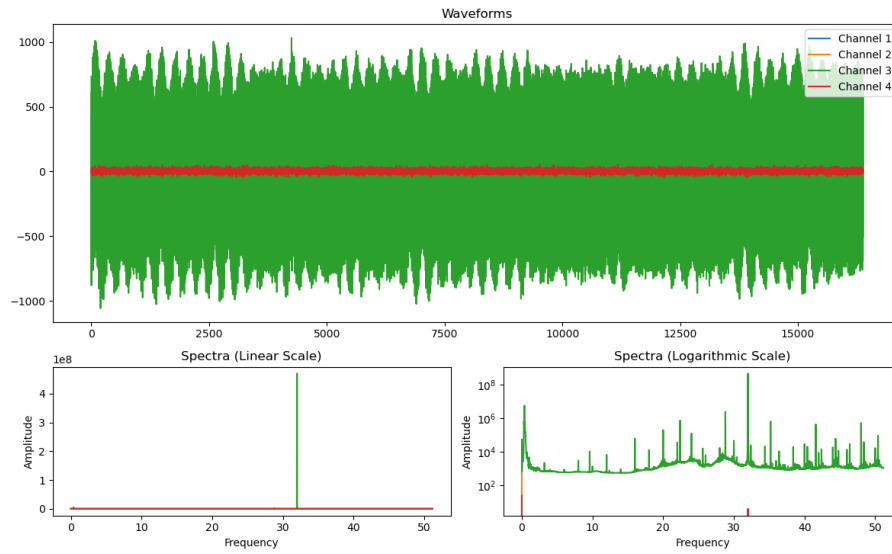
Gain M (30.0 30.0 30.0 30.0)MHz (0.0 0.0 0.0 0.0)mVpp



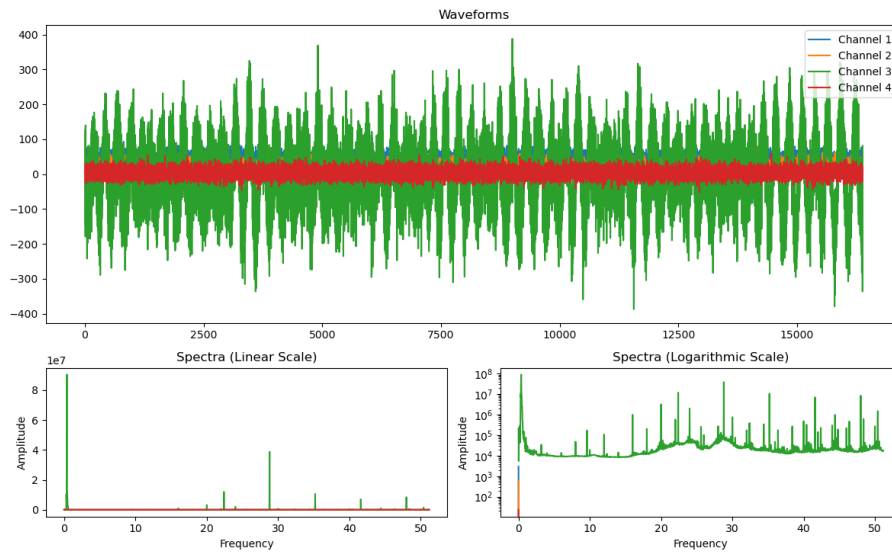
Gain M (32.0 32.0 32.0 32.0)MHz (0.0 0.0 400.0 0.0)mVpp



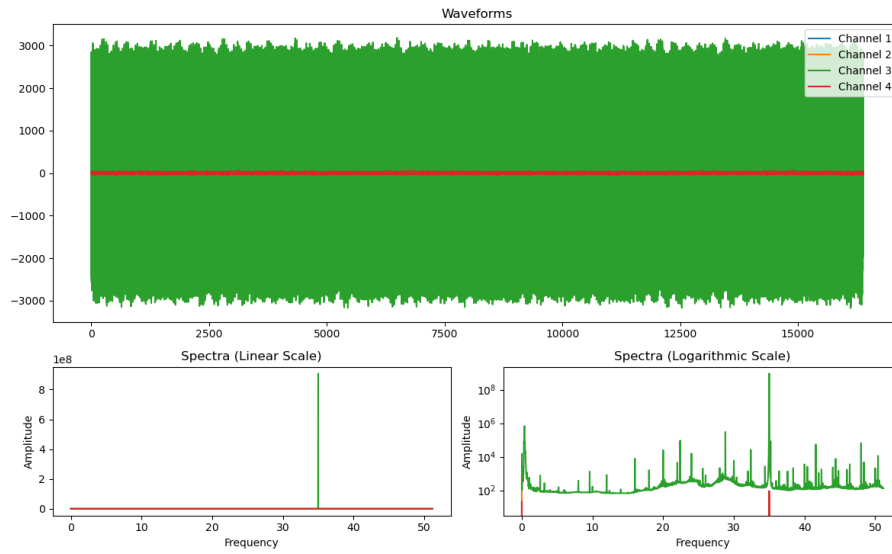
Gain M (32.0 32.0 32.0 32.0)MHz (0.0 0.0 100.0 0.0)mVpp



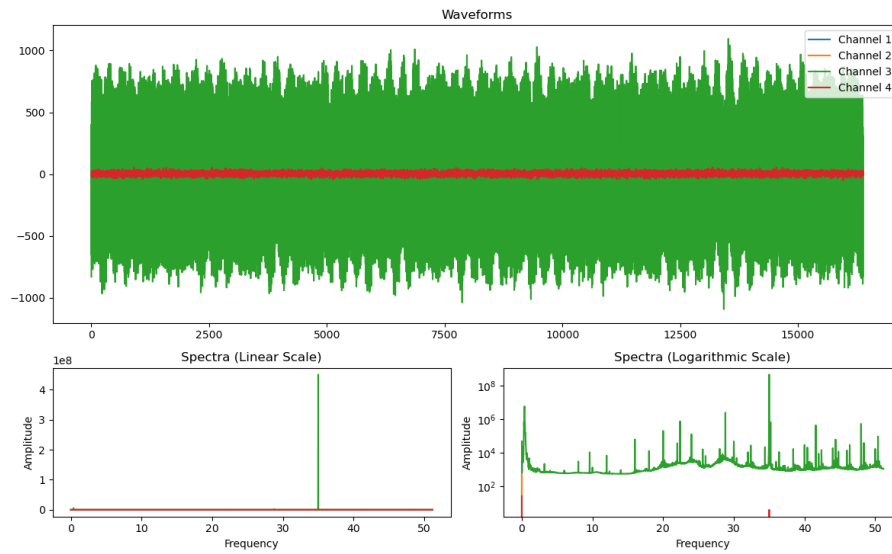
Gain M (32.0 32.0 32.0 32.0)MHz (0.0 0.0 0.0 0.0)mVpp



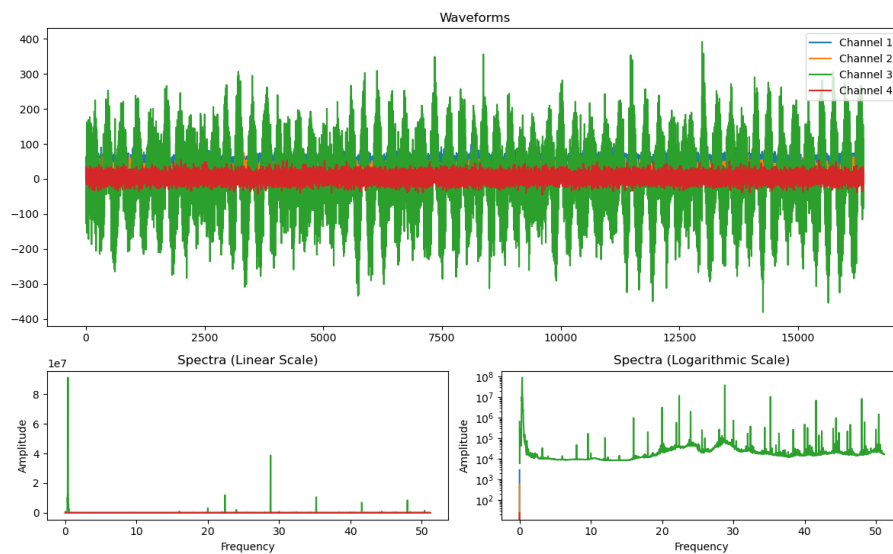
Gain M (35.0 35.0 35.0 35.0)MHz (0.0 0.0 400.0 0.0)mVpp



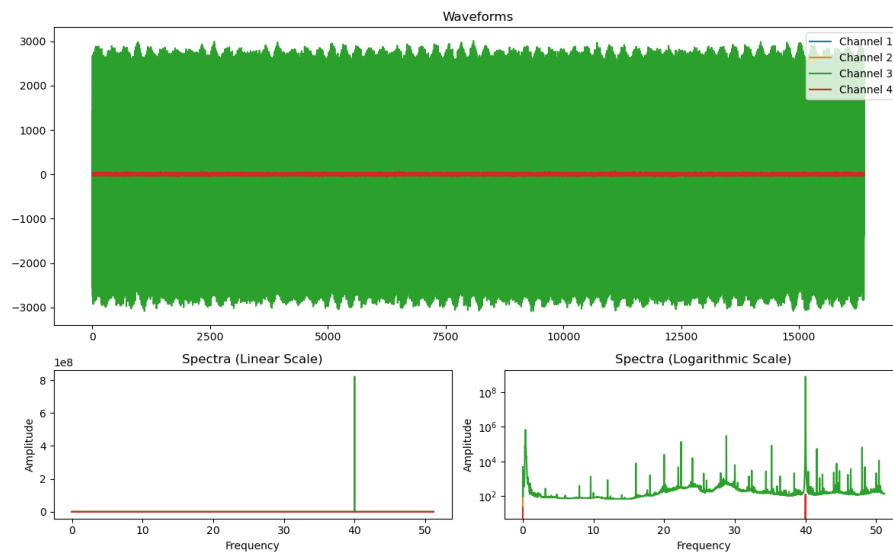
Gain M (35.0 35.0 35.0 35.0)MHz (0.0 0.0 100.0 0.0)mVpp



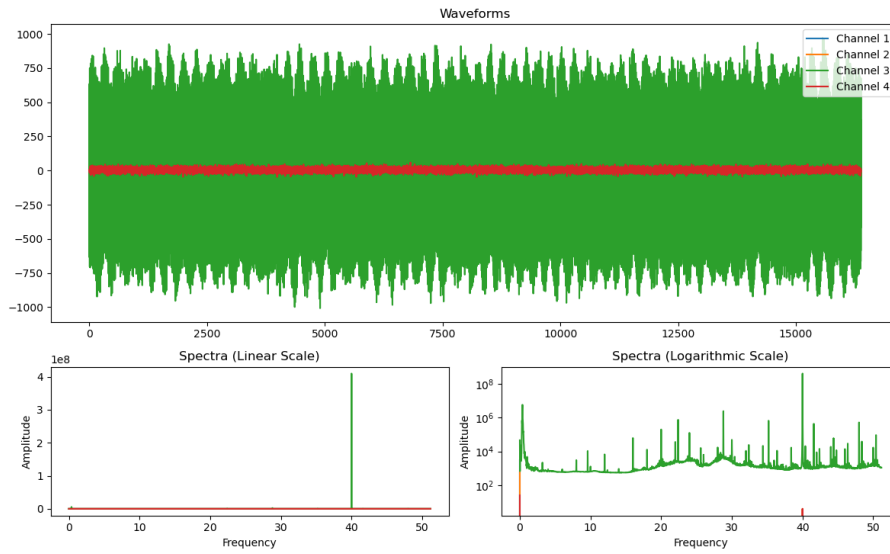
Gain M (35.0 35.0 35.0 35.0)MHz (0.0 0.0 0.0 0.0)mVpp



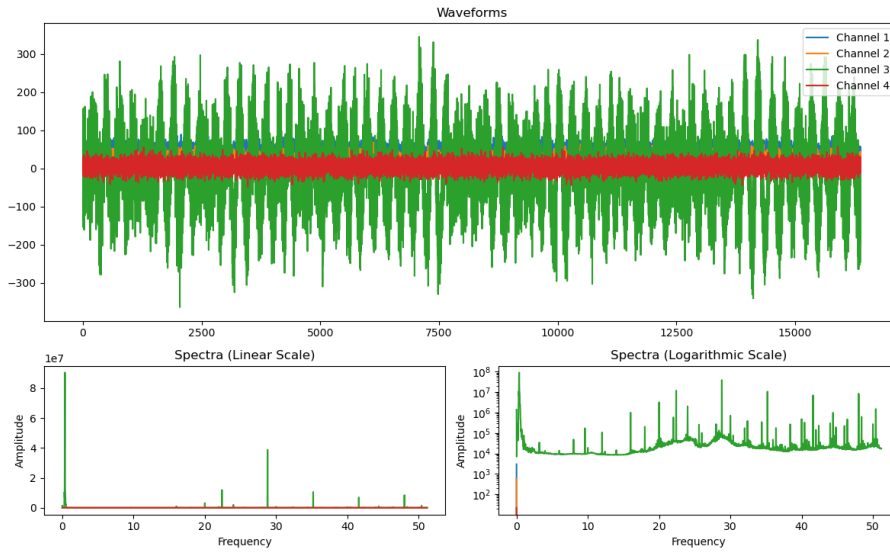
Gain M (40.0 40.0 40.0 40.0)MHz (0.0 0.0 400.0 0.0)mVpp



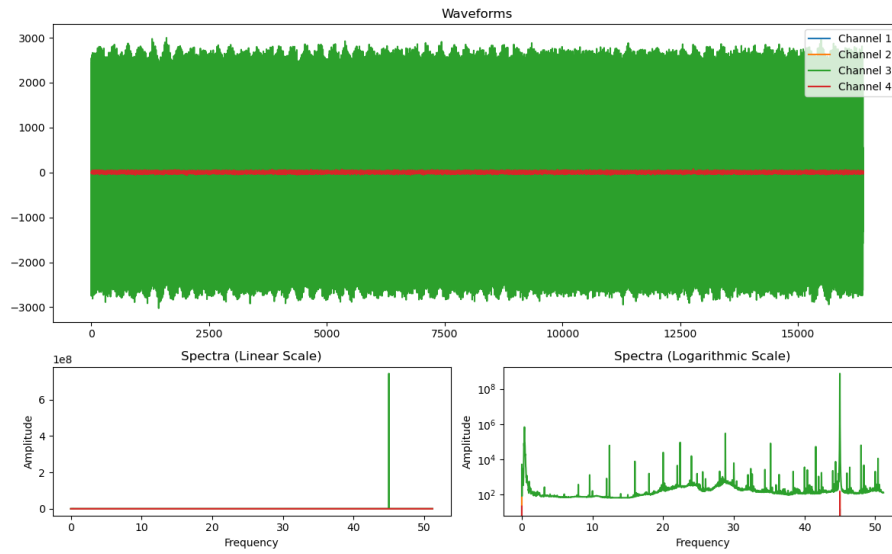
Gain M (40.0 40.0 40.0 40.0)MHz (0.0 0.0 100.0 0.0)mVpp



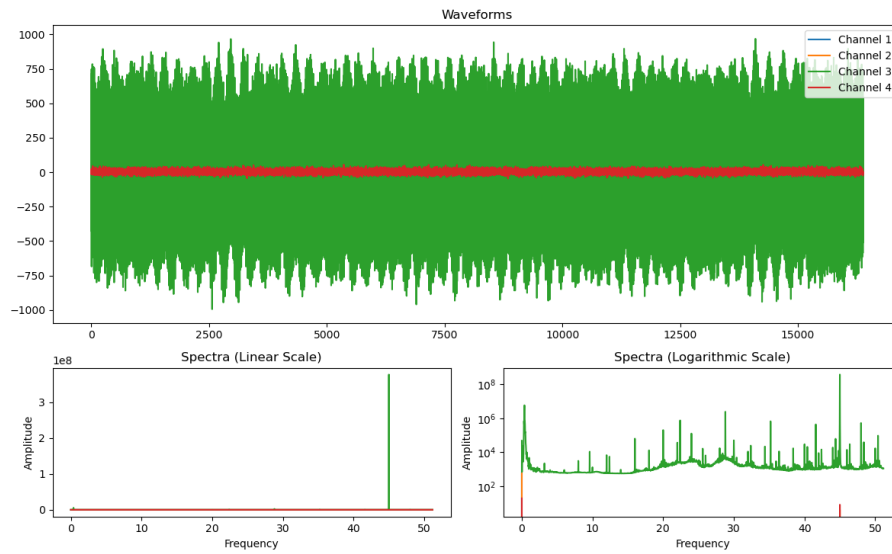
Gain M (40.0 40.0 40.0 40.0)MHz (0.0 0.0 0.0 0.0)mVpp



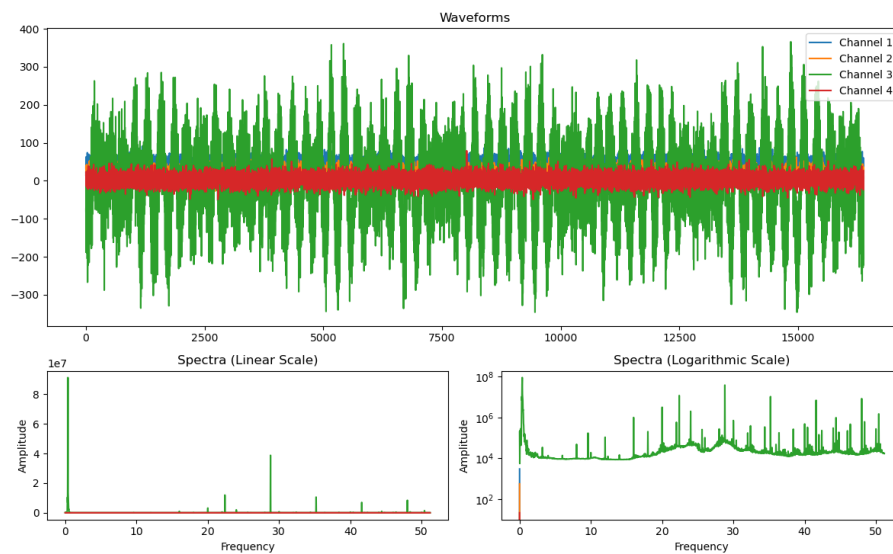
Gain M (45.0 45.0 45.0 45.0)MHz (0.0 0.0 400.0 0.0)mVpp



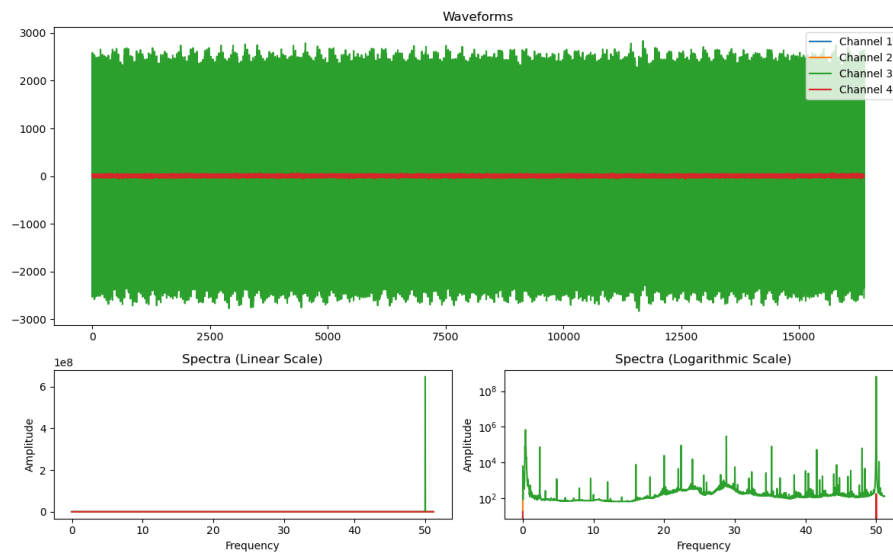
Gain M (45.0 45.0 45.0 45.0)MHz (0.0 0.0 100.0 0.0)mVpp



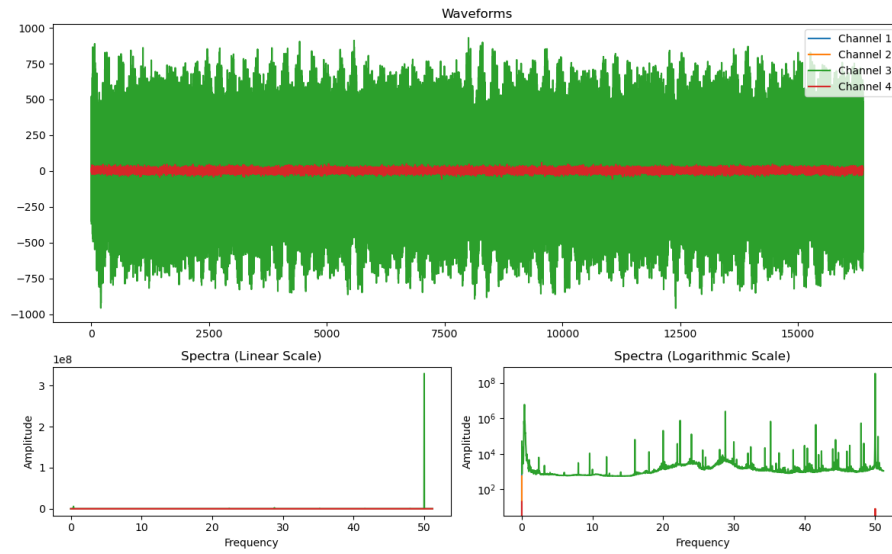
Gain M (45.0 45.0 45.0 45.0)MHz (0.0 0.0 0.0 0.0)mVpp



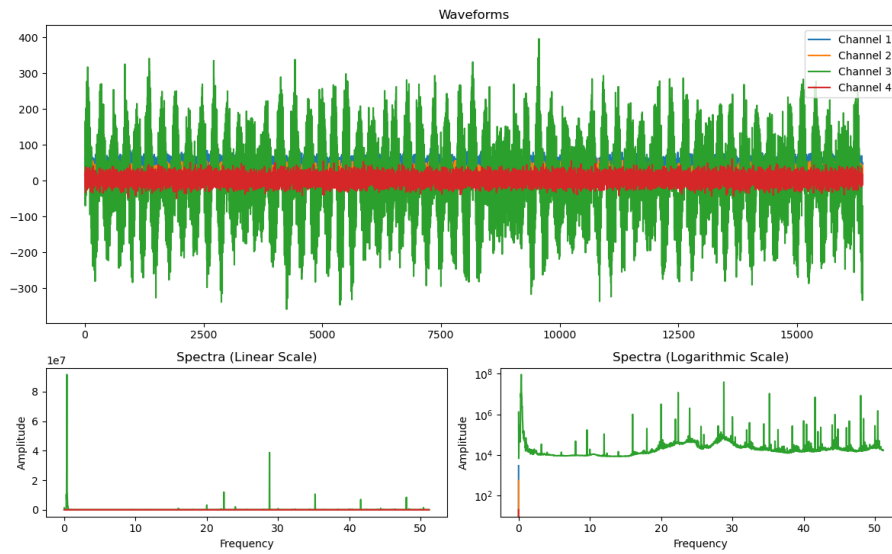
Gain M (50.0 50.0 50.0 50.0)MHz (0.0 0.0 400.0 0.0)mVpp



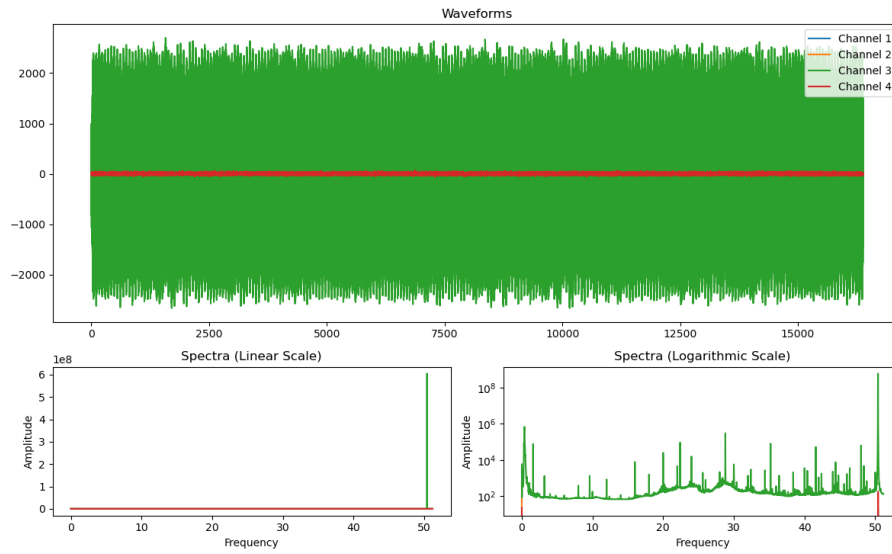
Gain M (50.0 50.0 50.0 50.0)MHz (0.0 0.0 100.0 0.0)mVpp



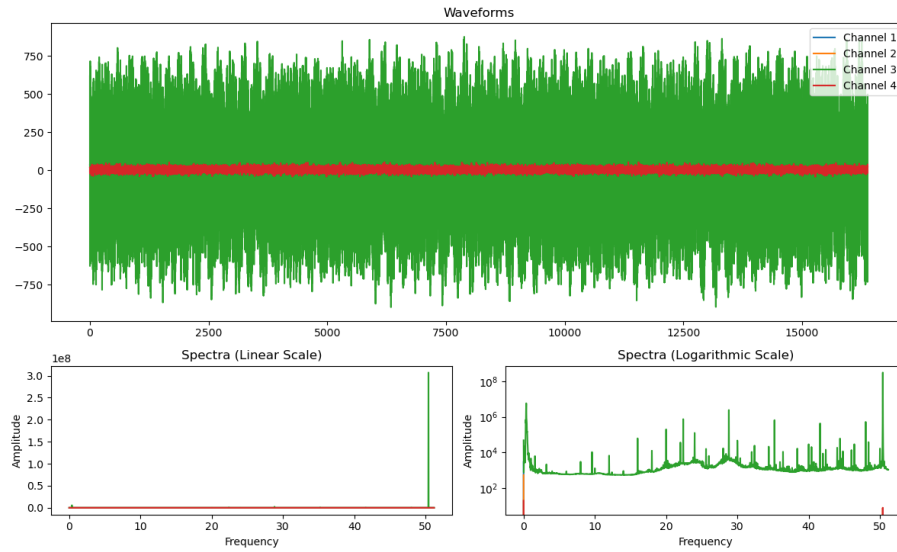
Gain M (50.0 50.0 50.0 50.0)MHz (0.0 0.0 0.0 0.0)mVpp



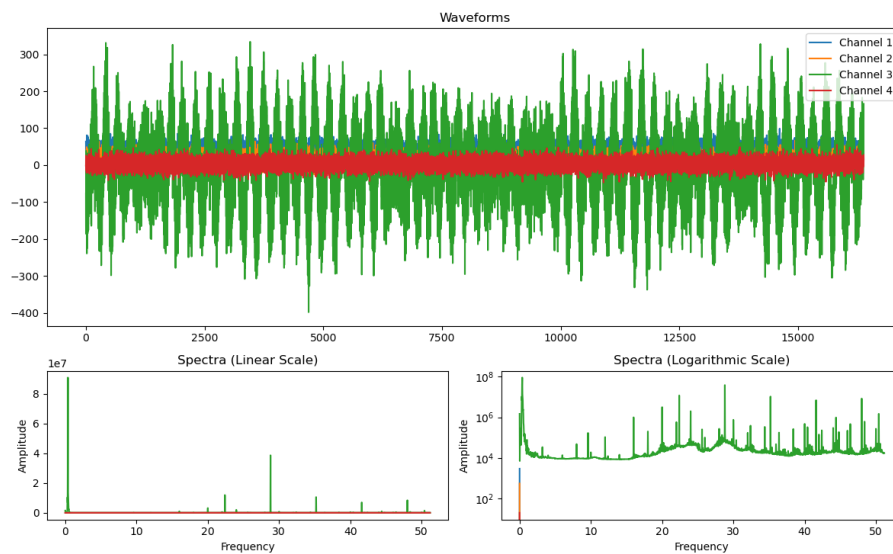
Gain M (52.0 52.0 52.0 52.0)MHz (0.0 0.0 400.0 0.0)mVpp



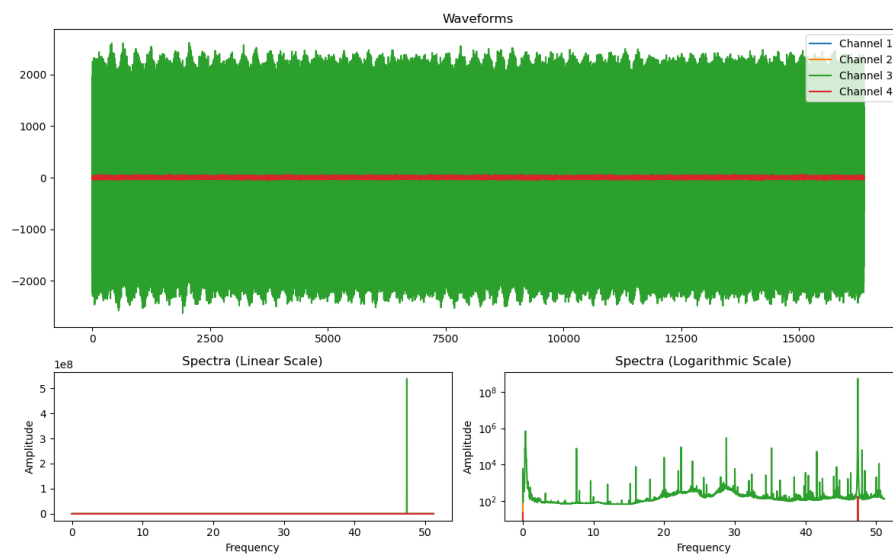
Gain M (52.0 52.0 52.0 52.0)MHz (0.0 0.0 100.0 0.0)mVpp



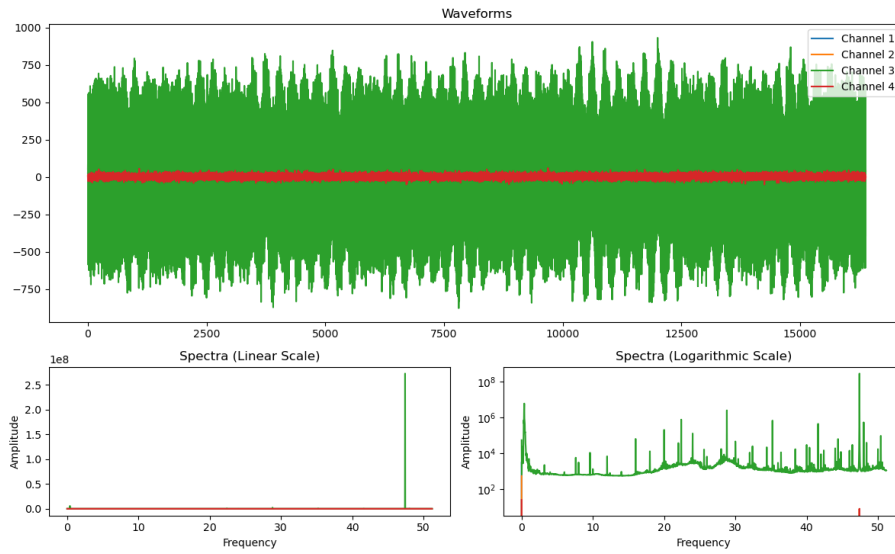
Gain M (52.0 52.0 52.0 52.0)MHz (0.0 0.0 0.0 0.0)mVpp



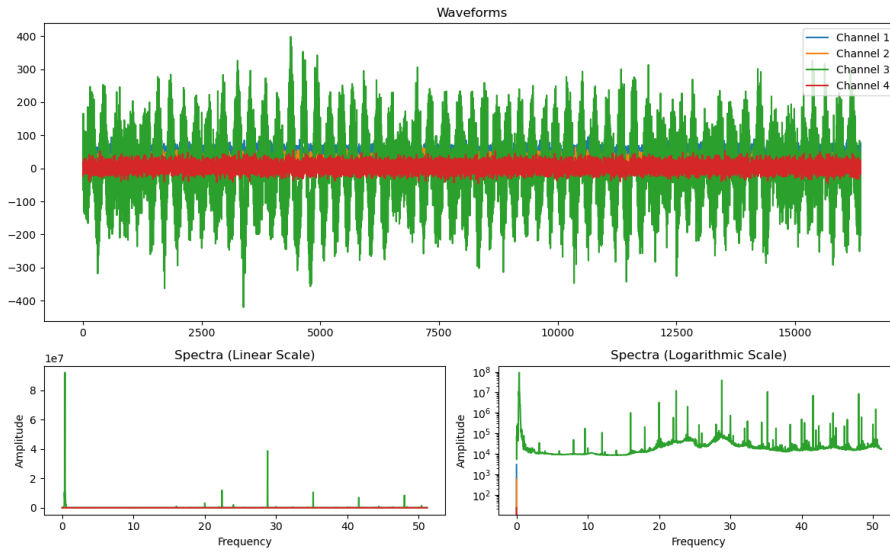
Gain M (55.0 55.0 55.0 55.0)MHz (0.0 0.0 400.0 0.0)mVpp



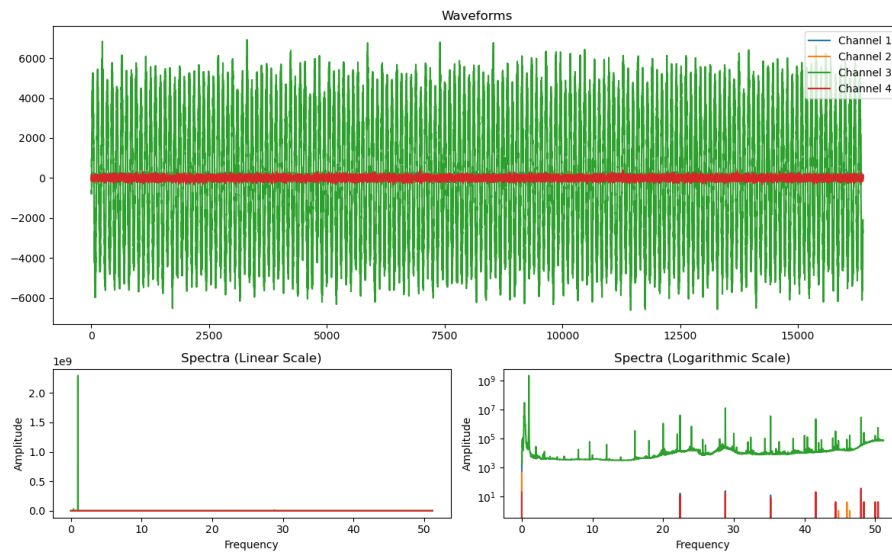
Gain M (55.0 55.0 55.0 55.0)MHz (0.0 0.0 100.0 0.0)mVpp



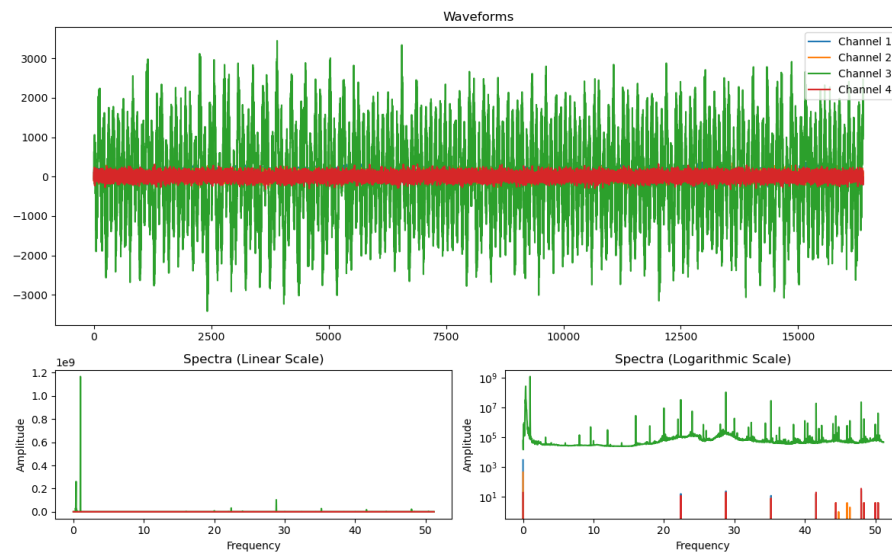
Gain M (55.0 55.0 55.0 55.0)MHz (0.0 0.0 0.0 0.0)mVpp



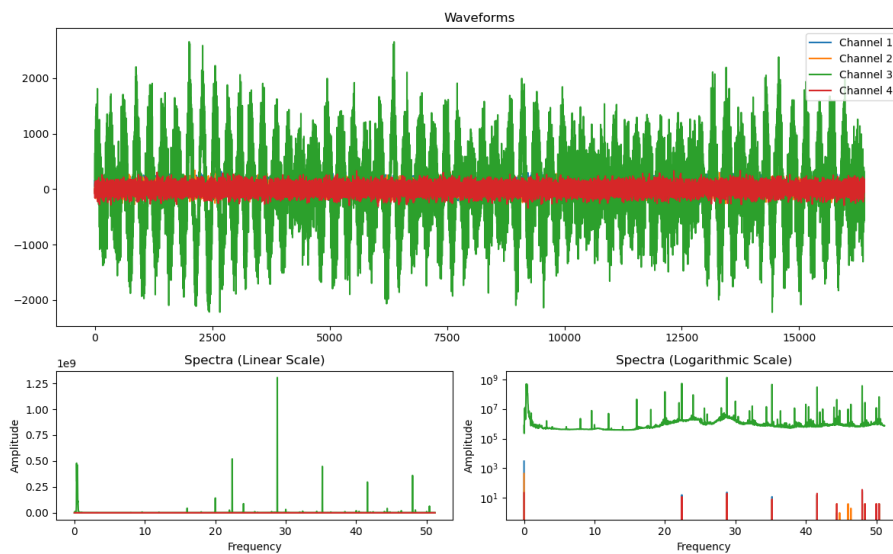
Gain H (1.0 1.0 1.0 1.0)MHz (0.0 0.0 80.0 0.0)mVpp



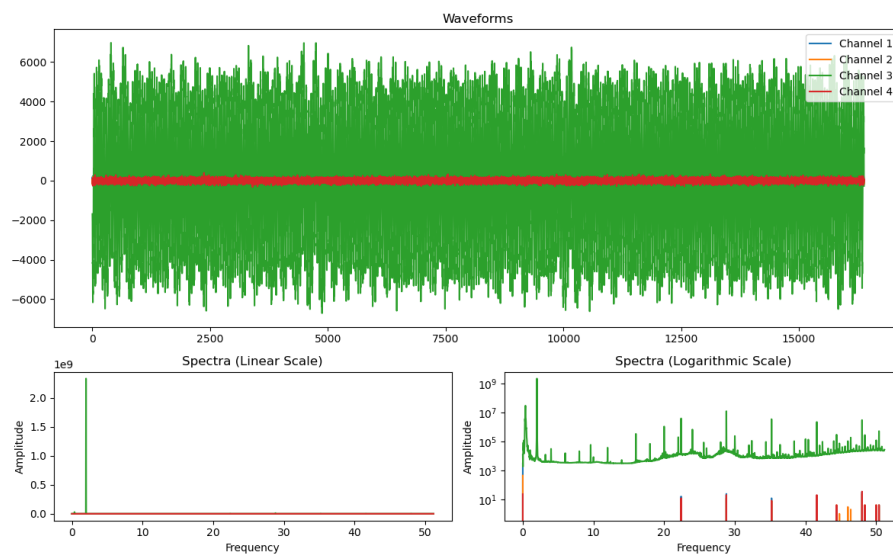
Gain H (1.0 1.0 1.0 1.0)MHz (0.0 0.0 20.0 0.0)mVpp



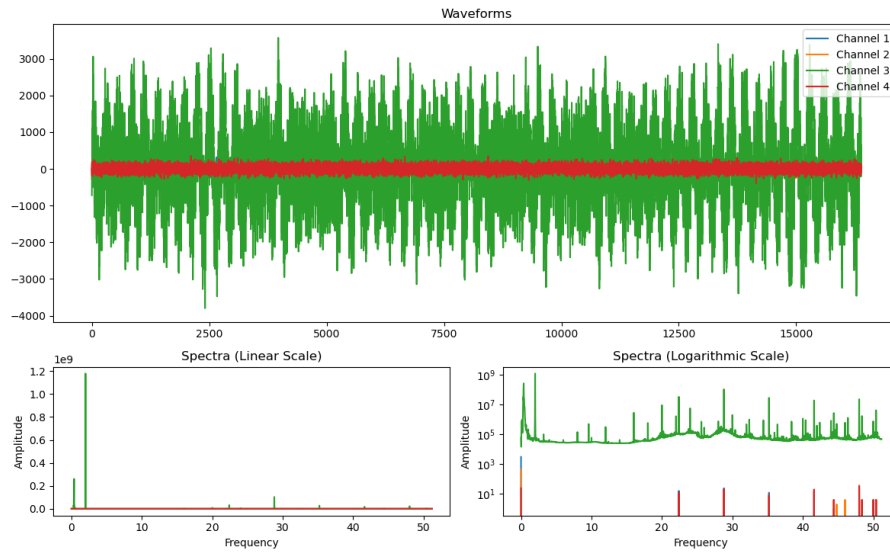
Gain H (1.0 1.0 1.0 1.0)MHz (0.0 0.0 0.0 0.0)mVpp



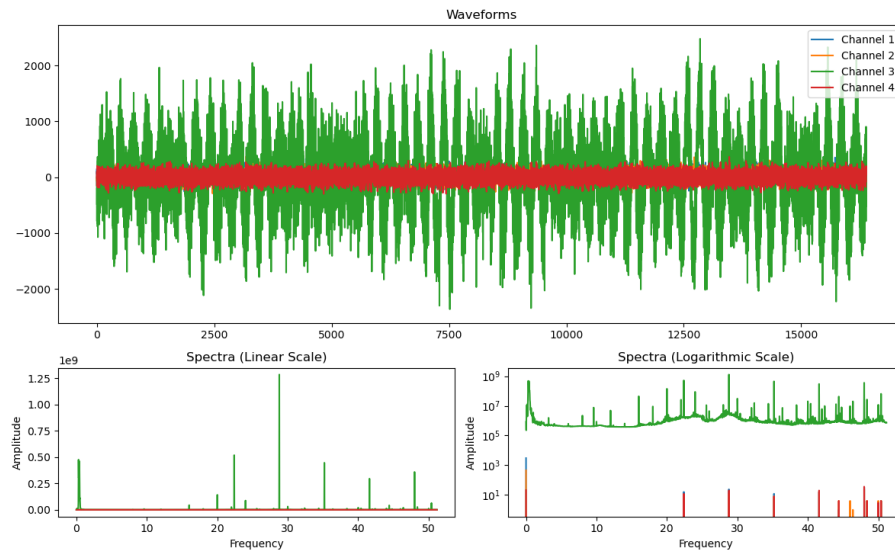
Gain H (2.0 2.0 2.0 2.0)MHz (0.0 0.0 80.0 0.0)mVpp



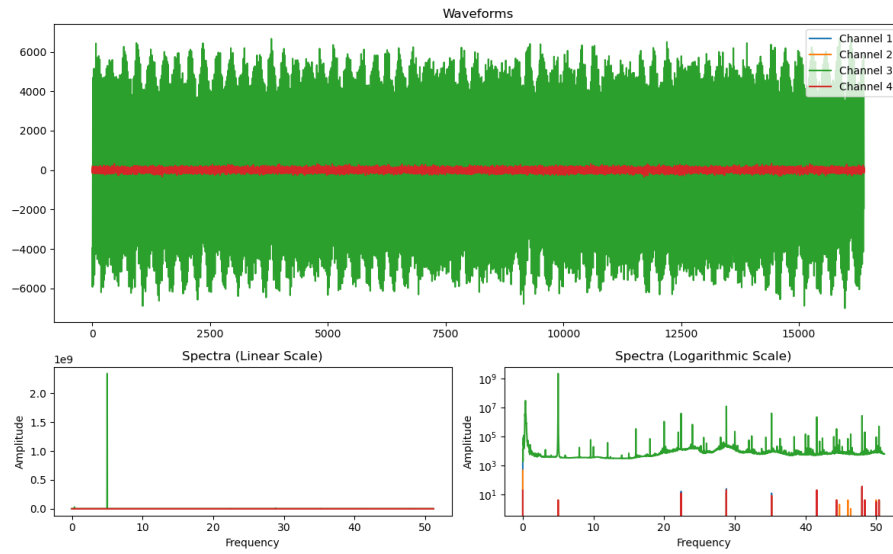
Gain H (2.0 2.0 2.0 2.0)MHz (0.0 0.0 20.0 0.0)mVpp



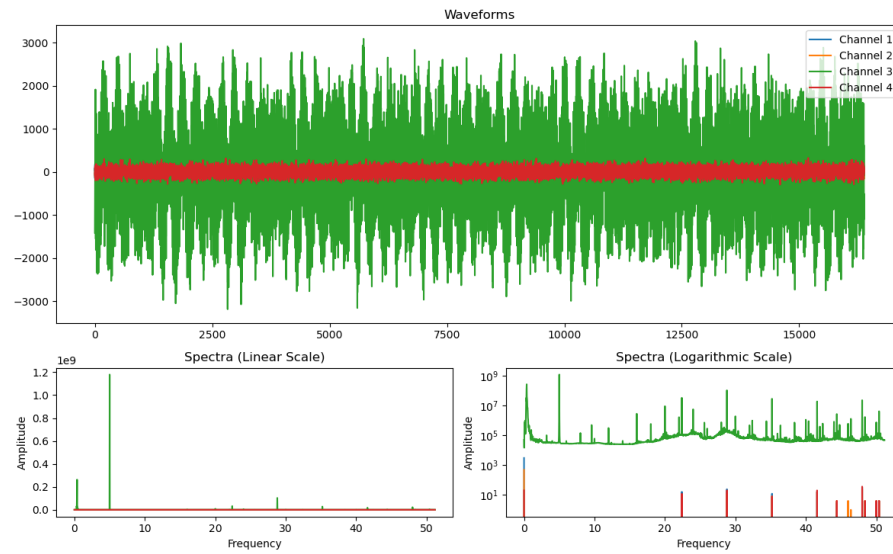
Gain H (2.0 2.0 2.0 2.0)MHz (0.0 0.0 0.0 0.0)mVpp



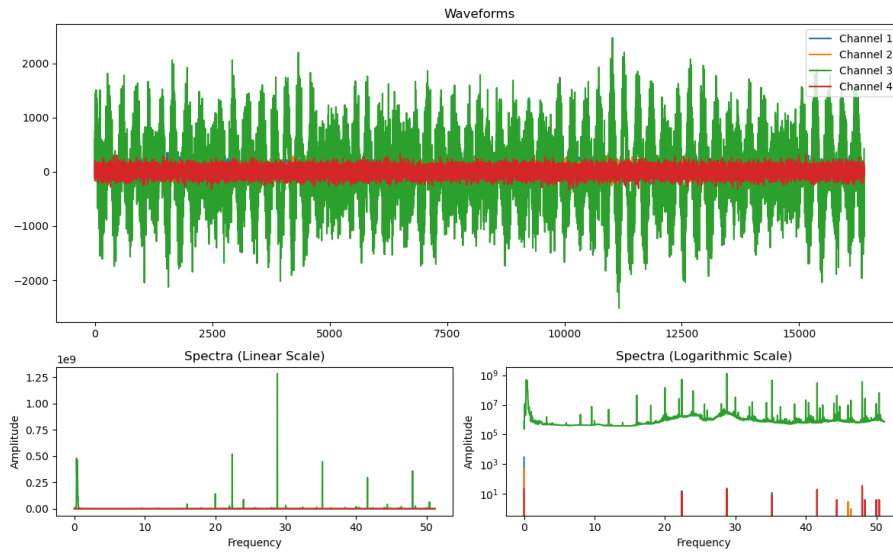
Gain H (5.0 5.0 5.0 5.0)MHz (0.0 0.0 80.0 0.0)mVpp



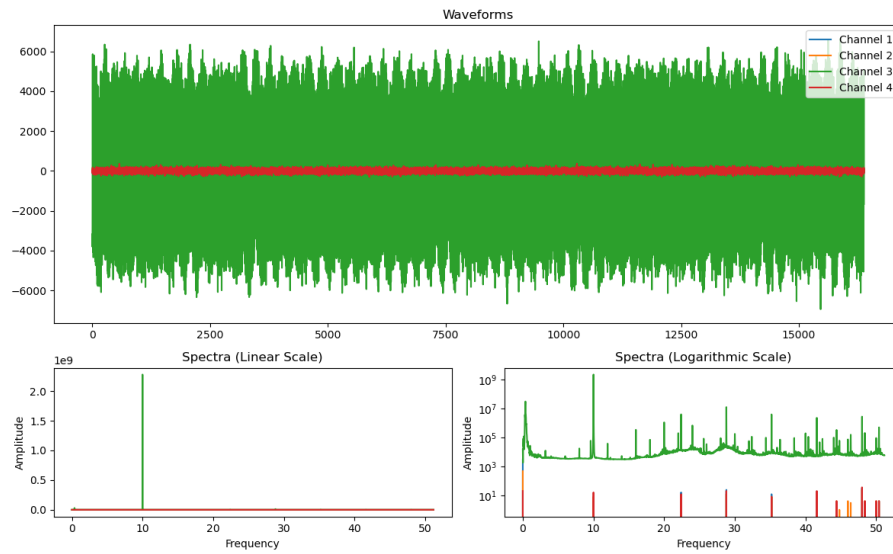
Gain H (5.0 5.0 5.0 5.0)MHz (0.0 0.0 20.0 0.0)mVpp



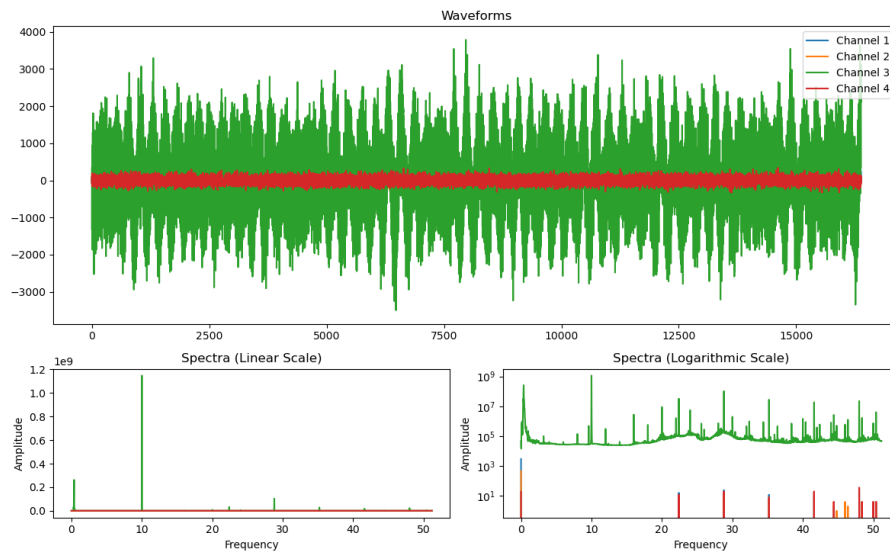
Gain H (5.0 5.0 5.0 5.0)MHz (0.0 0.0 0.0 0.0)mVpp



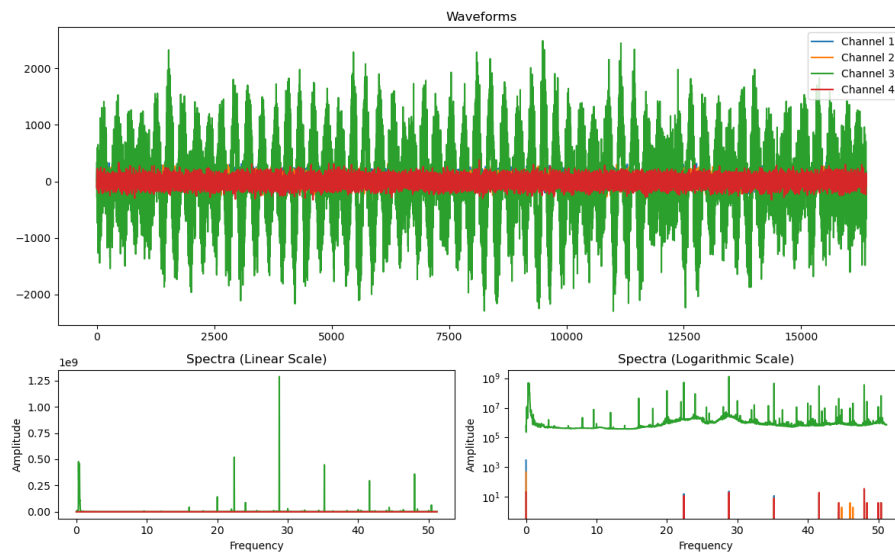
Gain H (10.0 10.0 10.0 10.0)MHz (0.0 0.0 80.0 0.0)mVpp



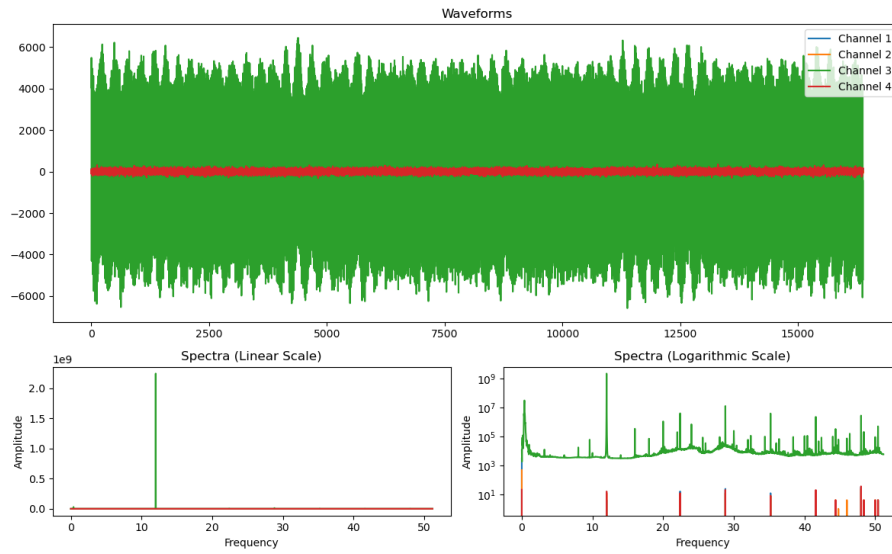
Gain H (10.0 10.0 10.0 10.0)MHz (0.0 0.0 20.0 0.0)mVpp



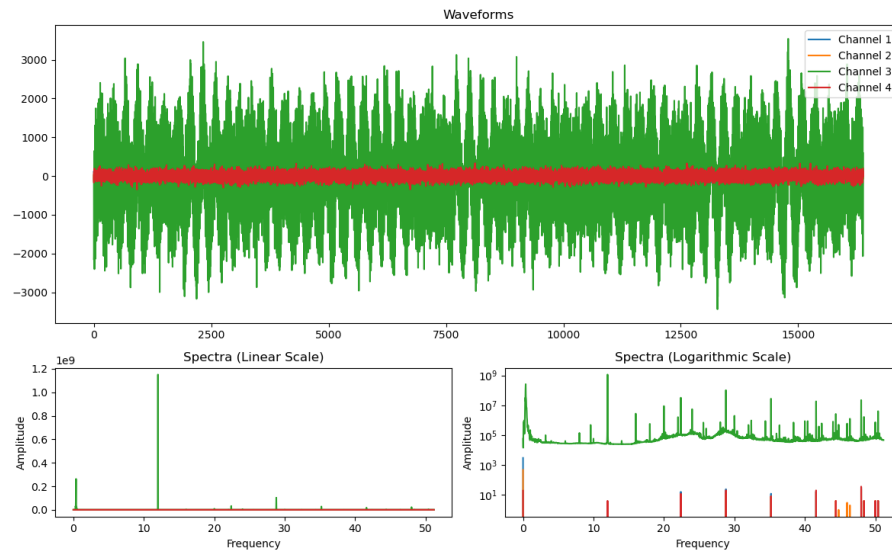
Gain H (10.0 10.0 10.0 10.0)MHz (0.0 0.0 0.0 0.0)mVpp



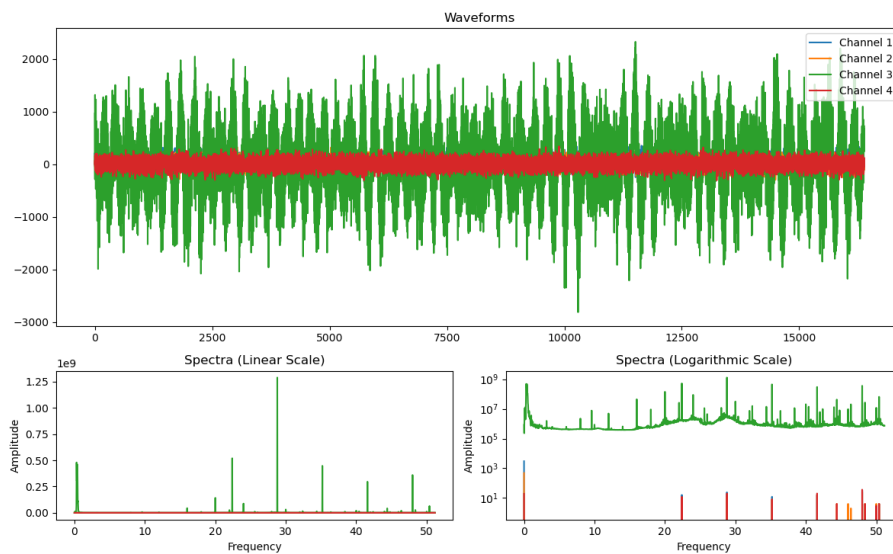
Gain H (12.0 12.0 12.0 12.0)MHz (0.0 0.0 80.0 0.0)mVpp



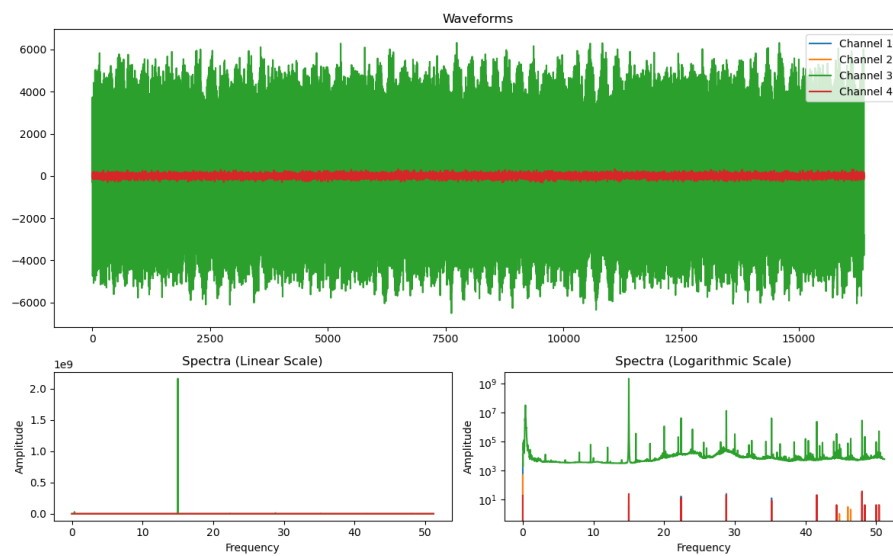
Gain H (12.0 12.0 12.0 12.0)MHz (0.0 0.0 20.0 0.0)mVpp



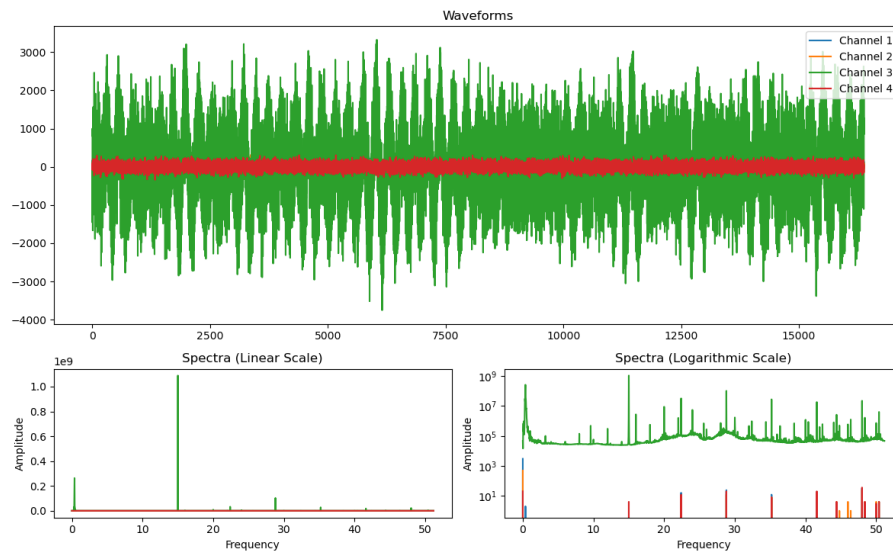
Gain H (12.0 12.0 12.0 12.0)MHz (0.0 0.0 0.0 0.0)mVpp



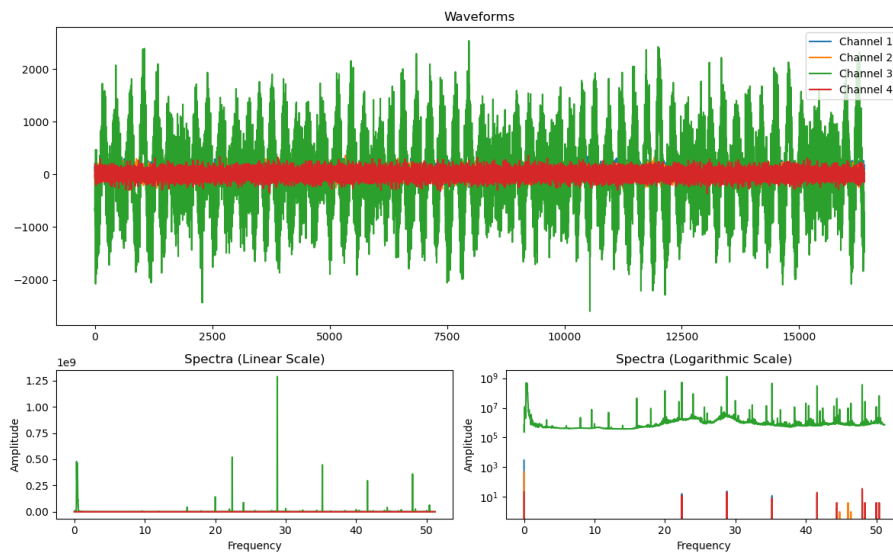
Gain H (15.0 15.0 15.0 15.0)MHz (0.0 0.0 80.0 0.0)mVpp



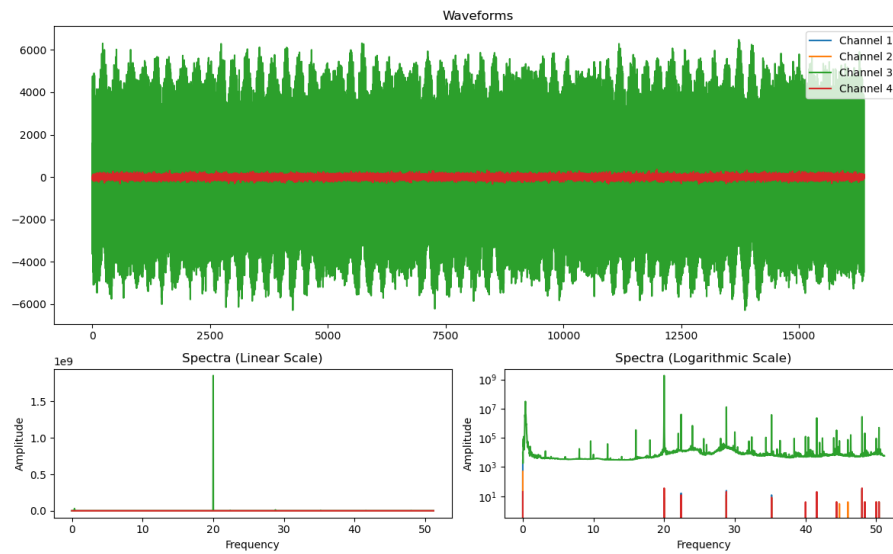
Gain H (15.0 15.0 15.0 15.0)MHz (0.0 0.0 20.0 0.0)mVpp



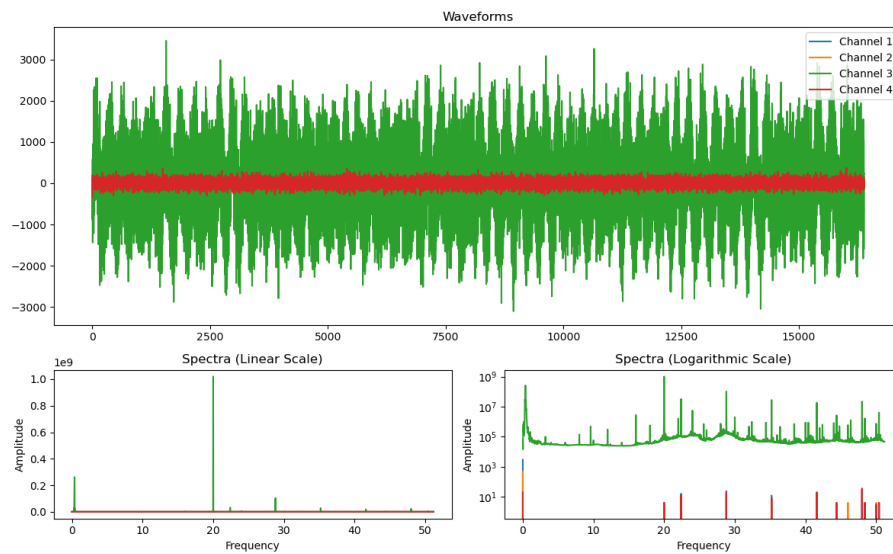
Gain H (15.0 15.0 15.0 15.0)MHz (0.0 0.0 0.0 0.0)mVpp



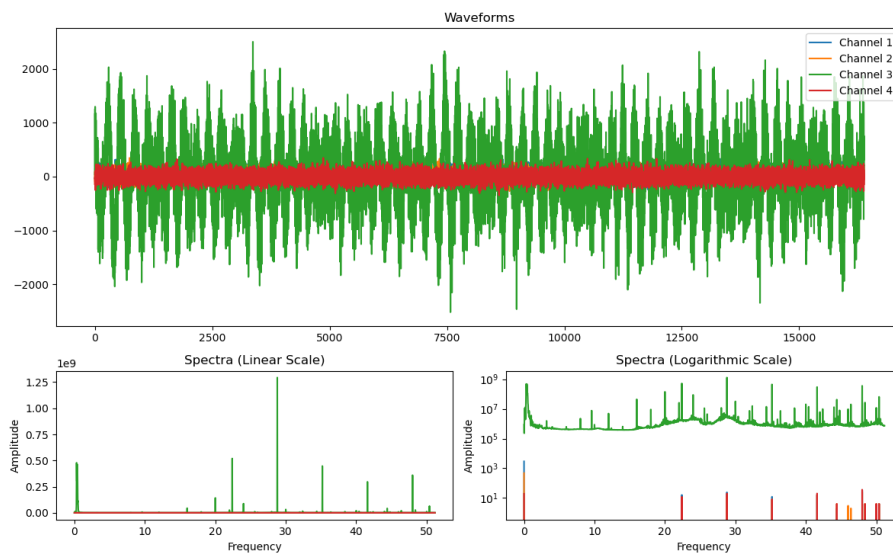
Gain H (20.0 20.0 20.0 20.0)MHz (0.0 0.0 80.0 0.0)mVpp



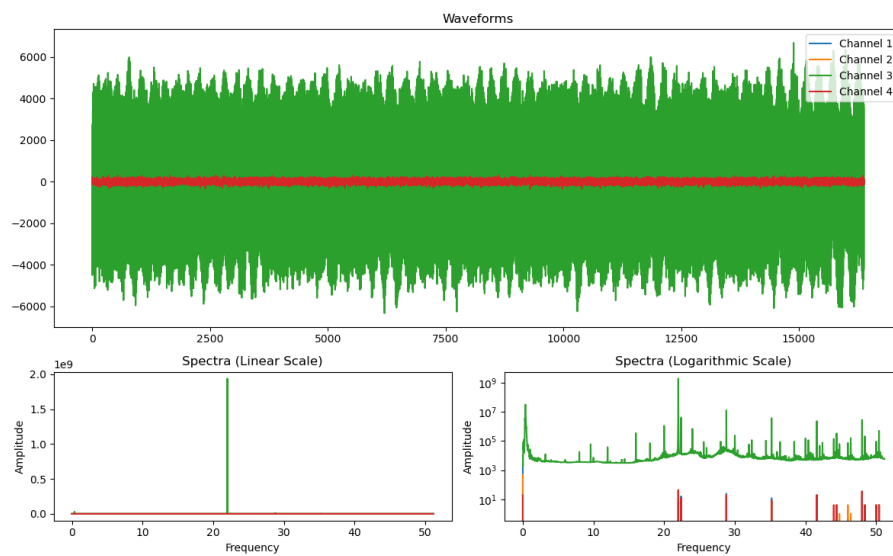
Gain H (20.0 20.0 20.0 20.0)MHz (0.0 0.0 20.0 0.0)mVpp



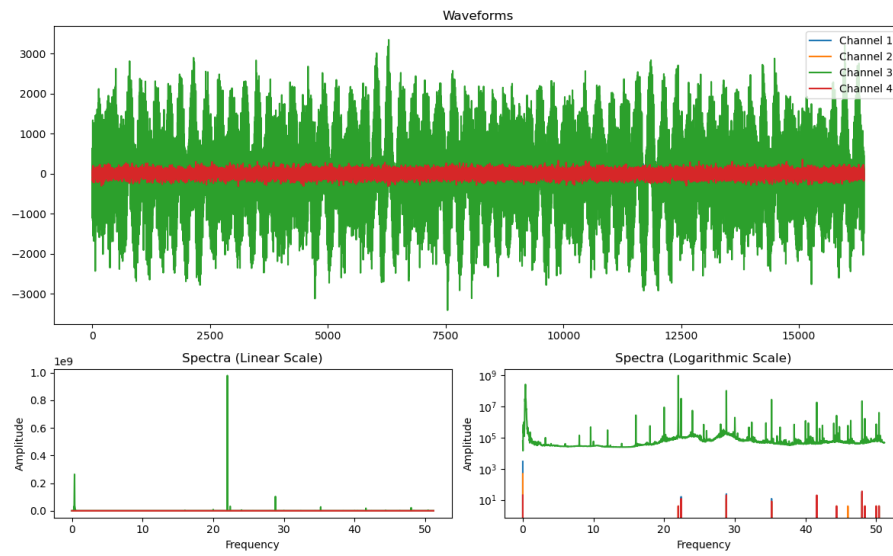
Gain H (20.0 20.0 20.0 20.0)MHz (0.0 0.0 0.0 0.0)mVpp



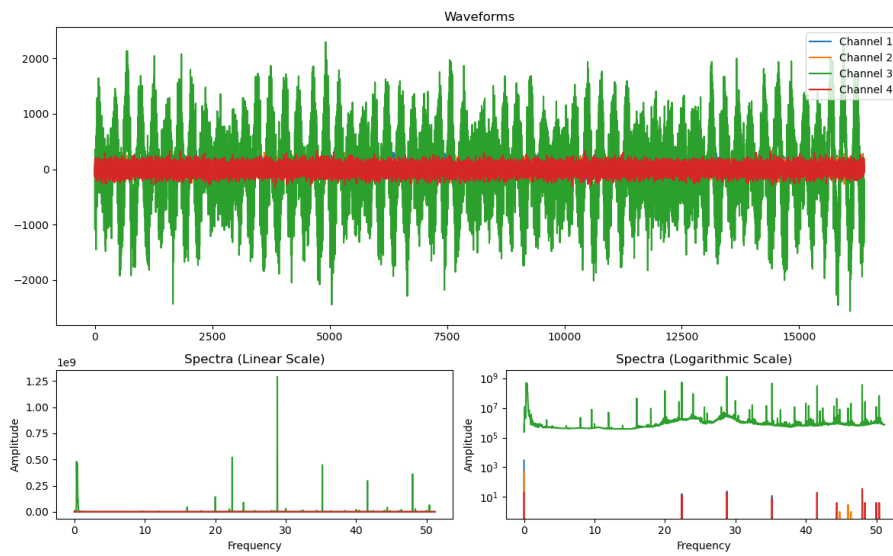
Gain H (22.0 22.0 22.0 22.0)MHz (0.0 0.0 80.0 0.0)mVpp



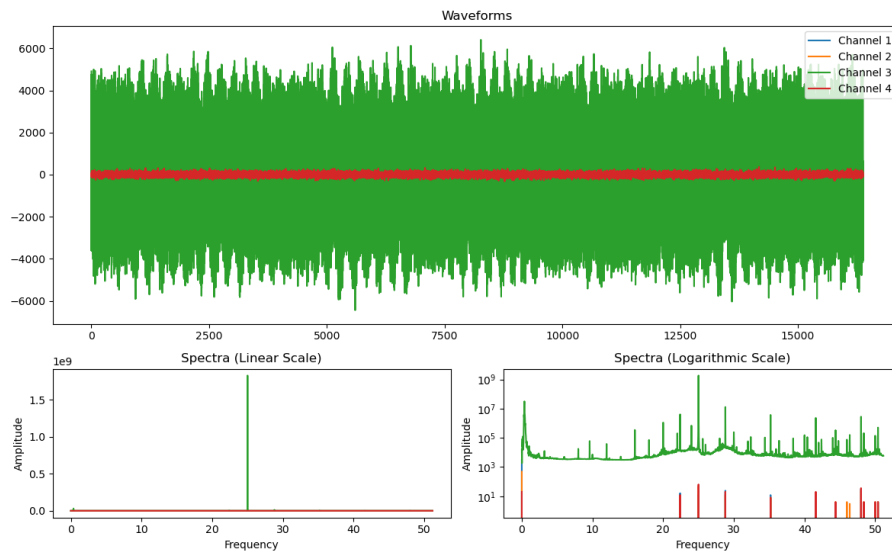
Gain H (22.0 22.0 22.0 22.0)MHz (0.0 0.0 20.0 0.0)mVpp



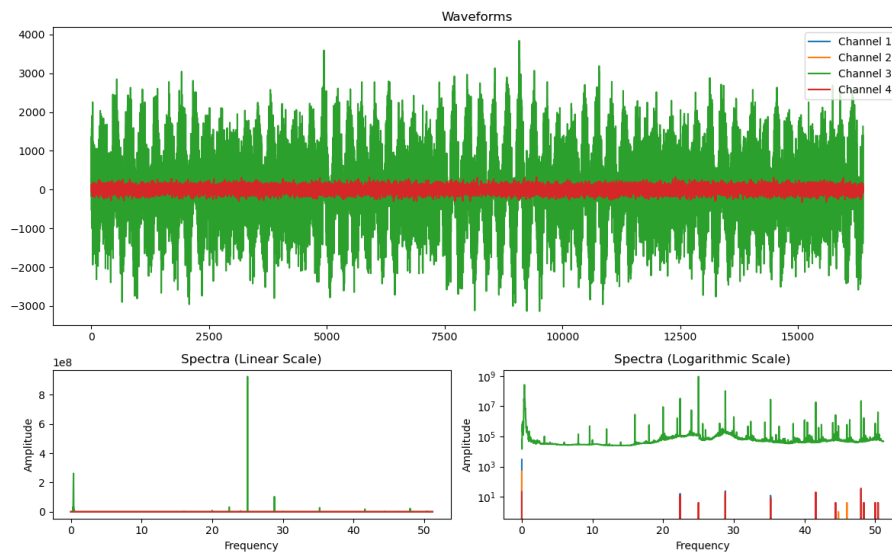
Gain H (22.0 22.0 22.0 22.0)MHz (0.0 0.0 0.0 0.0)mVpp



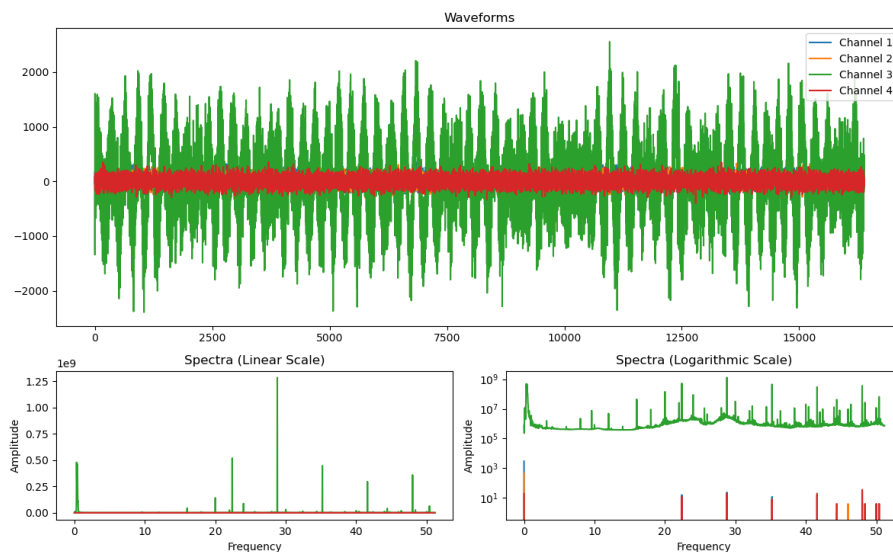
Gain H (25.0 25.0 25.0 25.0)MHz (0.0 0.0 80.0 0.0)mVpp



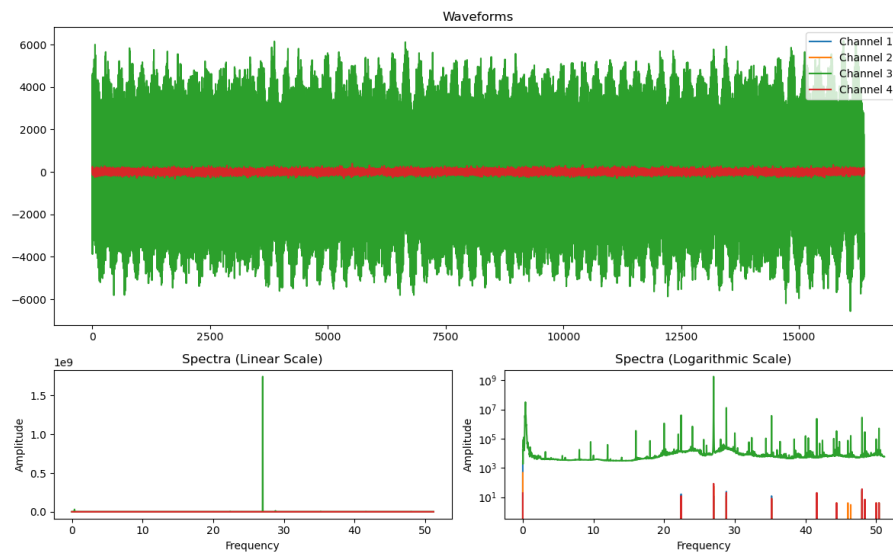
Gain H (25.0 25.0 25.0 25.0)MHz (0.0 0.0 20.0 0.0)mVpp



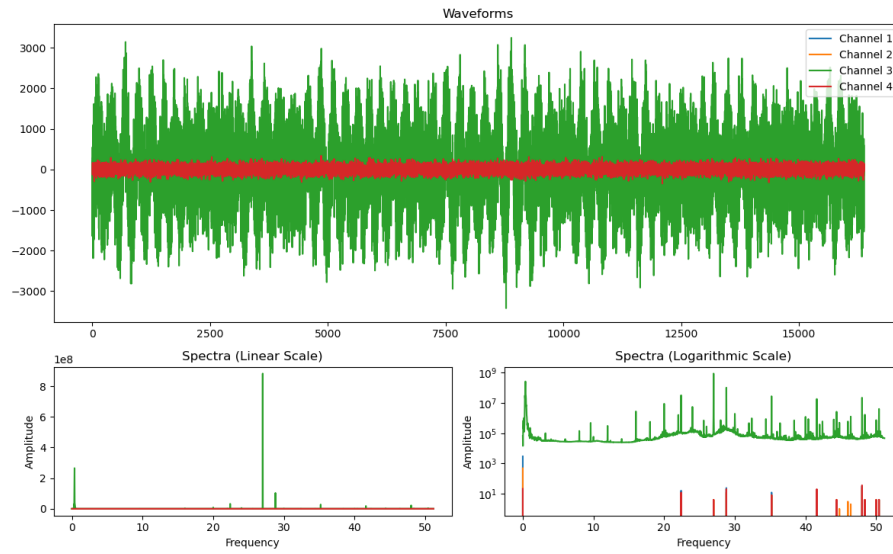
Gain H (25.0 25.0 25.0 25.0)MHz (0.0 0.0 0.0 0.0)mVpp



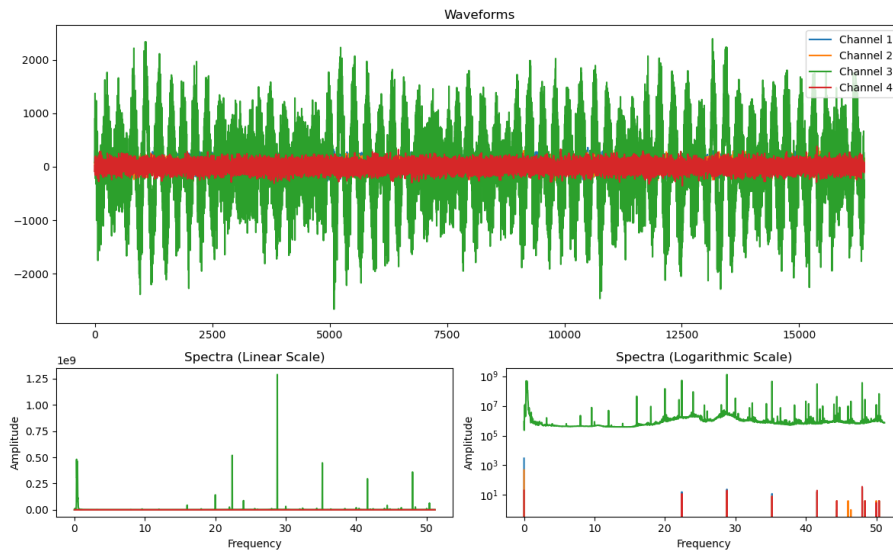
Gain H (27.0 27.0 27.0 27.0)MHz (0.0 0.0 80.0 0.0)mVpp



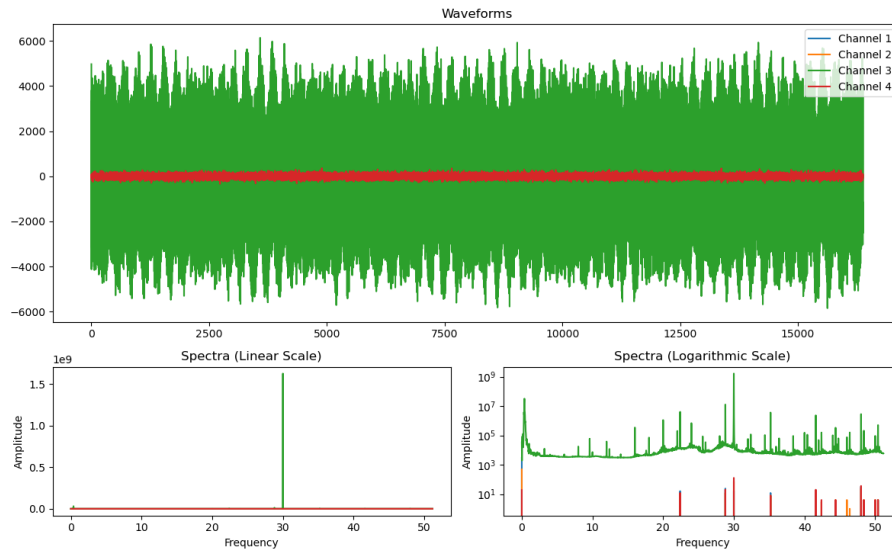
Gain H (27.0 27.0 27.0 27.0)MHz (0.0 0.0 20.0 0.0)mVpp



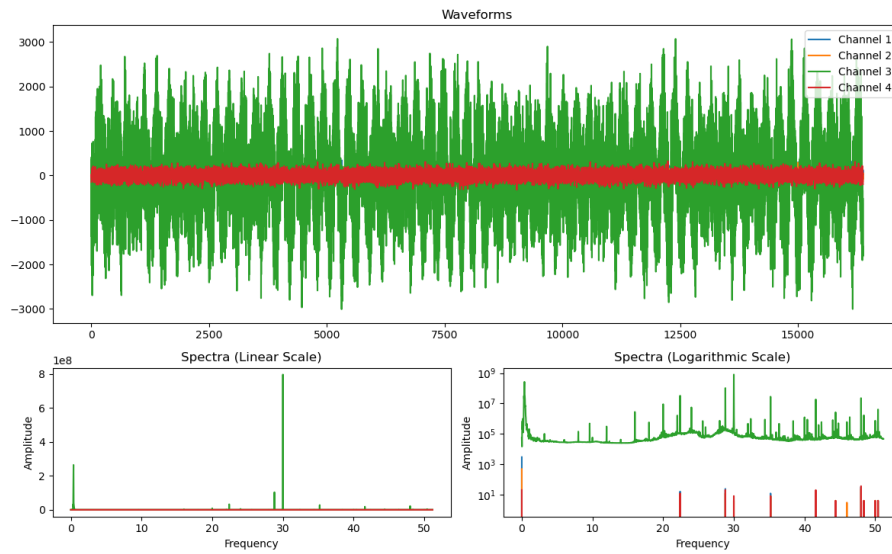
Gain H (27.0 27.0 27.0 27.0)MHz (0.0 0.0 0.0 0.0)mVpp



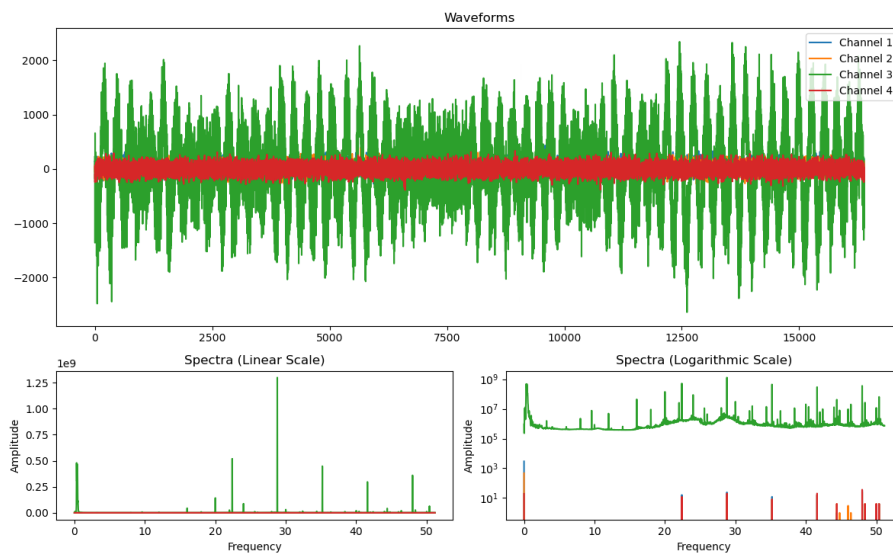
Gain H (30.0 30.0 30.0 30.0)MHz (0.0 0.0 80.0 0.0)mVpp



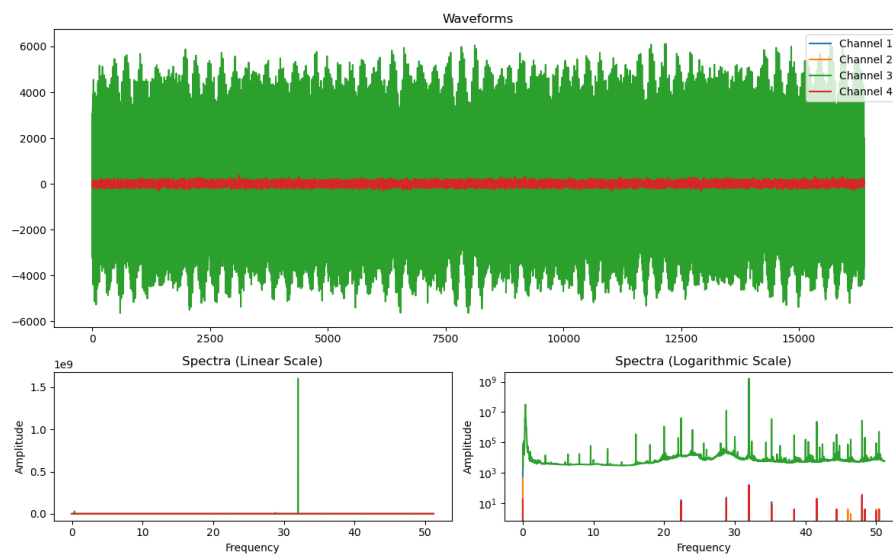
Gain H (30.0 30.0 30.0 30.0)MHz (0.0 0.0 20.0 0.0)mVpp



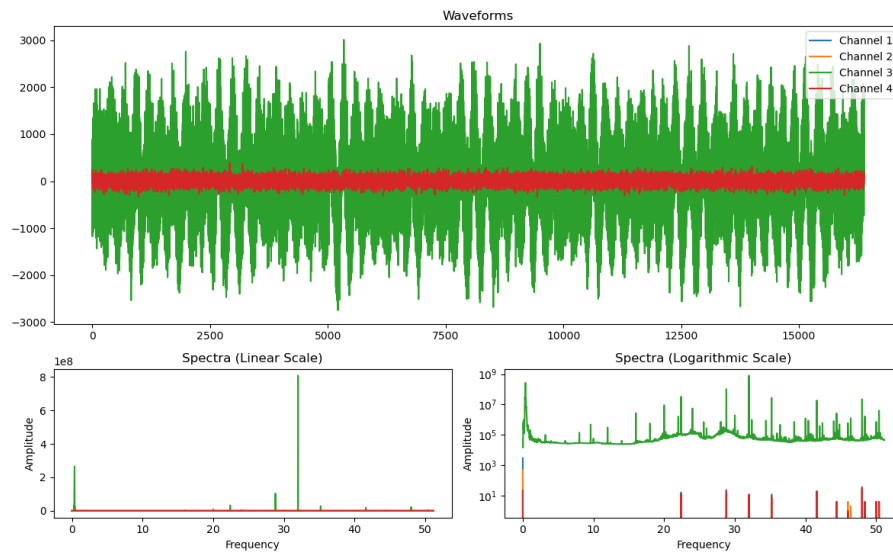
Gain H (30.0 30.0 30.0 30.0)MHz (0.0 0.0 0.0 0.0)mVpp



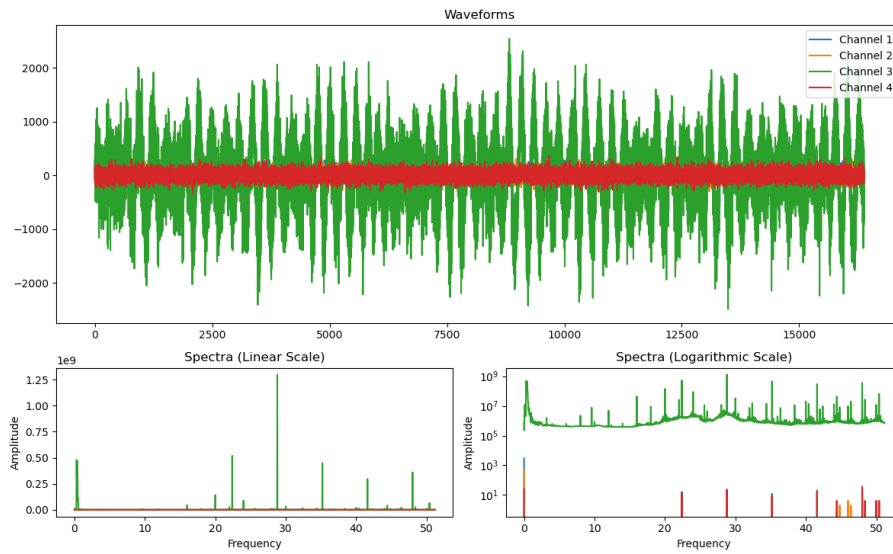
Gain H (32.0 32.0 32.0 32.0)MHz (0.0 0.0 80.0 0.0)mVpp



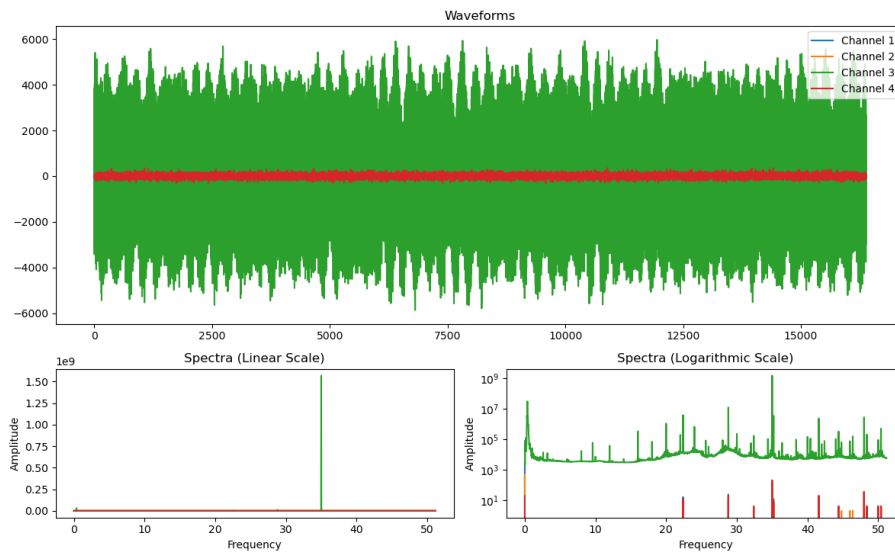
Gain H (32.0 32.0 32.0 32.0)MHz (0.0 0.0 20.0 0.0)mVpp



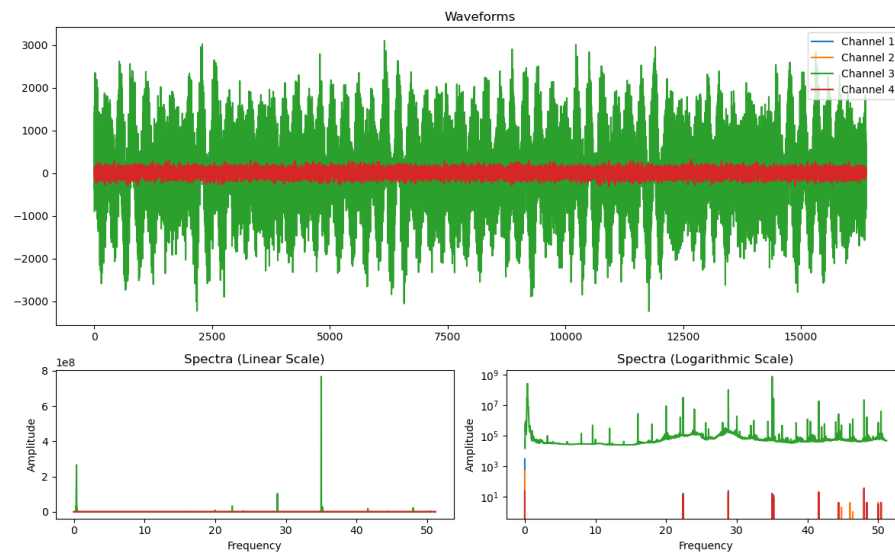
Gain H (32.0 32.0 32.0 32.0)MHz (0.0 0.0 0.0 0.0)mVpp



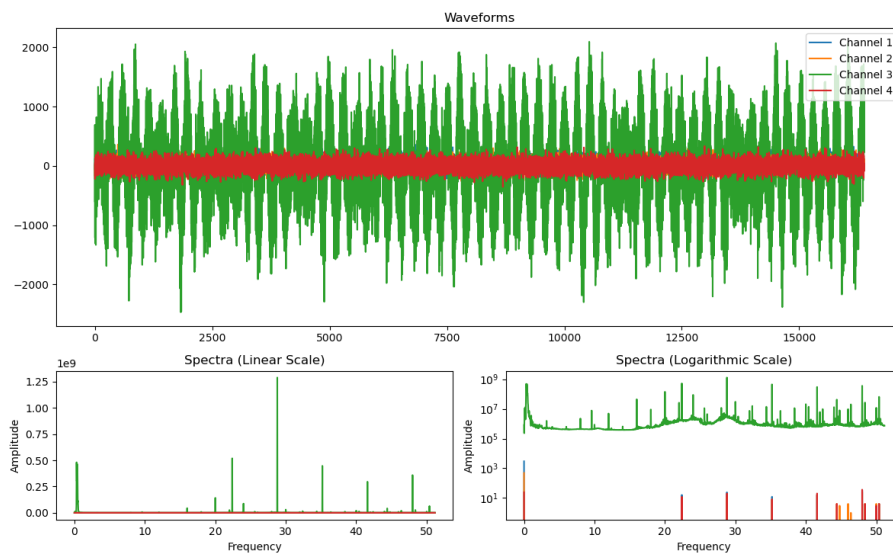
Gain H (35.0 35.0 35.0 35.0)MHz (0.0 0.0 80.0 0.0)mVpp



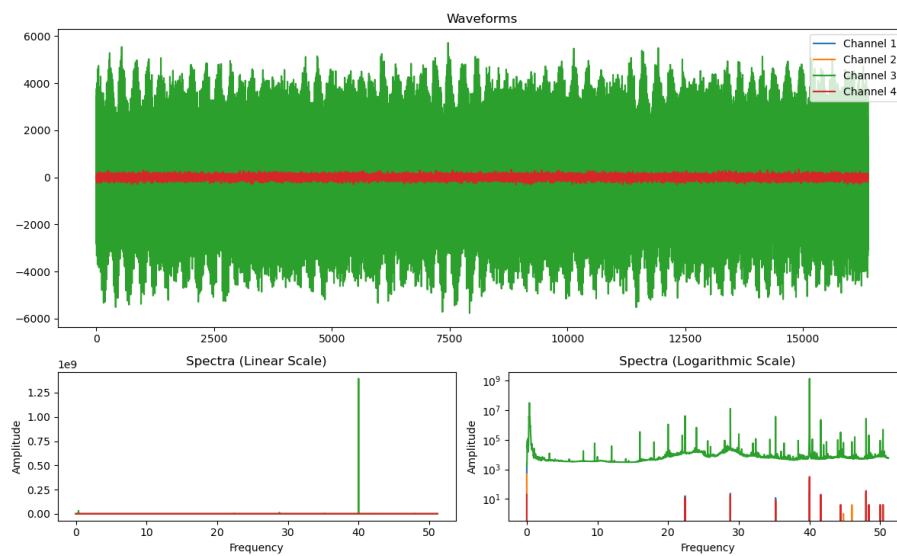
Gain H (35.0 35.0 35.0 35.0)MHz (0.0 0.0 20.0 0.0)mVpp



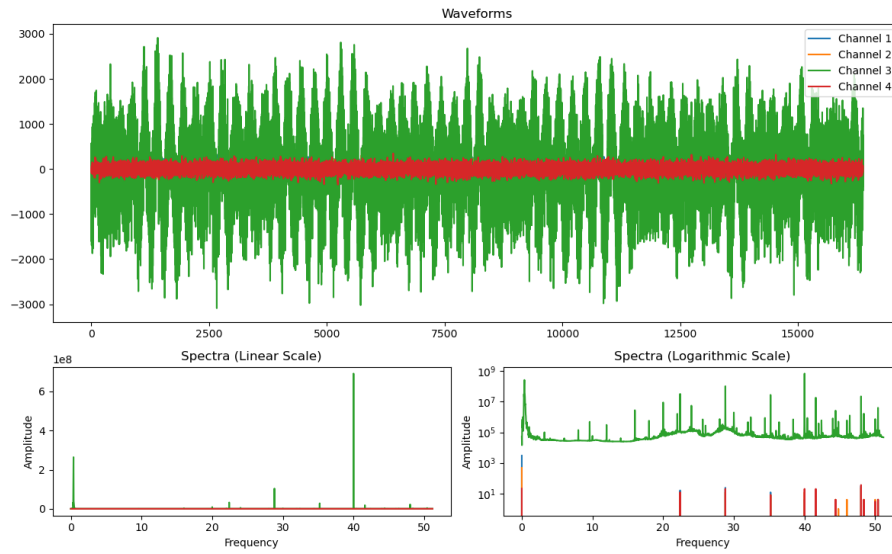
Gain H (35.0 35.0 35.0 35.0)MHz (0.0 0.0 0.0 0.0)mVpp



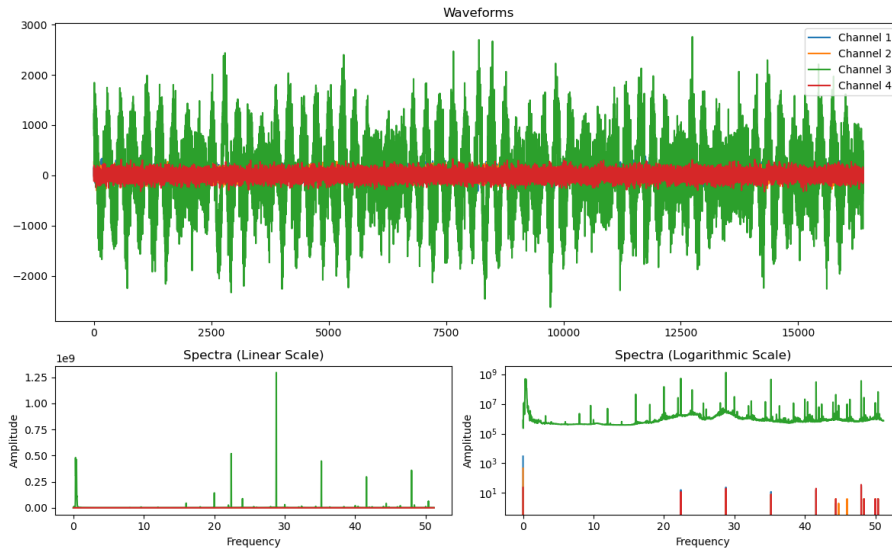
Gain H (40.0 40.0 40.0 40.0)MHz (0.0 0.0 80.0 0.0)mVpp



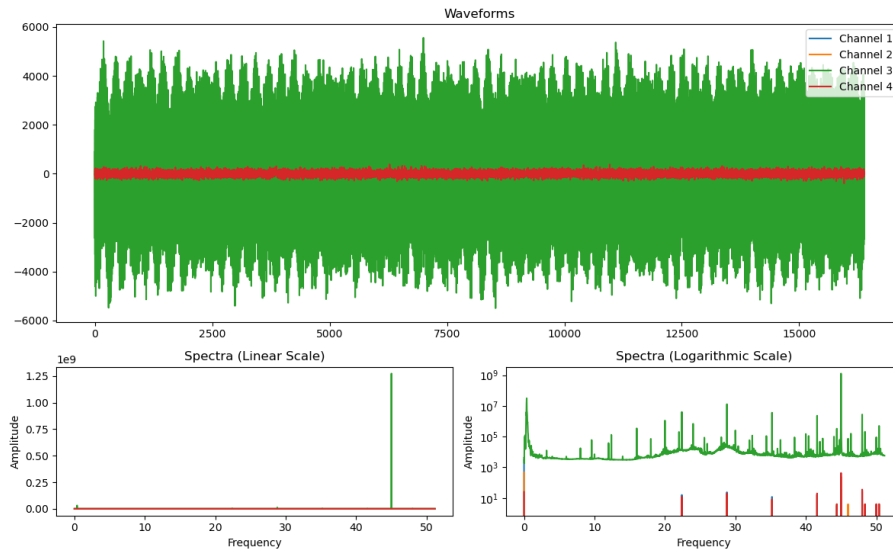
Gain H (40.0 40.0 40.0 40.0)MHz (0.0 0.0 20.0 0.0)mVpp



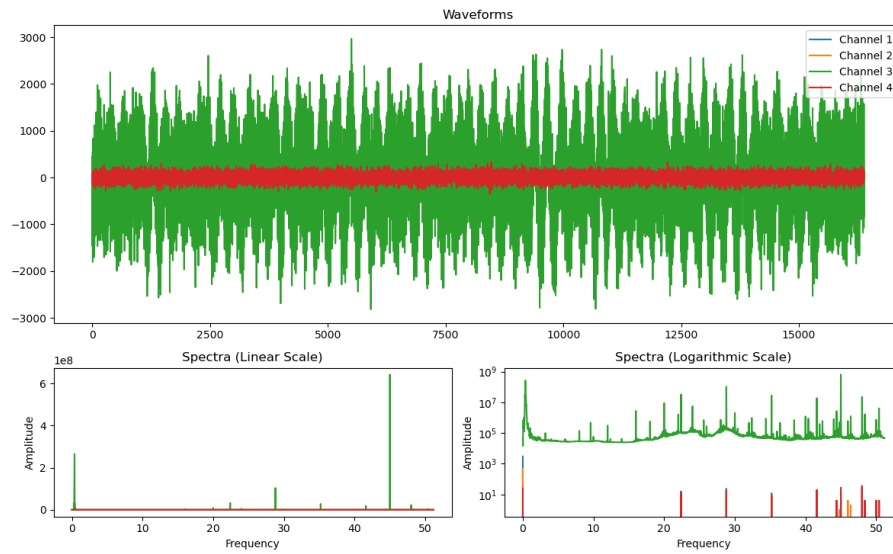
Gain H (40.0 40.0 40.0 40.0)MHz (0.0 0.0 0.0 0.0)mVpp



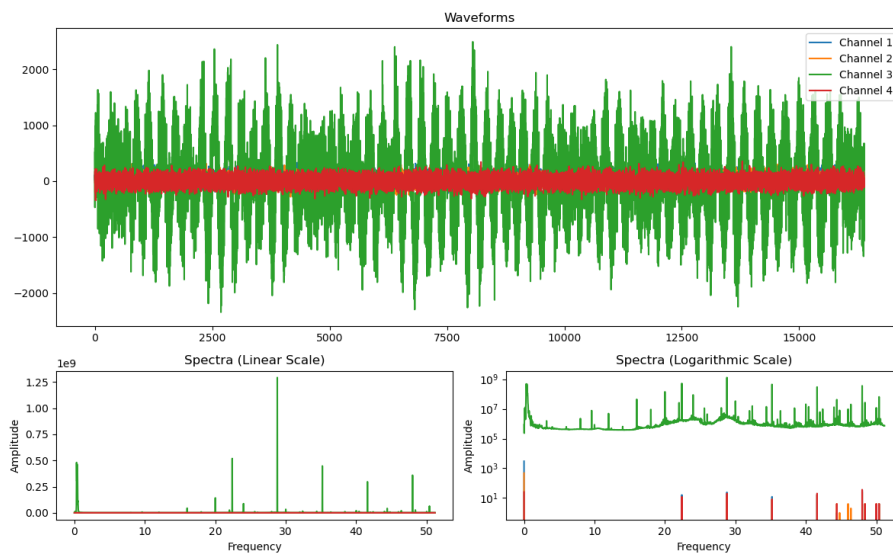
Gain H (45.0 45.0 45.0 45.0)MHz (0.0 0.0 80.0 0.0)mVpp



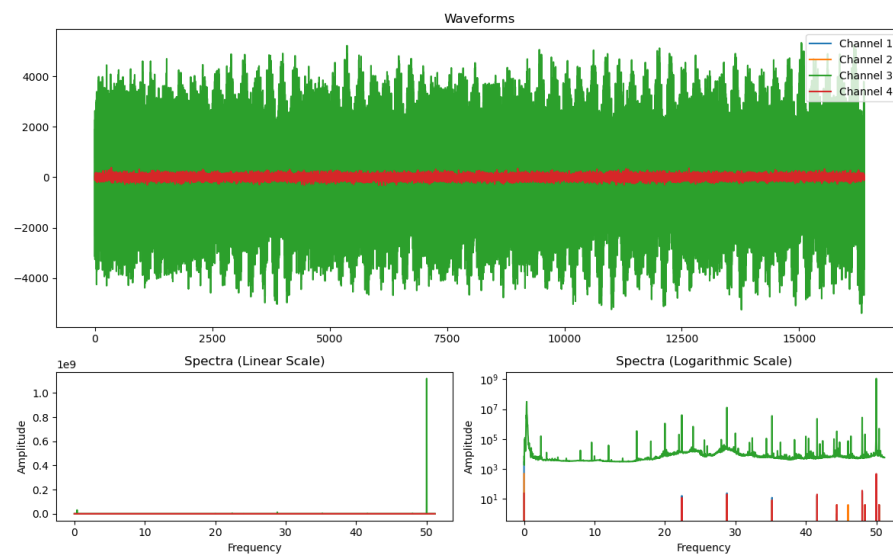
Gain H (45.0 45.0 45.0 45.0)MHz (0.0 0.0 20.0 0.0)mVpp



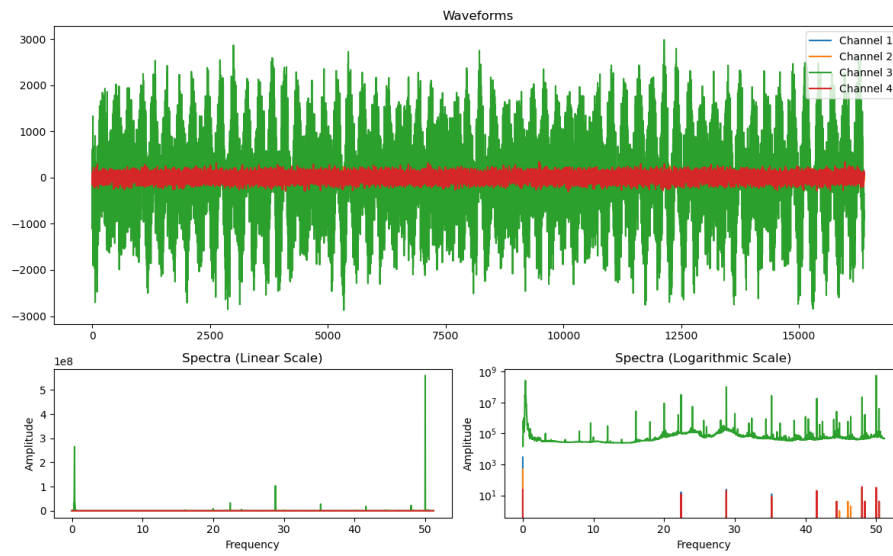
Gain H (45.0 45.0 45.0 45.0)MHz (0.0 0.0 0.0 0.0)mVpp



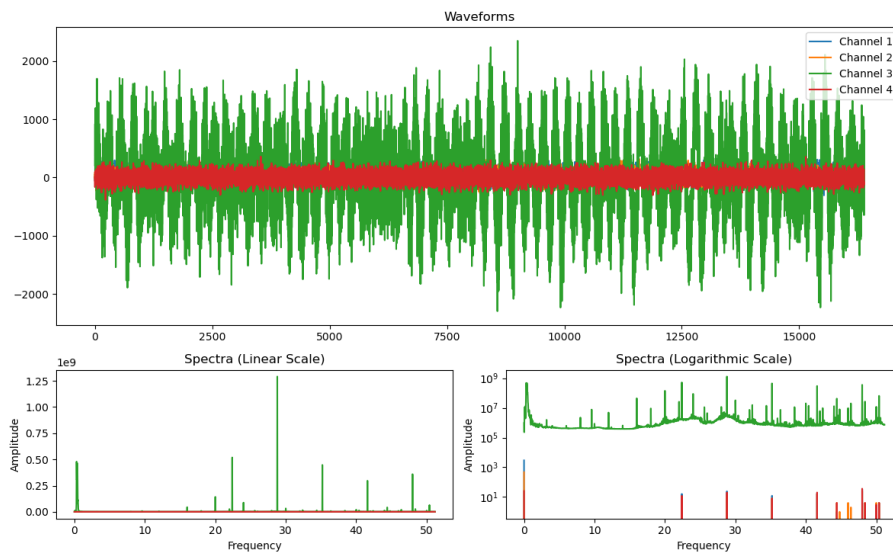
Gain H (50.0 50.0 50.0 50.0)MHz (0.0 0.0 80.0 0.0)mVpp



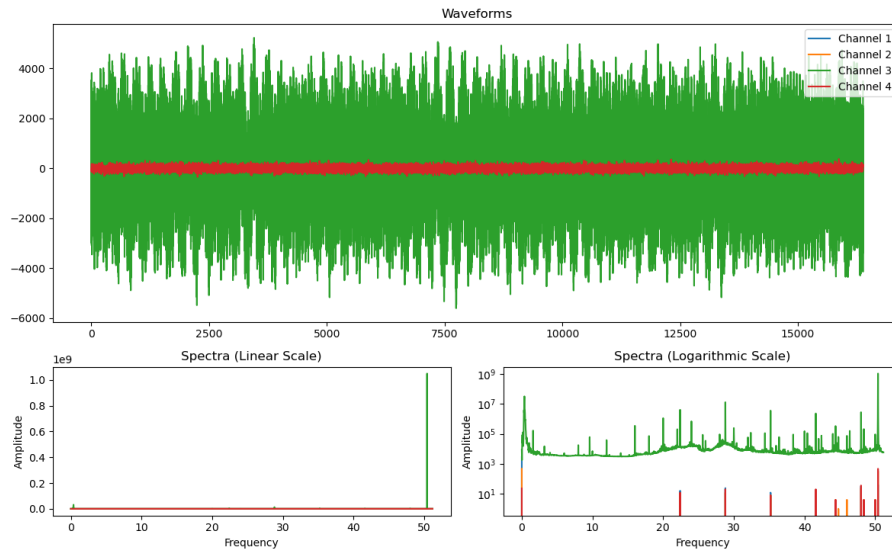
Gain H (50.0 50.0 50.0 50.0)MHz (0.0 0.0 20.0 0.0)mVpp



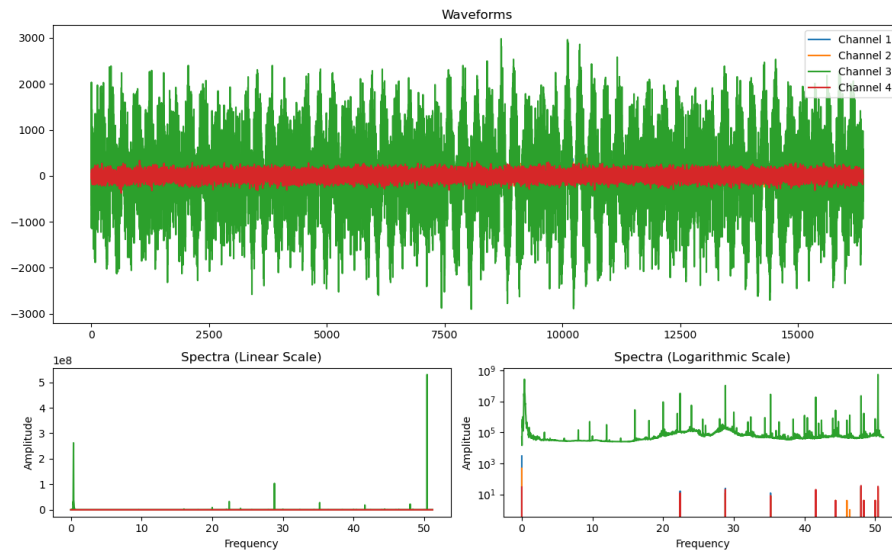
Gain H (50.0 50.0 50.0 50.0)MHz (0.0 0.0 0.0 0.0)mVpp



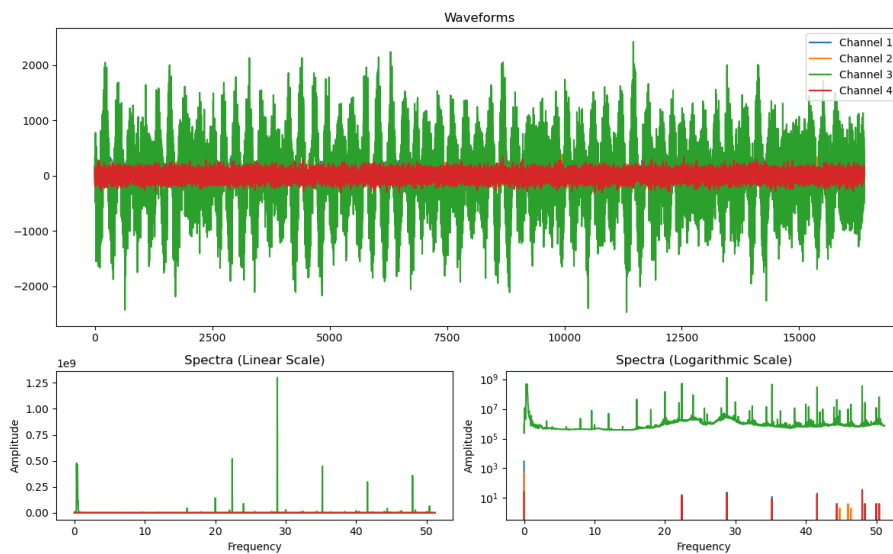
Gain H (52.0 52.0 52.0 52.0)MHz (0.0 0.0 80.0 0.0)mVpp



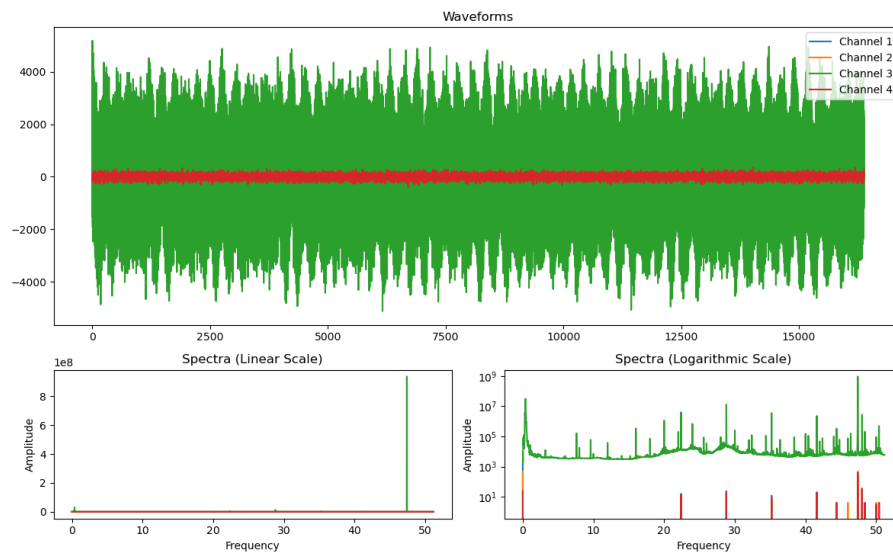
Gain H (52.0 52.0 52.0 52.0)MHz (0.0 0.0 20.0 0.0)mVpp



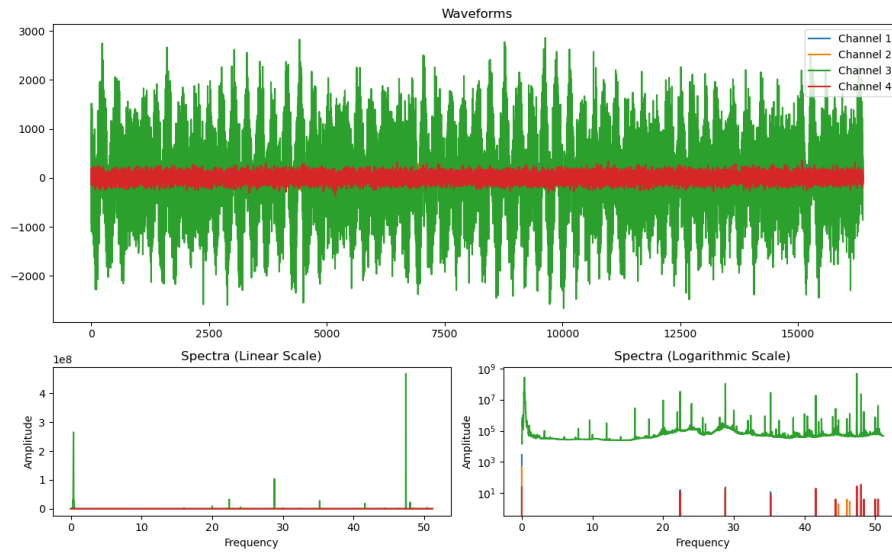
Gain H (52.0 52.0 52.0 52.0)MHz (0.0 0.0 0.0 0.0)mVpp



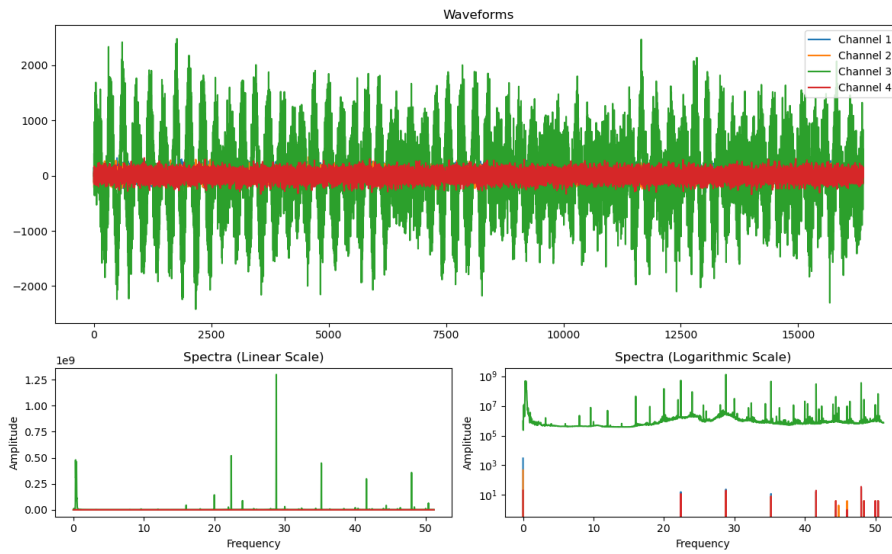
Gain H (55.0 55.0 55.0 55.0)MHz (0.0 0.0 80.0 0.0)mVpp



Gain H (55.0 55.0 55.0 55.0)MHz (0.0 0.0 20.0 0.0)mVpp



Gain H (55.0 55.0 55.0 55.0)MHz (0.0 0.0 0.0 0.0)mVpp



5 UART log

6 Commander log

```
*****
* Commander *
*****
```

```
Cleared session directory: session_cpt-short
DCB Emulator backend initializing
```

```
Attempting to open UART serial...
Found: n/a at /dev/ttyS1
Found: ttyS0 at /dev/ttyS0
Found: FlashPro5 - FlashPro5 at /dev/ttyUSB2
Found: CP2105 Dual USB to UART Bridge Controller - Standard Com Port at /dev/ttyUSB1
Found: CP2105 Dual USB to UART Bridge Controller - Enhanced Com Port at /dev/ttyUSB0
Using CP2105 Dual USB to UART Bridge Controller - Standard Com Port at port /dev/ttyUSB1.
Connecting to USB...
Initialized LuSEE_ETHERNET with version 1.12.
UDP IP is 192.168.121.1 and PC IP is 192.168.121.100.
Listening on ports 32004
Starting UART thread
```

```
Starting data collection threads.
[ 2s ]: Received AWG command: INIT
[ 2s ]: Sent CDI command 0x10 with argument 0x201 .
[ 2s ]: Waiting for 3.0s.
[ 2s ]: Stored AppID 0x209 len=32
[ 5s ]: Sent CDI command 0x10 with argument 0x502e .
[ 5s ]: Sent CDI command 0x10 with argument 0x560f .
[ 5s ]: Sent CDI command 0x10 with argument 0x5700 .
[ 5s ]: Sent CDI command 0x10 with argument 0x3000 .
[ 5s ]: Waiting for 0.2s.
[ 5s ]: Received AWG command: TONE 0 1.0 0
[ 5s ]: Sent CDI command 0x10 with argument 0x33f8 .
[ 5s ]: Received AWG command: TONE 1 1.0 0
[ 5s ]: Sent CDI command 0x10 with argument 0x33f9 .
[ 5s ]: Received AWG command: TONE 2 1.0 2000.0
[ 5s ]: Sent CDI command 0x10 with argument 0x33ba .
[ 5s ]: Received AWG command: TONE 3 1.0 0
[ 5s ]: Sent CDI command 0x10 with argument 0x33fb .
[ 5s ]: Waiting for 0.1s.
[ 5s ]: Sent CDI command 0x10 with argument 0x804 .
[ 5s ]: Waiting for 1.0s.
[ 5s ]: Stored AppID 0x2f0 len=32768
[ 5s ]: Stored AppID 0x2f1 len=32768
```

```

[ 6s ]: Stored AppID 0x2f2 len=32768
[ 6s ]: Sent CDI command 0x10 with argument 0x101 .
[ 6s ]: Waiting for 4.0s.
[ 6s ]: Stored AppID 0x2f3 len=32768
[ 9s ]: Stored AppID 0x20f len=248
[ 9s ]: Stored AppID 0x210 len=8200
[ 9s ]: Stored AppID 0x211 len=8200
[ 10s ]: Stored AppID 0x212 len=8200
[ 10s ]: Stored AppID 0x213 len=8200
[ 10s ]: Sent CDI command 0x10 with argument 0x1 .
[ 10s ]: Received AWG command: TONE 0 1.0 0
[ 10s ]: Sent CDI command 0x10 with argument 0x33f8 .
[ 10s ]: Received AWG command: TONE 1 1.0 0
[ 10s ]: Sent CDI command 0x10 with argument 0x33f9 .
[ 10s ]: Received AWG command: TONE 2 1.0 500.0
[ 10s ]: Sent CDI command 0x10 with argument 0x33a2 .
[ 10s ]: Received AWG command: TONE 3 1.0 0
[ 10s ]: Sent CDI command 0x10 with argument 0x33fb .
[ 10s ]: Waiting for 0.1s.
[ 10s ]: Sent CDI command 0x10 with argument 0x804 .
[ 10s ]: Waiting for 1.0s.
[ 10s ]: Stored AppID 0x2f0 len=32768
[ 11s ]: Stored AppID 0x2f1 len=32768
[ 11s ]: Stored AppID 0x2f2 len=32768
[ 11s ]: Sent CDI command 0x10 with argument 0x101 .
[ 11s ]: Waiting for 4.0s.
[ 11s ]: Stored AppID 0x2f3 len=32768
[ 12s ]: Stored AppID 0x20a len=16
[ 14s ]: Stored AppID 0x20f len=248
[ 14s ]: Stored AppID 0x210 len=8200
[ 15s ]: Stored AppID 0x211 len=8200
[ 15s ]: Stored AppID 0x212 len=8200
[ 15s ]: Stored AppID 0x213 len=8200
[ 15s ]: Sent CDI command 0x10 with argument 0x1 .
[ 15s ]: Received AWG command: TONE 0 1.0 0
[ 15s ]: Sent CDI command 0x10 with argument 0x33f8 .
[ 15s ]: Received AWG command: TONE 1 1.0 0
[ 15s ]: Sent CDI command 0x10 with argument 0x33f9 .
[ 15s ]: Received AWG command: TONE 2 1.0 0.0
[ 15s ]: Sent CDI command 0x10 with argument 0x3382 .
[ 15s ]: Received AWG command: TONE 3 1.0 0
[ 15s ]: Sent CDI command 0x10 with argument 0x33fb .
[ 15s ]: Waiting for 0.1s.
[ 15s ]: Sent CDI command 0x10 with argument 0x804 .
[ 15s ]: Waiting for 1.0s.
[ 15s ]: Stored AppID 0x2f0 len=32768

```

```

[ 16s ]: Stored AppID 0x2f1 len=32768
[ 16s ]: Stored AppID 0x2f2 len=32768
[ 16s ]: Sent CDI command 0x10 with argument 0x101 .
[ 16s ]: Waiting for 4.0s.
[ 16s ]: Stored AppID 0x2f3 len=32768
[ 19s ]: Stored AppID 0x20f len=248
[ 20s ]: Stored AppID 0x210 len=8200
[ 20s ]: Stored AppID 0x211 len=8200
[ 20s ]: Stored AppID 0x212 len=8200
[ 20s ]: Stored AppID 0x213 len=8200
[ 20s ]: Sent CDI command 0x10 with argument 0x1 .
[ 20s ]: Received AWG command: TONE  0  2.0 0
[ 20s ]: Sent CDI command 0x10 with argument 0x33f8 .
[ 20s ]: Received AWG command: TONE  1  2.0 0
[ 20s ]: Sent CDI command 0x10 with argument 0x33f9 .
[ 20s ]: Received AWG command: TONE  2  2.0 2000.0
[ 20s ]: Sent CDI command 0x10 with argument 0x33ba .
[ 20s ]: Received AWG command: TONE  3  2.0 0
[ 20s ]: Sent CDI command 0x10 with argument 0x33fb .
[ 20s ]: Waiting for 0.1s.
[ 20s ]: Sent CDI command 0x10 with argument 0x804 .
[ 20s ]: Waiting for 1.0s.
[ 21s ]: Stored AppID 0x2f0 len=32768
[ 21s ]: Stored AppID 0x2f1 len=32768
[ 21s ]: Stored AppID 0x2f2 len=32768
[ 21s ]: Sent CDI command 0x10 with argument 0x101 .
[ 21s ]: Waiting for 4.0s.
[ 22s ]: Stored AppID 0x2f3 len=32768
[ 22s ]: Stored AppID 0x20a len=16
[ 25s ]: Stored AppID 0x20f len=248
[ 25s ]: Stored AppID 0x210 len=8200
[ 25s ]: Stored AppID 0x211 len=8200
[ 25s ]: Stored AppID 0x212 len=8200
[ 25s ]: Stored AppID 0x213 len=8200
[ 25s ]: Sent CDI command 0x10 with argument 0x1 .
[ 25s ]: Received AWG command: TONE  0  2.0 0
[ 25s ]: Sent CDI command 0x10 with argument 0x33f8 .
[ 25s ]: Received AWG command: TONE  1  2.0 0
[ 25s ]: Sent CDI command 0x10 with argument 0x33f9 .
[ 25s ]: Received AWG command: TONE  2  2.0 500.0
[ 25s ]: Sent CDI command 0x10 with argument 0x33a2 .
[ 25s ]: Received AWG command: TONE  3  2.0 0
[ 25s ]: Sent CDI command 0x10 with argument 0x33fb .
[ 25s ]: Waiting for 0.1s.
[ 26s ]: Sent CDI command 0x10 with argument 0x804 .
[ 26s ]: Waiting for 1.0s.

```

```

[ 26s ]: Stored AppID 0x2f0 len=32768
[ 26s ]: Stored AppID 0x2f1 len=32768
[ 26s ]: Stored AppID 0x2f2 len=32768
[ 27s ]: Sent CDI command 0x10 with argument 0x101 .
[ 27s ]: Waiting for 4.0s.
[ 27s ]: Stored AppID 0x2f3 len=32768
[ 30s ]: Stored AppID 0x20f len=248
[ 30s ]: Stored AppID 0x210 len=8200
[ 30s ]: Stored AppID 0x211 len=8200
[ 30s ]: Stored AppID 0x212 len=8200
[ 30s ]: Stored AppID 0x213 len=8200
[ 31s ]: Sent CDI command 0x10 with argument 0x1 .
[ 31s ]: Received AWG command: TONE  0  2.0 0
[ 31s ]: Sent CDI command 0x10 with argument 0x33f8 .
[ 31s ]: Received AWG command: TONE  1  2.0 0
[ 31s ]: Sent CDI command 0x10 with argument 0x33f9 .
[ 31s ]: Received AWG command: TONE  2  2.0 0.0
[ 31s ]: Sent CDI command 0x10 with argument 0x3382 .
[ 31s ]: Received AWG command: TONE  3  2.0 0
[ 31s ]: Sent CDI command 0x10 with argument 0x33fb .
[ 31s ]: Waiting for 0.1s.
[ 31s ]: Sent CDI command 0x10 with argument 0x804 .
[ 31s ]: Waiting for 1.0s.
[ 31s ]: Stored AppID 0x2f0 len=32768
[ 31s ]: Stored AppID 0x2f1 len=32768
[ 32s ]: Stored AppID 0x2f2 len=32768
[ 32s ]: Sent CDI command 0x10 with argument 0x101 .
[ 32s ]: Waiting for 4.0s.
[ 32s ]: Stored AppID 0x2f3 len=32768
[ 32s ]: Stored AppID 0x20a len=16
[ 35s ]: Stored AppID 0x20f len=248
[ 35s ]: Stored AppID 0x210 len=8200
[ 35s ]: Stored AppID 0x211 len=8200
[ 35s ]: Stored AppID 0x212 len=8200
[ 36s ]: Stored AppID 0x213 len=8200
[ 36s ]: Sent CDI command 0x10 with argument 0x1 .
[ 36s ]: Received AWG command: TONE  0  5.0 0
[ 36s ]: Sent CDI command 0x10 with argument 0x33f8 .
[ 36s ]: Received AWG command: TONE  1  5.0 0
[ 36s ]: Sent CDI command 0x10 with argument 0x33f9 .
[ 36s ]: Received AWG command: TONE  2  5.0 2000.0
[ 36s ]: Sent CDI command 0x10 with argument 0x33ba .
[ 36s ]: Received AWG command: TONE  3  5.0 0
[ 36s ]: Sent CDI command 0x10 with argument 0x33fb .
[ 36s ]: Waiting for 0.1s.
[ 36s ]: Sent CDI command 0x10 with argument 0x804 .

```



```
[ 36s ]: Waiting for 1.0s.
[ 36s ]: Stored AppID 0x2f0 len=32768
[ 36s ]: Stored AppID 0x2f1 len=32768
[ 37s ]: Stored AppID 0x2f2 len=32768
[ 37s ]: Sent CDI command 0x10 with argument 0x101 .
[ 37s ]: Waiting for 4.0s.
[ 37s ]: Stored AppID 0x2f3 len=32768
[ 40s ]: Stored AppID 0x20f len=248
[ 40s ]: Stored AppID 0x210 len=8200
[ 40s ]: Stored AppID 0x211 len=8200
[ 41s ]: Stored AppID 0x212 len=8200
[ 41s ]: Stored AppID 0x213 len=8200
[ 41s ]: Sent CDI command 0x10 with argument 0x1 .
[ 41s ]: Received AWG command: TONE  0  5.0 0
[ 41s ]: Sent CDI command 0x10 with argument 0x33f8 .
[ 41s ]: Received AWG command: TONE  1  5.0 0
[ 41s ]: Sent CDI command 0x10 with argument 0x33f9 .
[ 41s ]: Received AWG command: TONE  2  5.0 500.0
... truncated ...
```